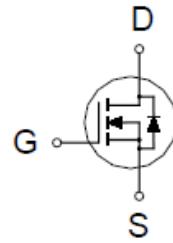
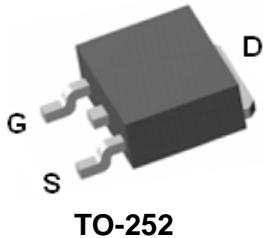


# P9515BD

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
150V	90mΩ @ $V_{GS} = 10V$	18A



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	150	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current $T_C = 25^\circ C$	$I_D$	18	A
		11	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	37	A
Avalanche Current	$I_{AS}$	8	
Avalanche Energy	$E_{AS}$	32	mJ
Power Dissipation $T_C = 25^\circ C$	$P_D$	56	W
		22.7	
Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$	1.8	62.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$			

<sup>1</sup>Pulse width limited by maximum junction temperature.

# P9515BD

## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

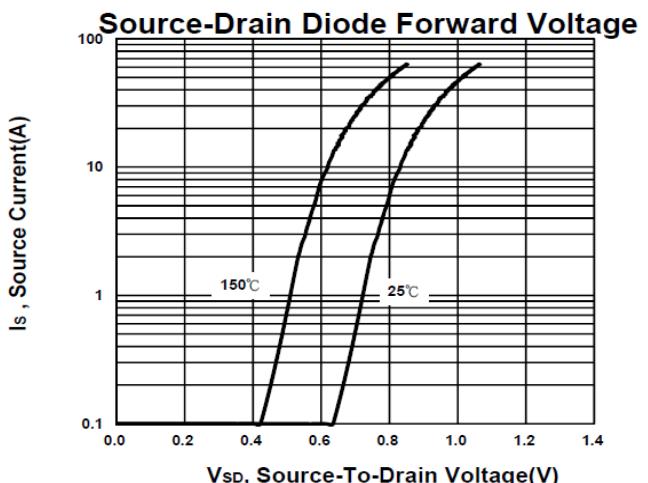
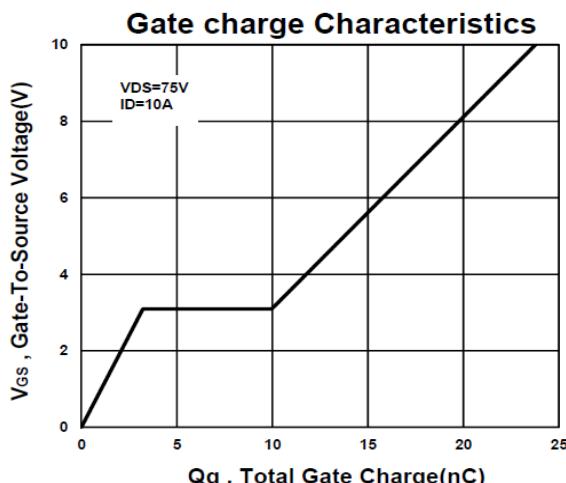
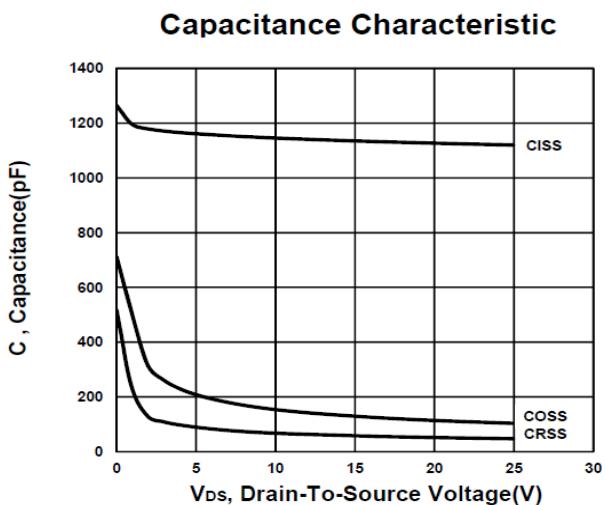
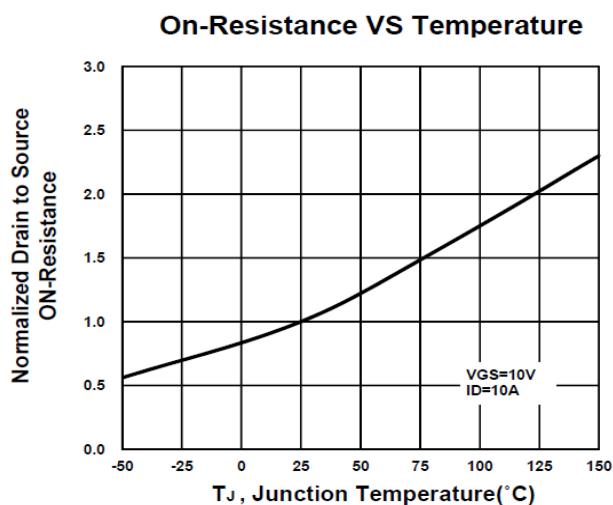
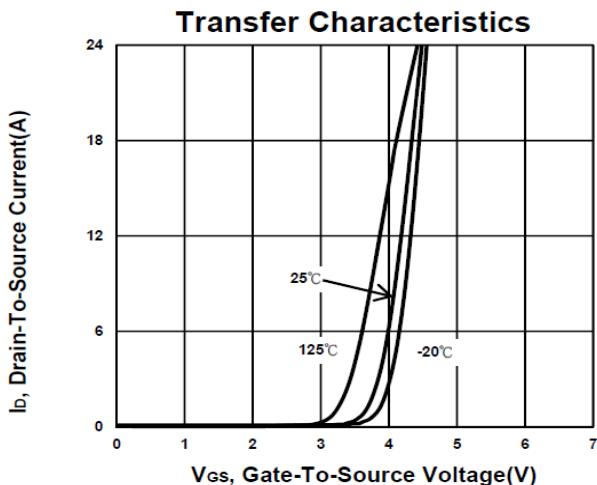
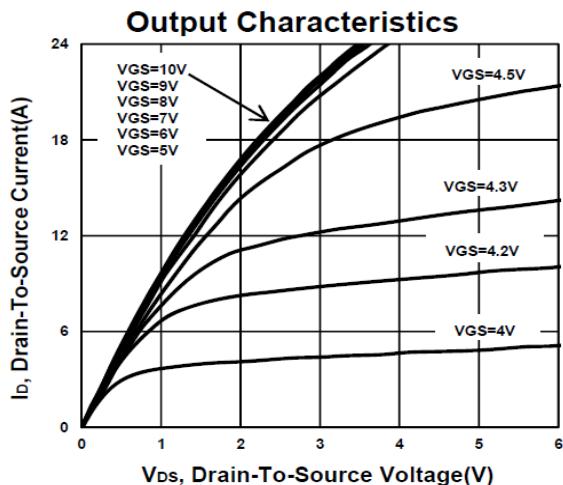
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	150			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.3	1.8	2.3	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 120\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
		$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 10\text{A}$		80	110	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 10\text{A}$		76	90	
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 5\text{V}, I_D = 10\text{A}$		35		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		1119		pF
Output Capacitance	$C_{\text{oss}}$			106		
Reverse Transfer Capacitance	$C_{\text{rss}}$			50		
Gate Resistance	$R_g$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		0.9		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g(V_{\text{GS}} = 10\text{V})$	$V_{\text{DS}} = 75\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 10\text{A}$		24		nC
	$Q_g(V_{\text{GS}} = 4.5\text{V})$			13		
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$			3.3		
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			7		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d(on)}}$	$V_{\text{DS}} = 75\text{V}, I_D \geq 10\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		21		nS
Rise Time <sup>2</sup>	$t_r$			34		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d(off)}}$			30		
Fall Time <sup>2</sup>	$t_f$			22		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>						
Continuous Current	$I_S$				15	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$	$I_F = 10\text{A}, V_{\text{GS}} = 0\text{V}$			1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 10\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		55		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			96		nC

<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

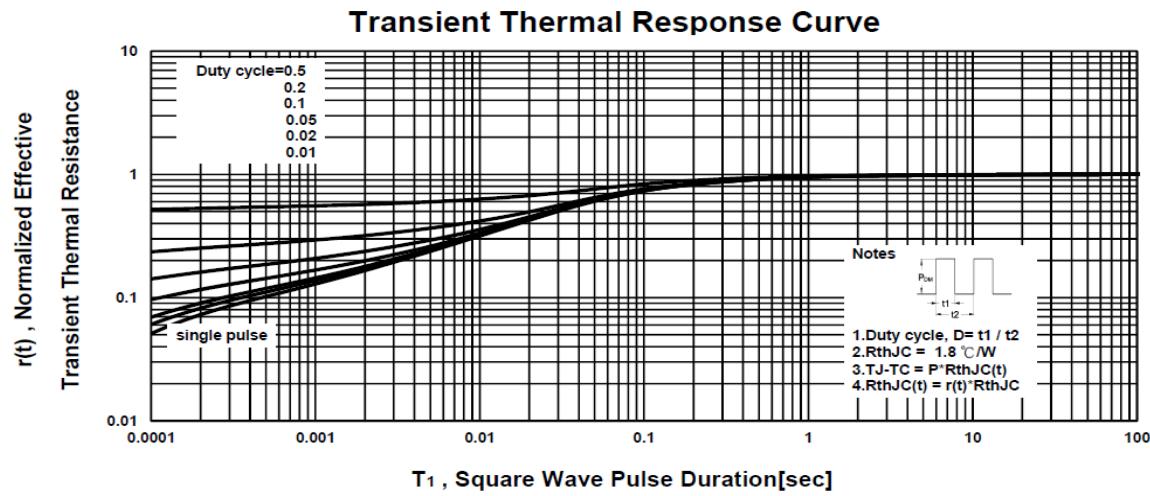
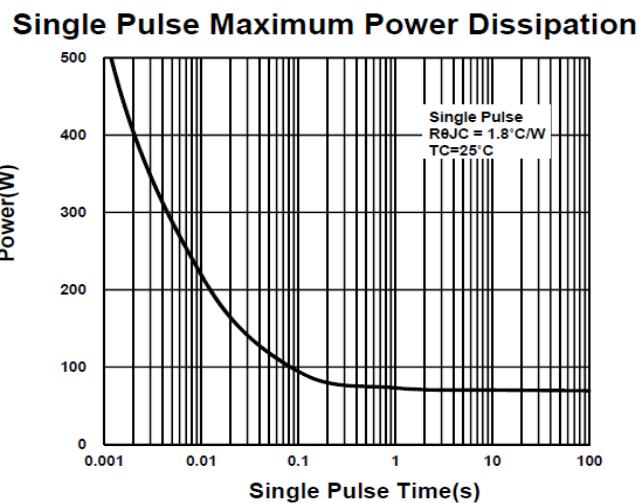
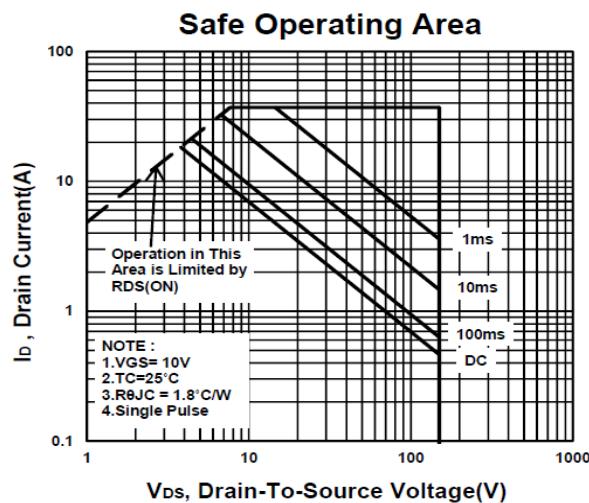
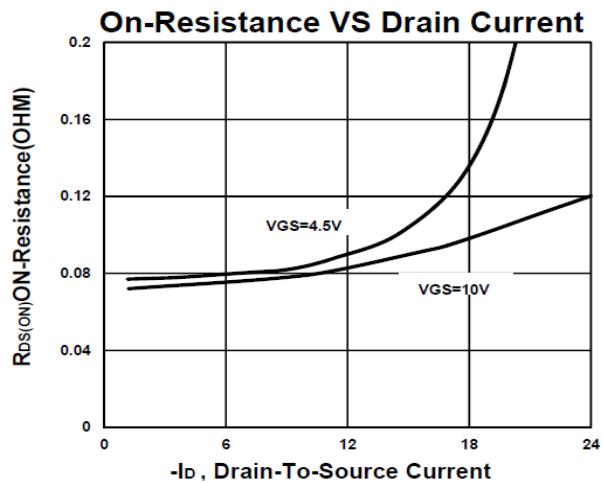
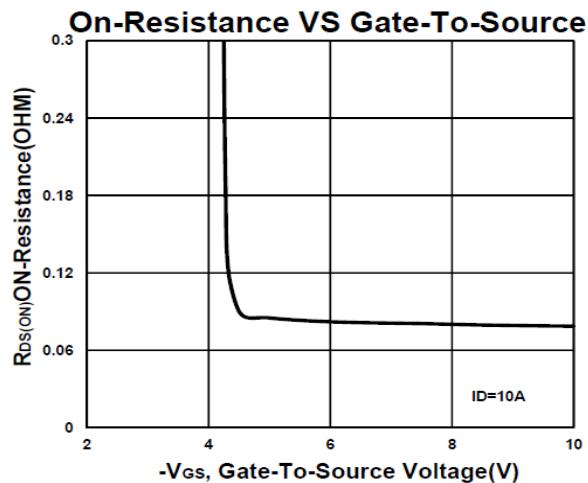
# P9515BD

## N-Channel Enhancement Mode MOSFET



# P9515BD

## N-Channel Enhancement Mode MOSFET



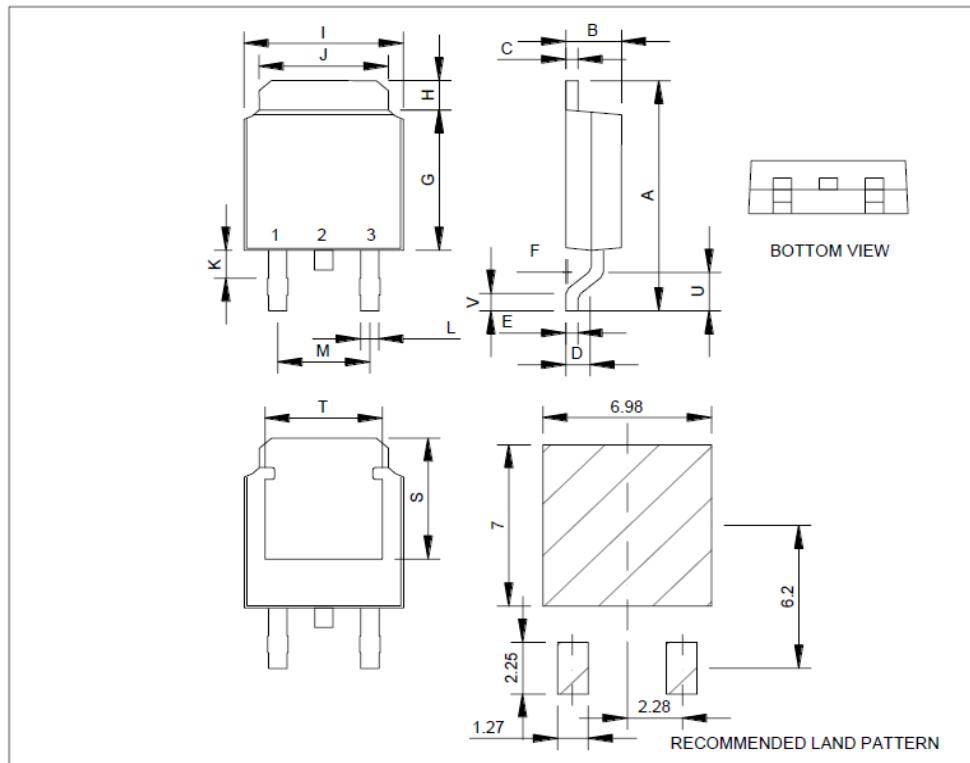
# P9515BD

## N-Channel Enhancement Mode MOSFET

### Package Dimension

#### TO-252 (DPAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	8.9	10	10.41	J	4.8		5.64
B	2.1	2.2	2.4	K	0.15		1.1
C	0.4	0.5	0.61	L	0.4	0.76	0.89
D	0.82	1.2	1.5	M	4.2	4.58	5
E	0.4	0.5	0.61	S	4.9	5.1	5.3
F	0		0.2	T	4.6	4.75	5.44
G	5.3	6.1	6.3	U	1.4		1.78
H	0.9		1.7	V	0.55	1.25	1.7
I	6.3	6.5	6.8				

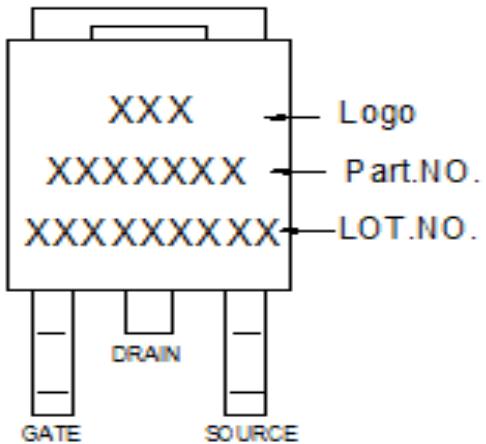


\*因为各家封装模具不同而外观略有差异，不影响电性及Layout。

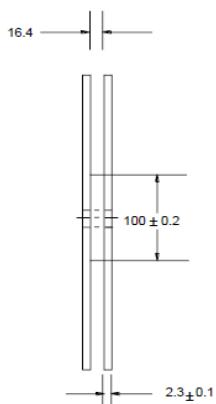
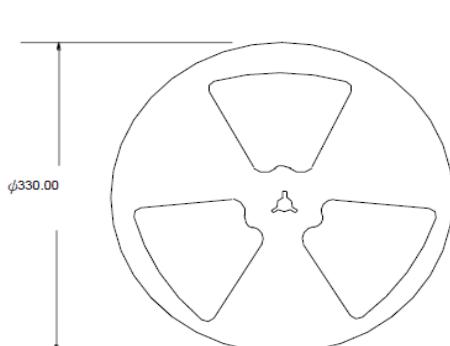
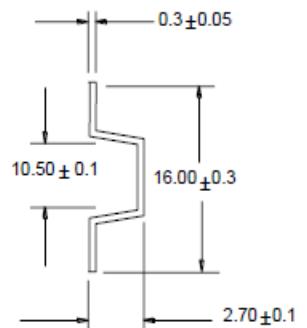
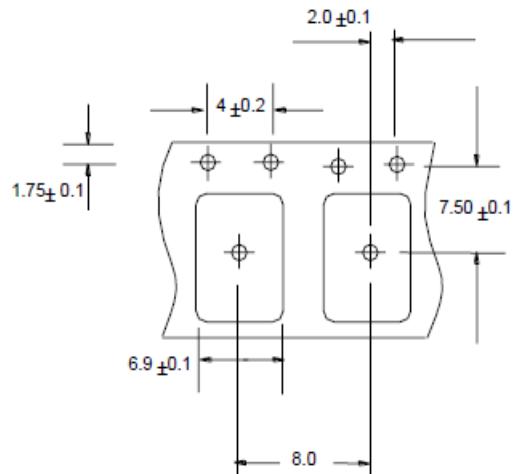
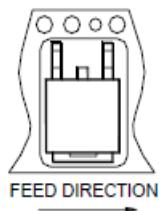
## P9515BD

### N-Channel Enhancement Mode MOSFET

#### A. Marking Information



#### B. Tape&Reel Information: 2500pcs/Reel

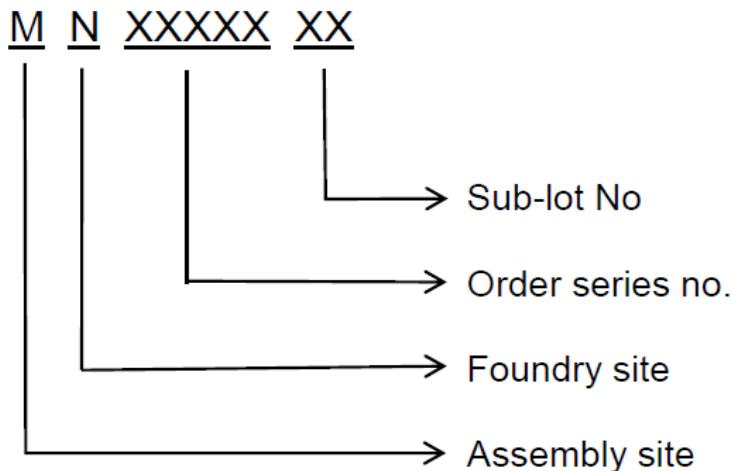


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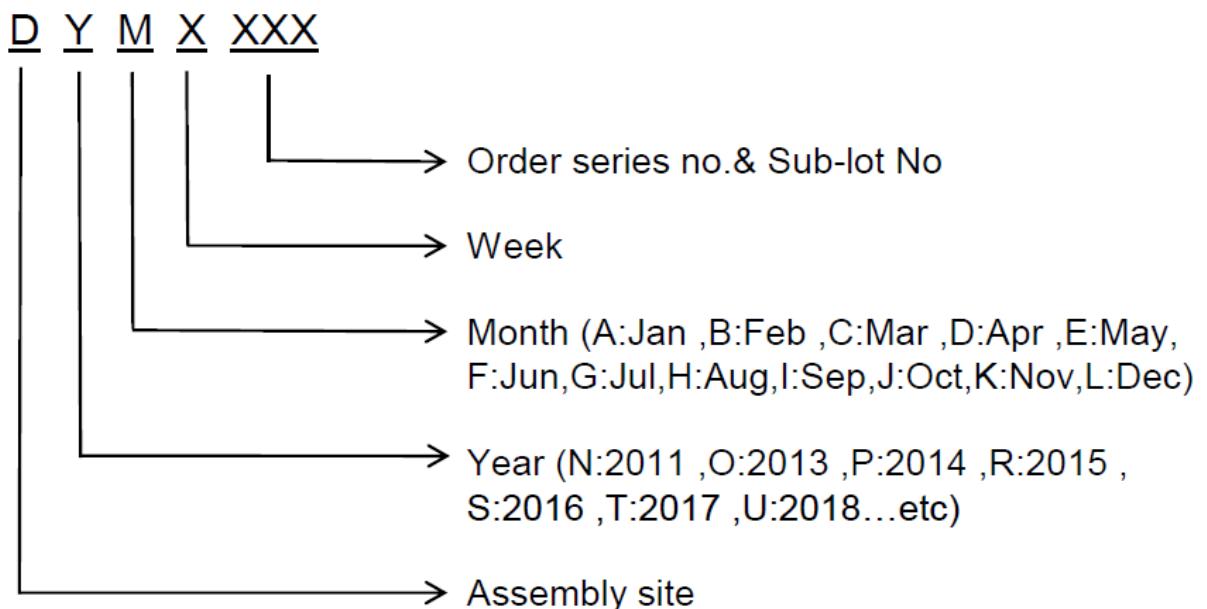
### N-Channel Enhancement Mode MOSFET

#### C. Lot No.&Date Code rule

##### 1. Lot No.



##### 2. Date Code



# P9515BD

## N-Channel Enhancement Mode MOSFET

### D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm			
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)			
3	U-NIKC	Height: 4 mm			
4	Package	Height: 2 mm			
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12			
6	Device	Height: 3 mm (Max: 16 Digit)			
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot			
8	D/C	Height: 3 mm (Max: 7 Digit)			
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed			
10	RoHS label	 long axis: 12 mm      minor axis: 6 mm bottom color: White Font color: Black      Font style: Arial			
11	Halogen Free label	 Diameter: 10 mm      bottom color: Green Font color: Black      Font style: Arial			
12	Scan information	Device / Lot / D/C / QTY , Insert “ / “ between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least			