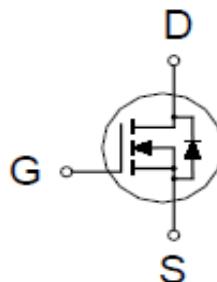
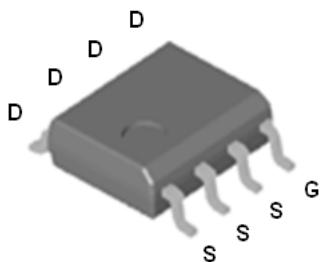


PA010BV

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	100m Ω @ $V_{GS} = 10V$	3A



SOP-8

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current $T_A = 25^\circ C$	I_D	3	A
$T_A = 70^\circ C$	I_D	2.2	
Pulsed Drain Current ¹	I_{DM}	15	A
Avalanche Current	I_{AS}	6	
Avalanche Energy	E_{AS}	18	mJ
Power Dissipation $T_A = 25^\circ C$	P_D	1.8	W
$T_A = 70^\circ C$	P_D	1.2	
Junction & Storage Temperature Range	T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		69	°C / W
Junction-to-Case	$R_{\theta JC}$		25	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ C$.

PA010BV N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

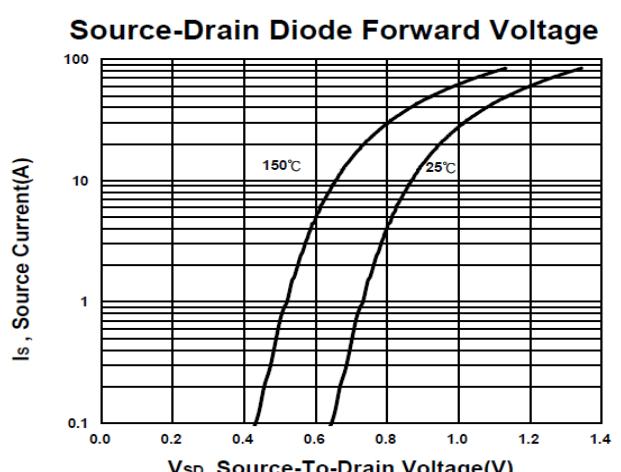
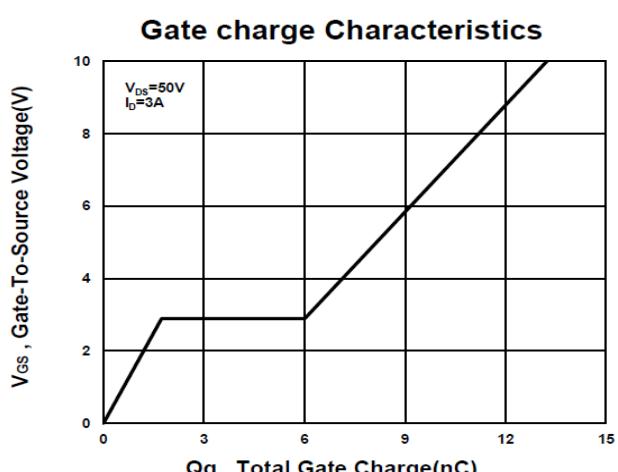
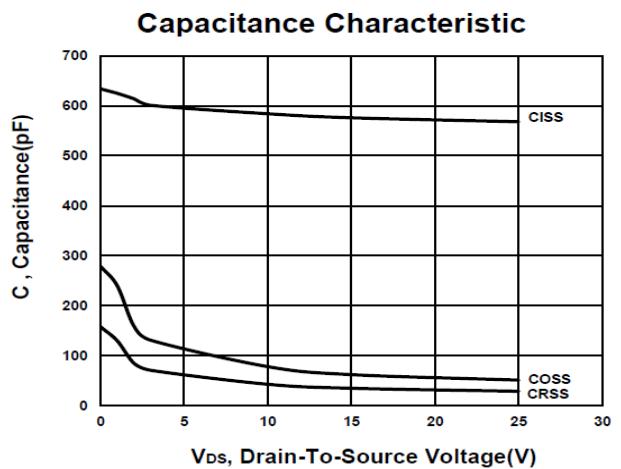
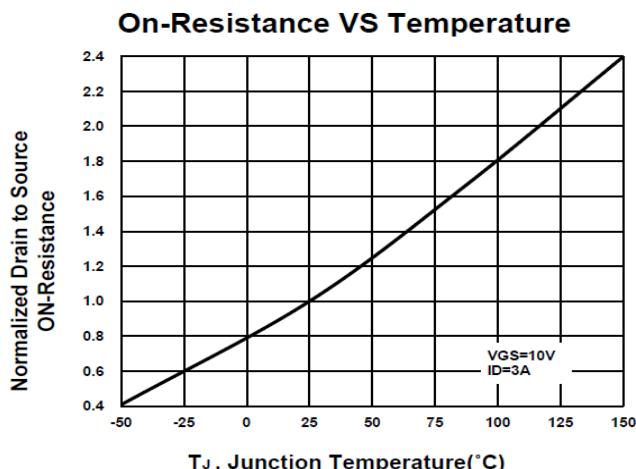
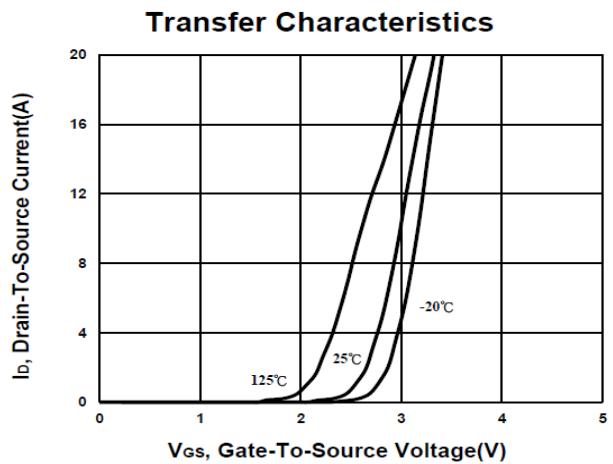
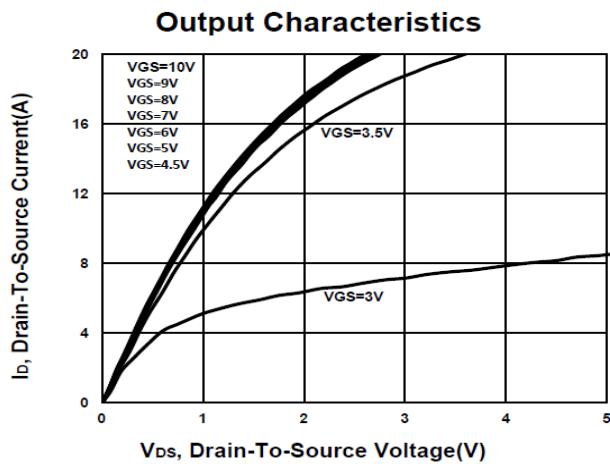
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 80\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 80\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 3\text{A}$		84	120	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 3\text{A}$		79	100	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 3\text{A}$		18		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1\text{MHz}$		570		pF
Output Capacitance	C_{oss}			50		
Reverse Transfer Capacitance	C_{rss}			30		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		1.3		Ω
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 3\text{A}$		13.6		nC
Gate-Source Charge ²	Q_{gs}			2.1		
Gate-Drain Charge ²	Q_{gd}			4.8		
Turn-On Delay Time ²	$t_{\text{d(on)}}$	$V_{\text{DS}} = 50\text{V}, I_D \geq 3\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		15		nS
Rise Time ²	t_r			5		
Turn-Off Delay Time ²	$t_{\text{d(off)}}$			36		
Fall Time ²	t_f			11		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S				1.3	A
Forward Voltage ¹	V_{SD}	$I_F = 3\text{A}, V_{\text{GS}} = 0\text{V}$			1.4	V
Diode Reverse Recovery Time	t_{rr}	$I_F = 3\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		22		nS
Diode Reverse Recovery Charge	Q_{rr}			15		nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

PA010BV

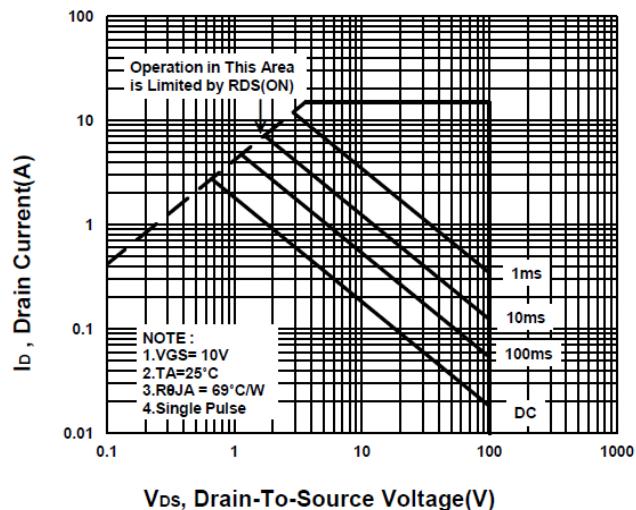
N-Channel Enhancement Mode MOSFET



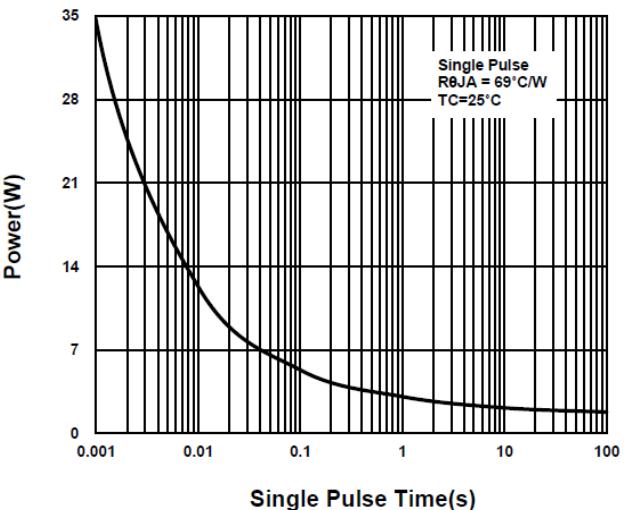
PA010BV

N-Channel Enhancement Mode MOSFET

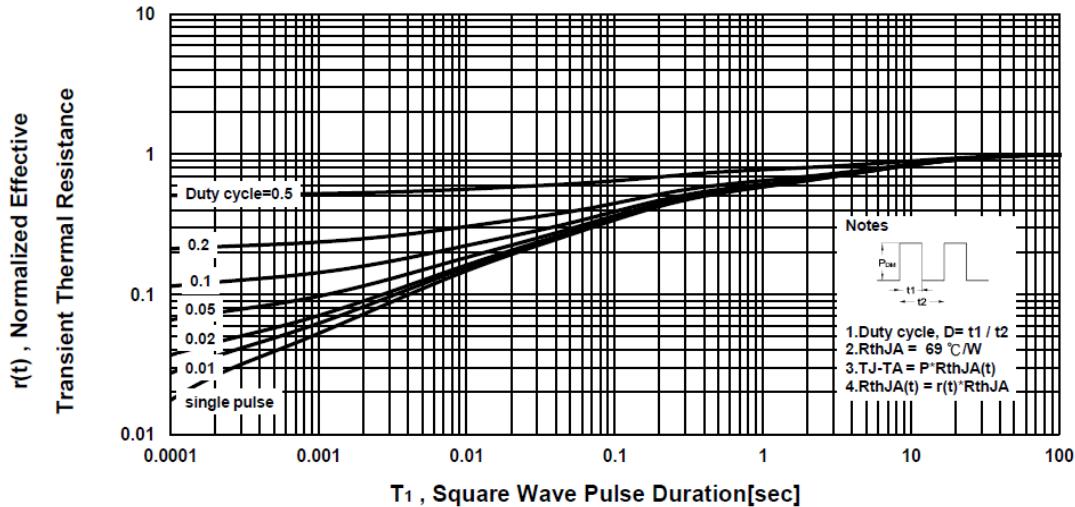
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



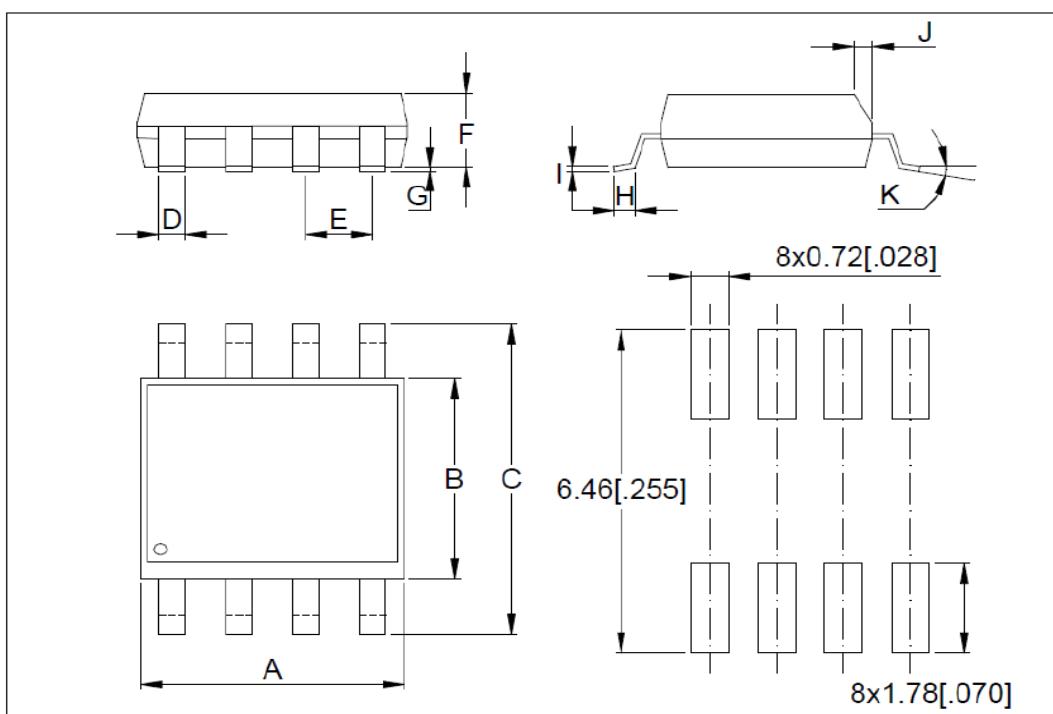
PA010BV

N-Channel Enhancement Mode MOSFET

Package Dimension

SOP-8 MECHANICAL DATA

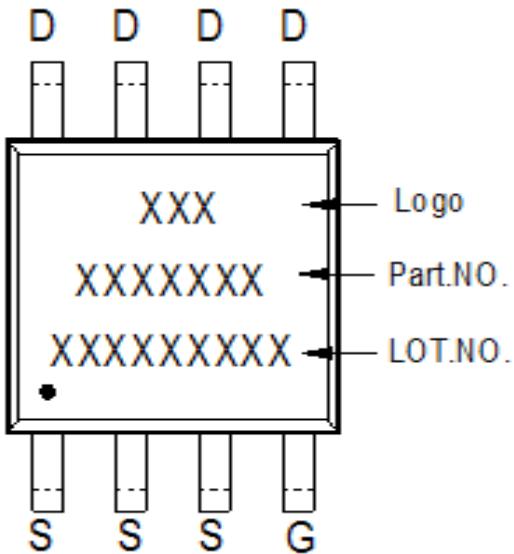
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				



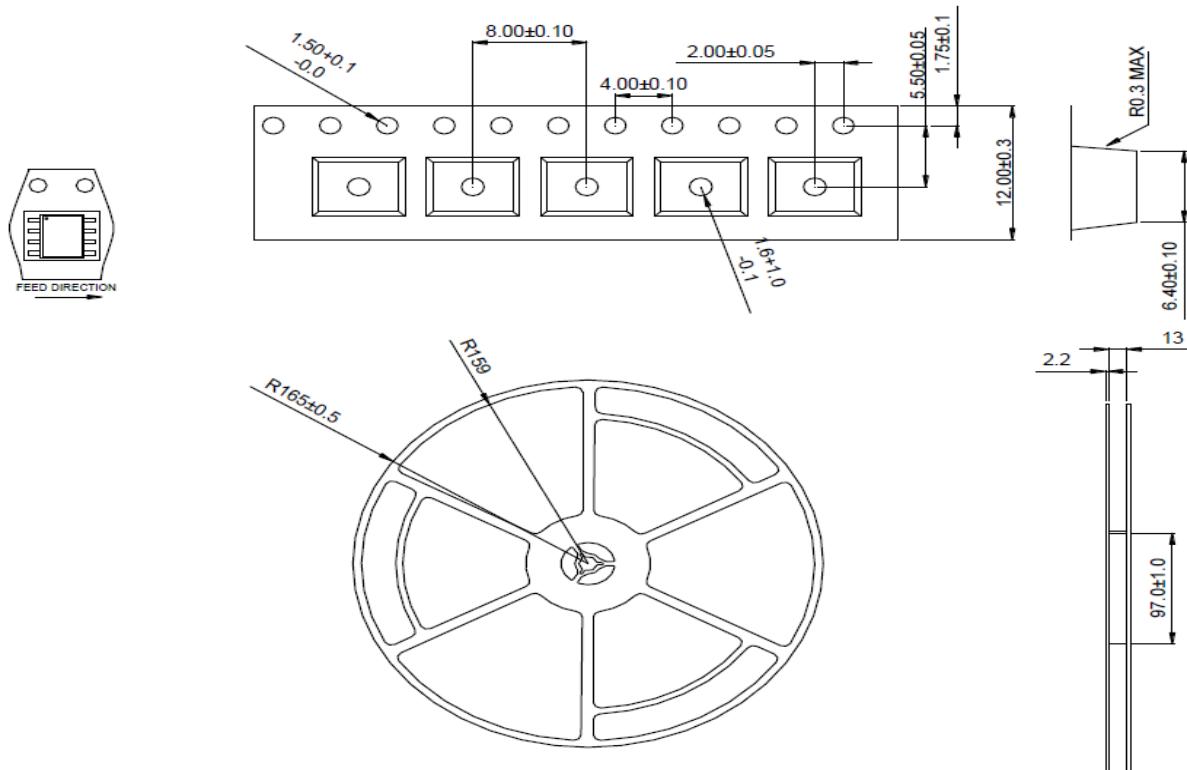
PA010BV

N-Channel Enhancement Mode MOSFET

A. Marking Information



B. Tape&Reel Information: 2500pcs/Reel

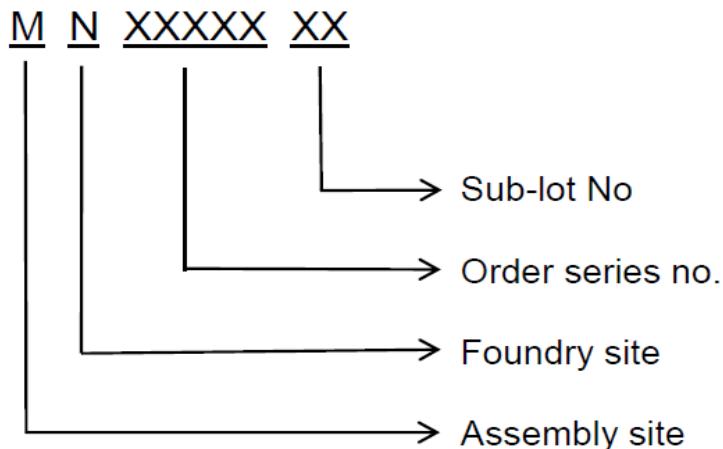


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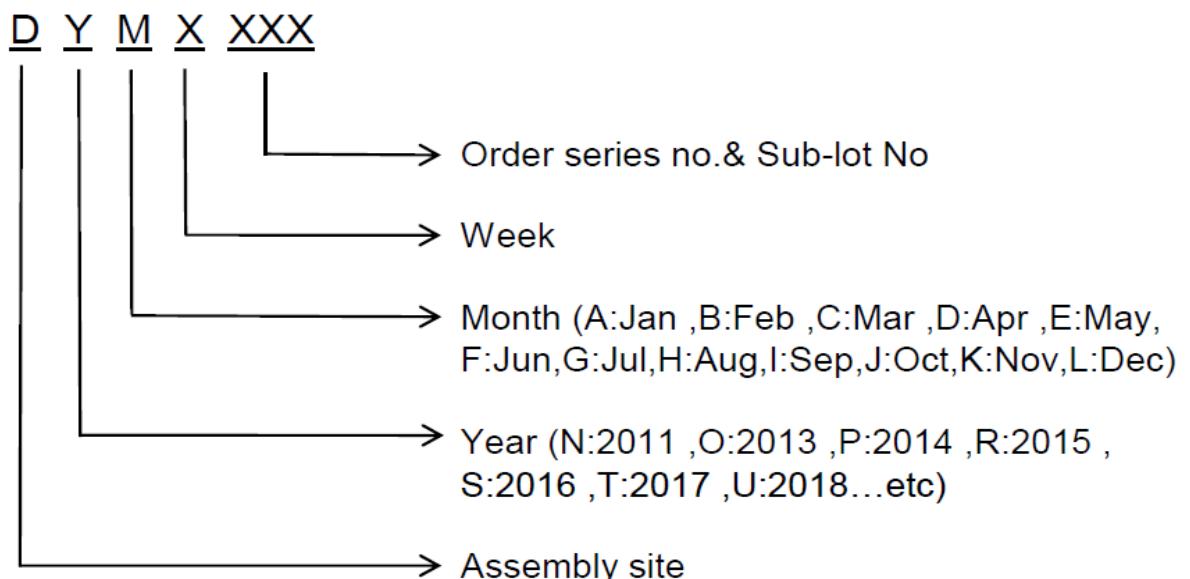
N-Channel Enhancement Mode MOSFET

C. Lot No.&Date Code rule

1. Lot No.



2. Date Code



PA010BV

N-Channel Enhancement Mode MOSFET

D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm		
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)		
3	U-NIKC	Height: 4 mm		
4	Package	Height: 2 mm		
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12		
6	Device	Height: 3 mm (Max: 16 Digit)		
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot		
8	D/C	Height: 3 mm (Max: 7 Digit)		
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed		
10	RoHS label	RoHS long axis: 12 mm minor axis: 6 mm bottom color: White Font color: Black Font style: Arial		
11	Halogen Free label	G Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial		
12	Scan information	Device / Lot / D/C / QTY , Insert “ / ” between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least		