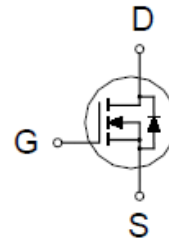
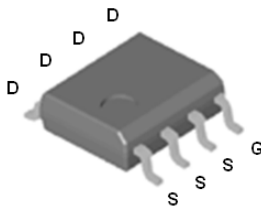


PA110BV

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	110m Ω @ $V_{GS} = 10V$	3.2A



SOP-8

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	$T_A = 25\text{ }^\circ\text{C}$	3.2
		$T_A = 70\text{ }^\circ\text{C}$	2.5
Pulsed Drain Current ¹	I_{DM}	10	A
Avalanche Current	I_{AS}	25	
Avalanche Energy	E_{AS}	31	mJ
Power Dissipation	P_D	$T_A = 25\text{ }^\circ\text{C}$	2.5
		$T_A = 70\text{ }^\circ\text{C}$	1.6
Junction & Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		50	$^\circ\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

PA110BV N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

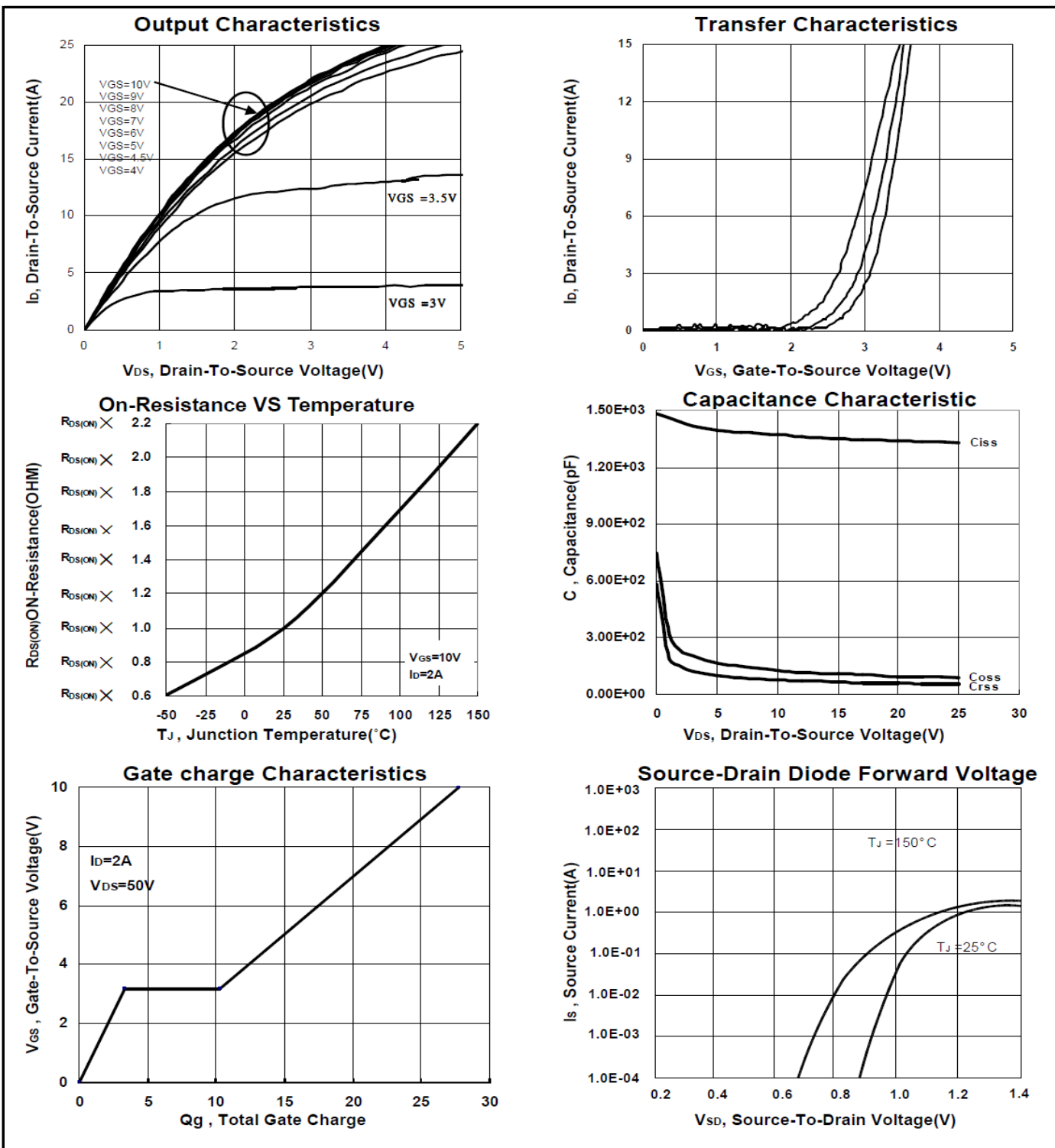
PARAMETER	SYMBOL	TEST CONDITIONS	LIMIT			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	2	3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 80V, V _{GS} = 0V			1	μA
		V _{DS} = 80V, V _{GS} = 0V, T _J = 55 °C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 2A		99	120	mΩ
		V _{GS} = 10V, I _D = 2A		93	110	
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 2A		20		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 50V, f = 1MHz		1350		pF
Output Capacitance	C _{oss}			90		
Reverse Transfer	C _{rss}			55		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.25		Ω
Total Gate Charge ²	Q _g	V _{GS} = 10V V _{GS} = 4.5V V _{DS} = 0.5V _{(BR)DSS} , V _{GS} = 10V, I _D = 2A		28		nC
				15		
				3.5		
Gate-Source Charge ²	Q _{gs}			7		
Gate-Drain Charge ²	Q _{gd}					
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 50V, R _L = 25Ω I _D ≅ 2A, V _{GS} = 10V, R _{GEN} = 6Ω		14		nS
Rise Time ²	t _r			14		
Turn-Off Delay Time ²	t _{d(off)}			15		
Fall Time ²	t _f			12		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				1.8	A
Forward Voltage ¹	V _{SD}	I _F = 2A, V _{GS} = 0V			1.4	V
Reverse Recovery Time	t _{rr}	I _F = 2A, dI _F /dt = 100A / μS		40		nS
Reverse Recovery Charge	Q _{rr}			39		nC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

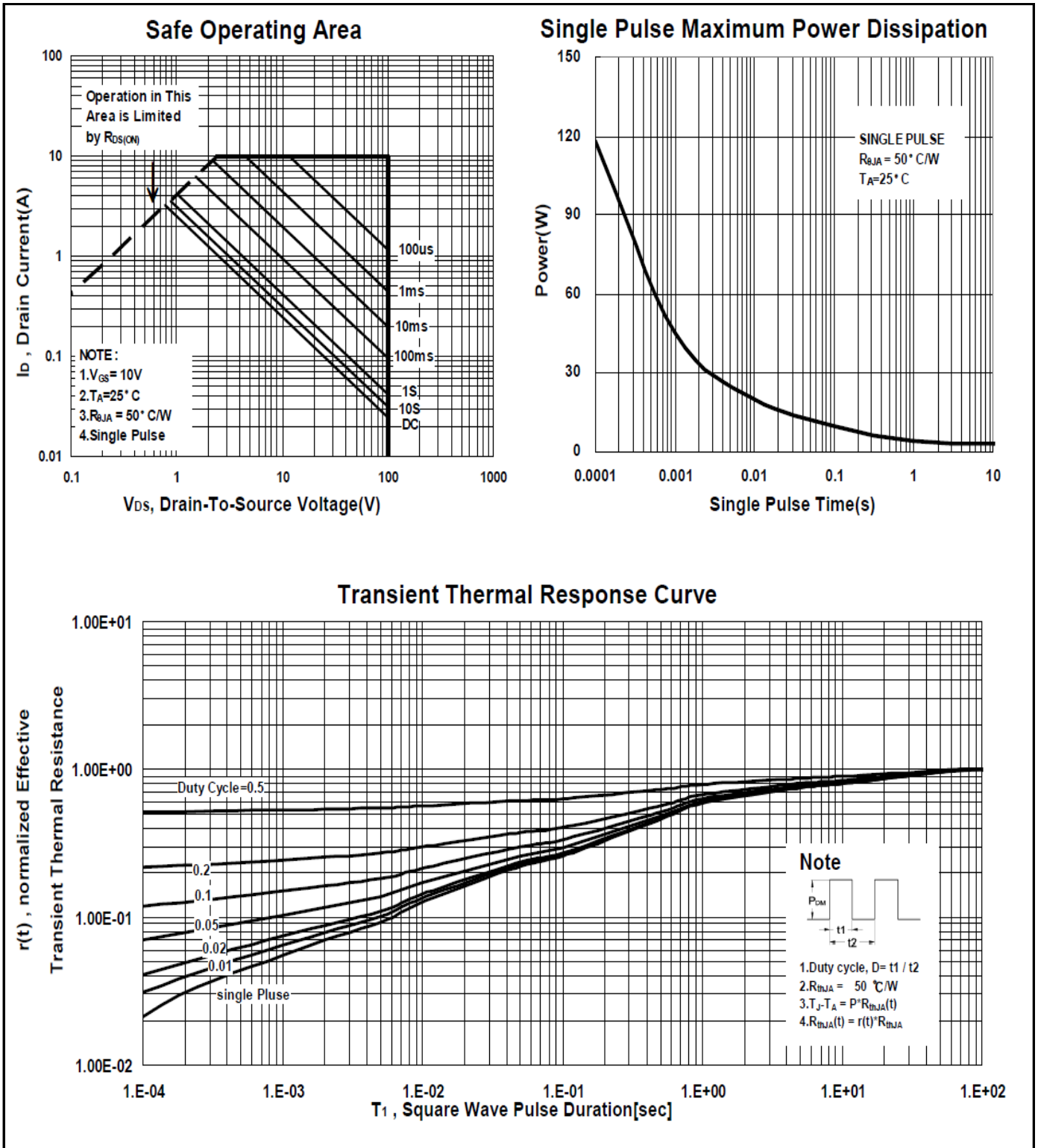
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SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

