PA20-16-1PD(Z)3(6) Data Sheet

20 pin PLCC Auto-eject *Dead-Bug* socket/16 pin DIP 0.3" or (0.6") plug

or

20 pin PLCC (Lidded ZIF Live-Bug) socket/16 pin DIP 0.3" or (0.6") plug

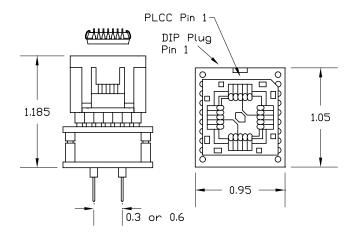
Supported Device/Footprints

These adapters allow programming of many 20 pin PLCC and CLCC devices in their 16 pin DIP footprint. Following is a <u>partial</u> list

Device			Footprint	
Mfgr	Device	Package	Device	Package
Several	74S288	20 pin PLCC	74S288	16 Pin DIP
AMD	AM2719	"	AM2719	II .

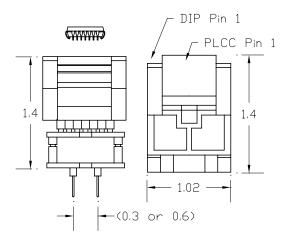
Adapter Dimensions

NOTE: THIS IS A DEAD-BUG SOCKET THE PLCC DEVICE IS INSERTED UPSIDE-DOWN



PA20-16-1PD3

NOTE: THIS IS A LIVE-BUG SOCKET THE PLCC DEVICE IS INSERTED RIGHT-SIDE-UP



PA20-16-1PZ3

LOGICAL SYSTEMS

Logical Systems Corporation PO Box 6184, Syracuse, NY 13217-6184 USA Tel (315) 478-0722, FAX (315) 479-6753 www.logicalsys.com, Email: info@logicalsys.com

Adapter Part Numbers

The following chart identifies the various boards and sockets that make up these versions of the adapter

Adapter	Socket	Top Board	Bottom Board
PA20-16-1PD3	20-205	20PLD	20-16-1-3
PA20-16-1PD6	20-205	20PLD	20-16-1-6
PA20-16-1PZ3	20-602	20PLZ	20-16-1-3
PA20-16-1PZ6	20-602	20PLZ	20-16-1-6

Adapter Construction

The adapter is made up of 3 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced when they wear out.

When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

Test Socket

PLCC Auto-Eject test socket:

PLCC Lidded ZIF test socket:

20PLD(Z)

Accepts the test socket and connects to the bottom board.

20-16-1-3(6)

Accepts the top board and provides the DIP footprint.

Adapter Wiring

The following chart shows the connections from the PLCC device to the adapter's DIP plug.

DEVICE	PLUG	
1	-	
2	1	
3	2	
4	3	
5	4	
6	-	
7	5	
8	6	
9	7	
10	8	
11	-	
12	9	
13	10	
14	11	
15	12	
16	-	
17	13	
18	14	
19	15	
20	16	