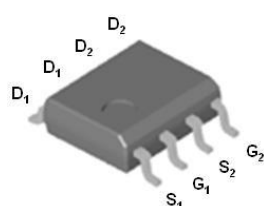


PA210HVA

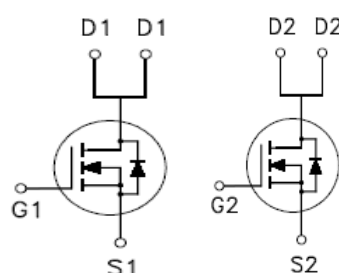
Dual N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	120mΩ @ $V_{GS} = 10V$	2.3A



SOP- 08



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_A = 25\text{ }^{\circ}\text{C}$	I_D	2.3	A
	$T_A = 70\text{ }^{\circ}\text{C}$		1.8	
Pulsed Drain Current ¹		I_{DM}	20	
Avalanche Current		I_{AS}	6	
Avalanche Energy	$L = 1\text{mH}$	E_{AS}	18	mJ
Power Dissipation	$T_A = 25\text{ }^{\circ}\text{C}$	P_D	1.5	W
	$T_A = 70\text{ }^{\circ}\text{C}$		0.9	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		85	$^{\circ}\text{C} / \text{W}$
Junction-to-Case	$R_{\theta JC}$		35	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}\text{C}$.

PA210HVA

Dual N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.0	1.8	3.0	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80V, V_{GS} = 0V$			1	μA
		$V_{DS} = 80V, V_{GS} = 0V, T_J = 70^\circ C$			10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 2A$		82	130	m Ω
		$V_{GS} = 10V, I_D = 2A$		78	120	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 2A$		10		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		591		pF
Output Capacitance	C_{oss}			52		
Reverse Transfer Capacitance	C_{rss}			31		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		1.6		Ω
Total Gate Charge ²	$Q_g(V_{GS}=10V)$	$V_{DS} = 50V, I_D = 2A$		13.5		nC
	$Q_g(V_{GS}=4.5V)$			7.8		
Gate-Source Charge ²	Q_{gs}			1.9		
Gate-Drain Charge ²	Q_{gd}			4.4		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DS} = 50V,$ $I_D \cong 2A, V_{GS} = 10V, R_{GEN} = 6\Omega$		15		nS
Rise Time ²	t_r			10		
Turn-Off Delay Time ²	$t_{d(off)}$			34		
Fall Time ²	t_f			10		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)						
Continuous Current	I_S				1.2	A
Forward Voltage ¹	V_{SD}	$I_F = 2A, V_{GS} = 0V$			1.2	V
Diode Reverse Recovery Time	t_{rr}	$I_F = 2A, di/dt = 100A / \mu S$		19		nS
Diode Reverse Recovery Charge	Q_{rr}			14		nC

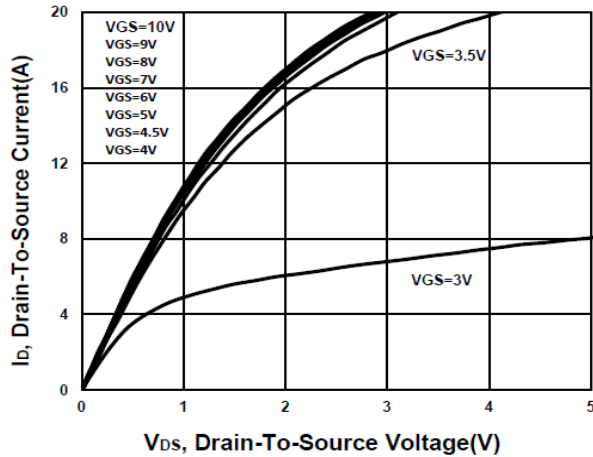
¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

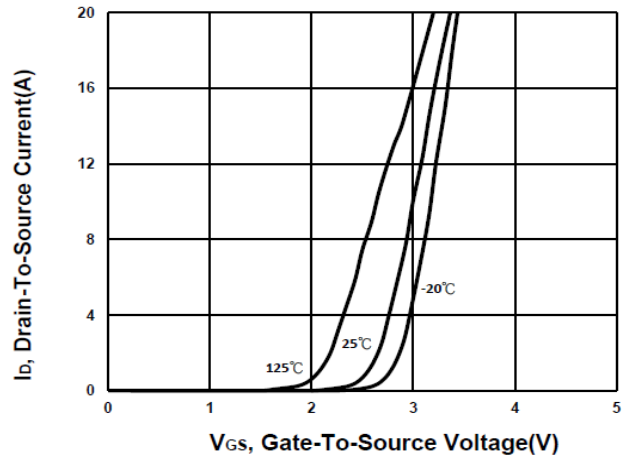
PA210HVA

Dual N-Channel Enhancement Mode MOSFET

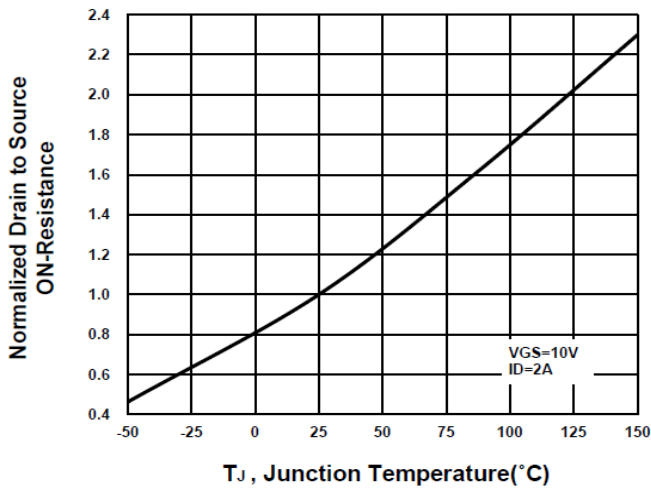
Output Characteristics



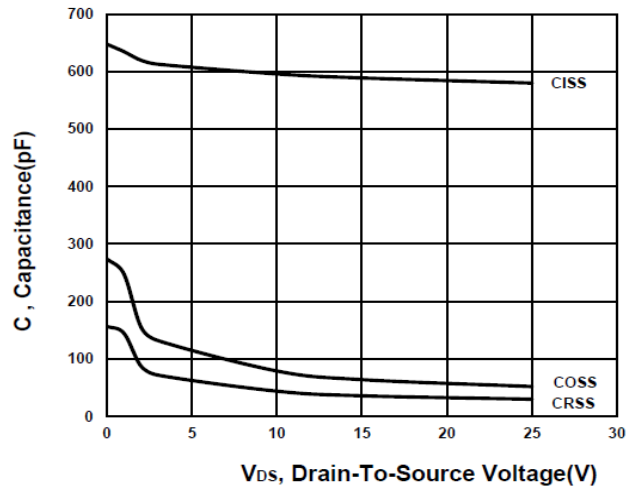
Transfer Characteristics



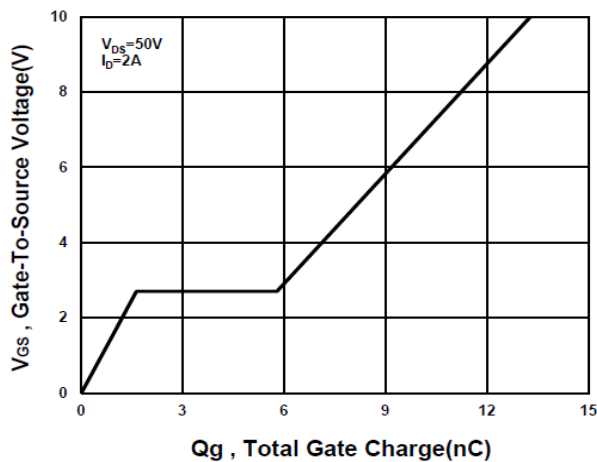
On-Resistance VS Temperature



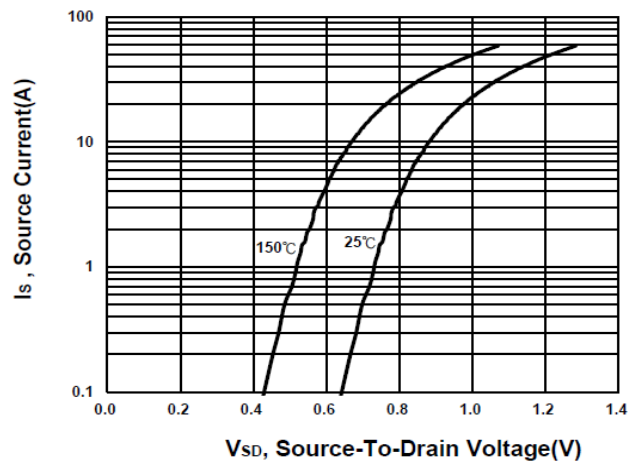
Capacitance Characteristic



Gate charge Characteristics



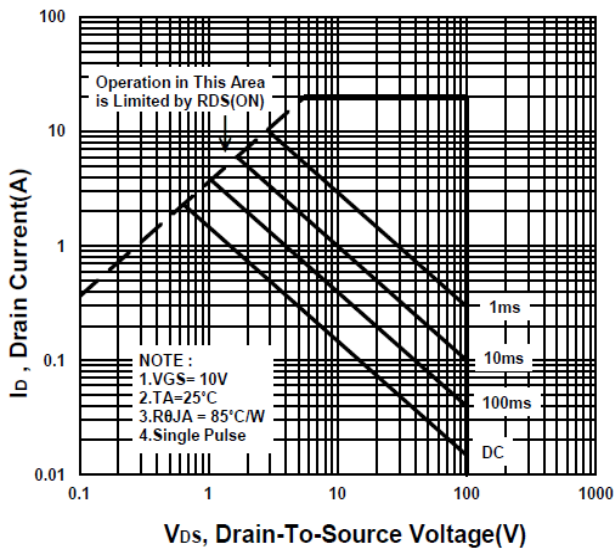
Source-Drain Diode Forward Voltage



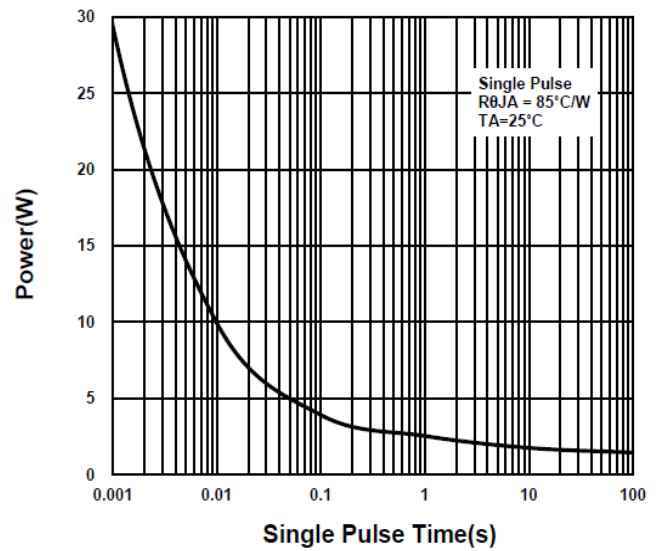
PA210HVA

Dual N-Channel Enhancement Mode MOSFET

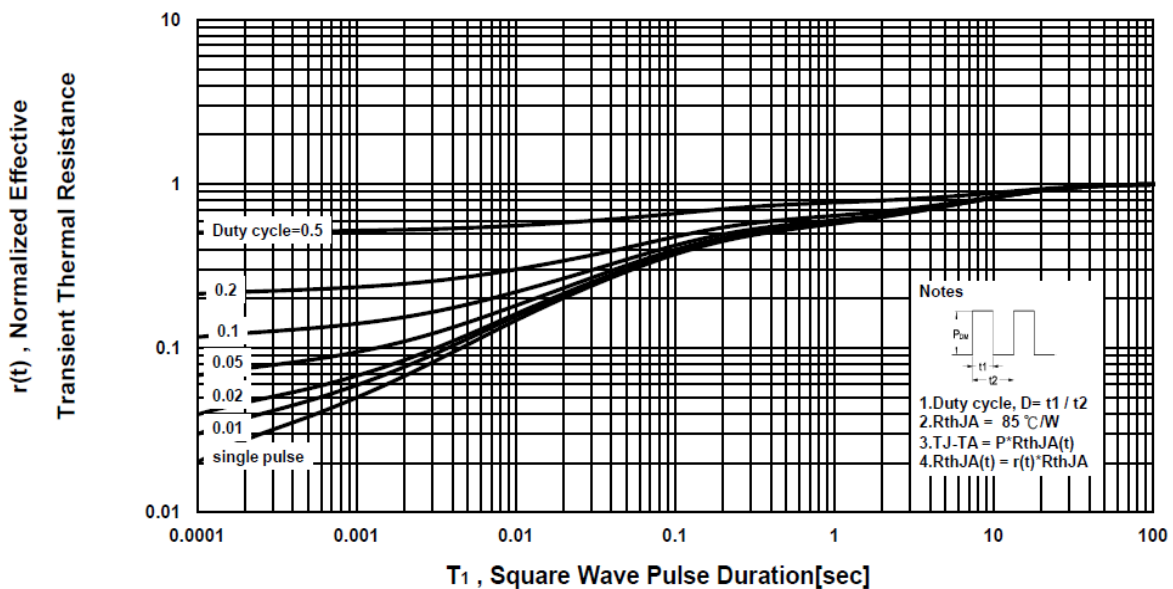
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



PA210HVA

Dual N-Channel Enhancement Mode MOSFET

Package Dimension

SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

