

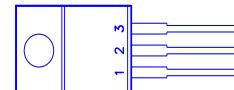
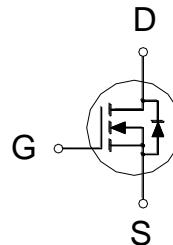
**NIKO-SEM**
**N-Channel Enhancement Mode  
Field Effect Transistor**
**PA410BT**

TO-220

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
100V	140m $\Omega$	11A



1. GATE
2. DRAIN
3. SOURCE

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	100	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	11	A
	$T_C = 100^\circ\text{C}$		6.8	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	30	
Avalanche Current		$I_{AS}$	9.7	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	4.7	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	38	W
	$T_C = 100^\circ\text{C}$		15	
Junction & Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		3.3	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.9	2.3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$			1	$\mu\text{A}$
		$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{V}, I_D = 5\text{A}$	102	170		$\text{m}\Omega$
		$V_{GS} = 10\text{V}, I_D = 5\text{A}$	91	140		
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 10\text{V}, I_D = 5\text{A}$	13			S

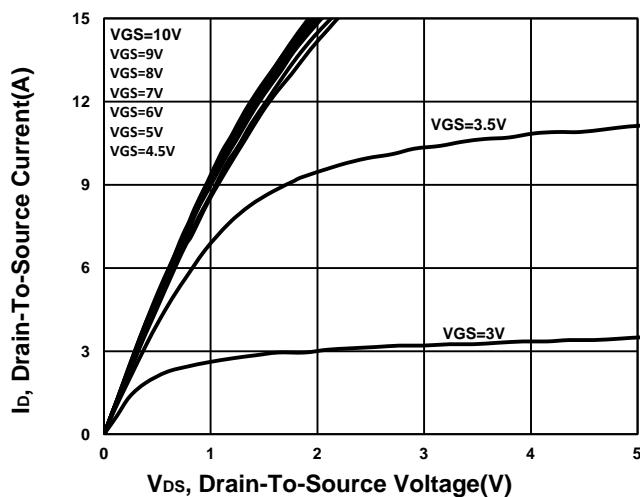
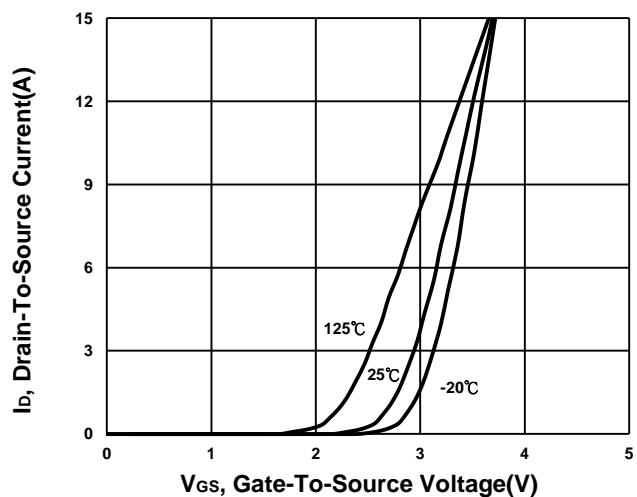
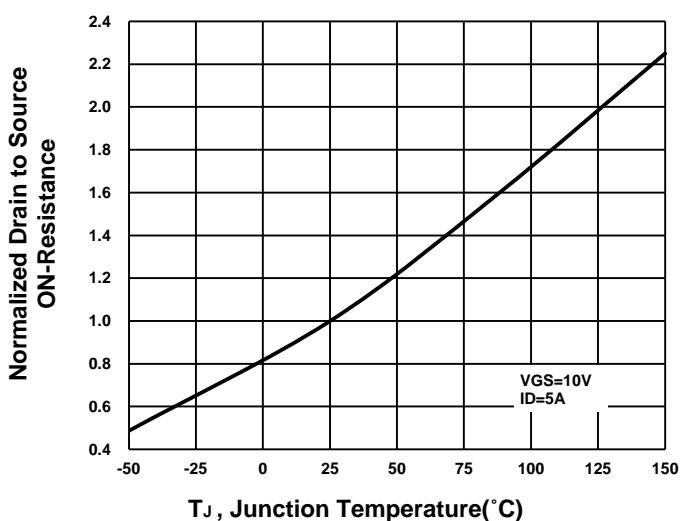
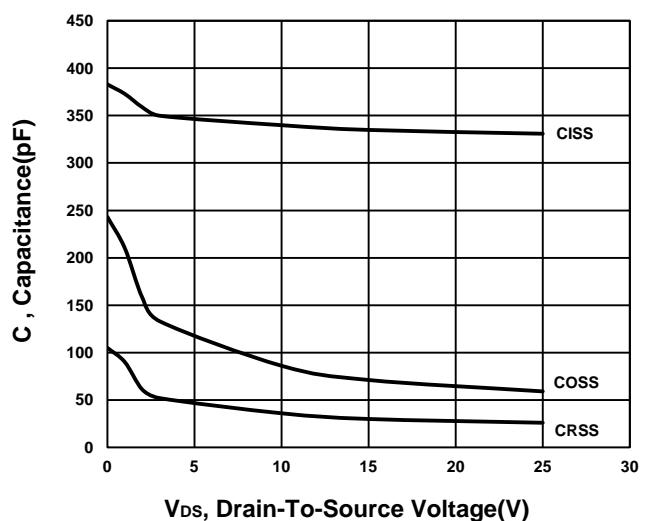
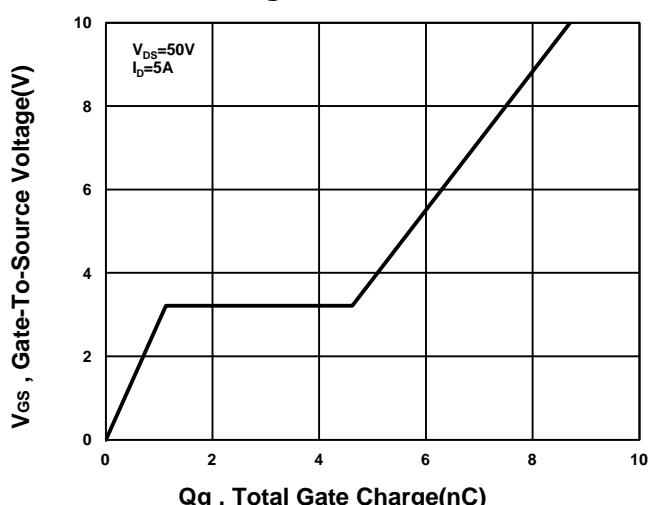
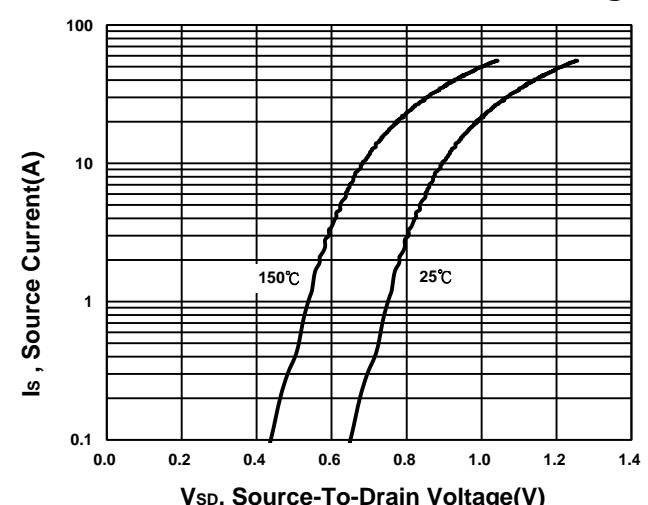
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DYNAMIC						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$	335			pF
Output Capacitance	$C_{oss}$		60			
Reverse Transfer Capacitance	$C_{rss}$		26			
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{GS} = 10V, V_{DS} = 50V, I_D = 5A$	8.8			nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$		1.6			
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$		3.8			
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 50V$ $I_D \geq 5A, V_{GS} = 10V, R_{GEN} = 6\Omega$	22			nS
Rise Time <sup>2</sup>	$t_r$		60			
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		30			
Fall Time <sup>2</sup>	$t_f$		40			
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( $T_J = 25^\circ C$ )						
Continuous Current	$I_S$				11	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 5A, V_{GS} = 0V$			1.1	V
Reverse Recovery Time	$t_{rr}$	$I_F = 5A, dI_F/dt = 100A/\mu S$			28	nS
Reverse Recovery Charge	$Q_{rr}$				31	nC

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.

**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****PA410BT  
TO-220  
Halogen-Free & Lead-Free****Output Characteristics****Transfer Characteristics****On-Resistance VS Temperature****Capacitance Characteristic****Gate charge Characteristics****Source-Drain Diode Forward Voltage**

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