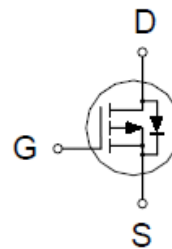
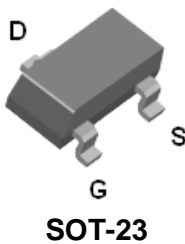


PA504EM

P-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
-40V	150m Ω @ $V_{GS} = -10V$	-1.5A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_A = 25\text{ }^\circ\text{C}$	I_D	-1.5	A
	$T_A = 70\text{ }^\circ\text{C}$		-1.1	
Pulsed Drain Current ¹		I_{DM}	-9	
Power Dissipation	$T_A = 25\text{ }^\circ\text{C}$	P_D	0.5	W
	$T_A = 70\text{ }^\circ\text{C}$		0.3	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		220	$^\circ\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz Copper

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

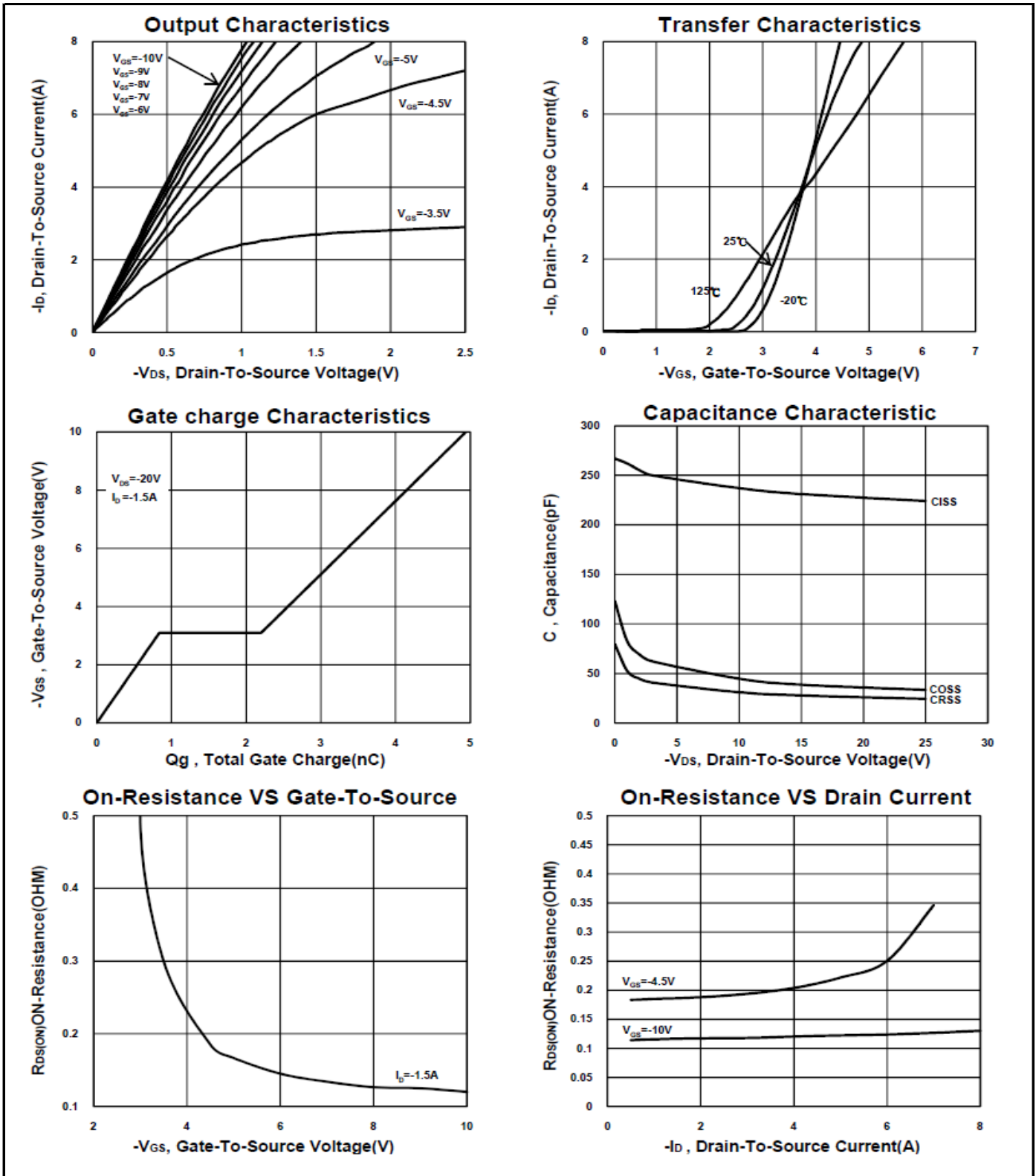
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-40			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.30	-1.8	-3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -32V, V _{GS} = 0V			-1	μA
		V _{DS} = -30V, V _{GS} = 0V, T _J = 55 °C			-10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = -10V, V _{GS} = -4.5V	-8			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = -4.5V, I _D = -1.2A		170	250	mΩ
		V _{GS} = -10V, I _D = -1.5A		120	150	
Forward Transconductance ¹	g _{fs}	V _{DS} = -5V, I _D = -1.5A		4		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = -20V, f = 1MHz		235		pF
Output Capacitance	C _{oss}			40		
Reverse Transfer Capacitance	C _{rss}			29		
Total Gate Charge ²	Q _g	V _{DS} = 0.5V _{(BR)DSS} , V _{GS} = -10V, I _D = -1.5A		5.1		nC
Gate-Source Charge ²	Q _{gs}			1		
Gate-Drain Charge ²	Q _{gd}			1.7		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = -20V I _D ≅ -1.5A, R _G = 6Ω		14		nS
Rise Time ²	t _r			6		
Turn-Off Delay Time ²	t _{d(off)}			30		
Fall Time ²	t _f			13		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTIC (T_J = 25 °C)						
Continuous Current	I _S				-0.5	A
Forward Voltage ¹	V _{SD}	I _F = -1.5A, V _{GS} = 0V			-1.1	V
Reverse Recovery Time	t _{rr}	I _F = -3A, dI _F /dt=100A/μS		13		nS
Reverse Recovery Charge	Q _{rr}				7	

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

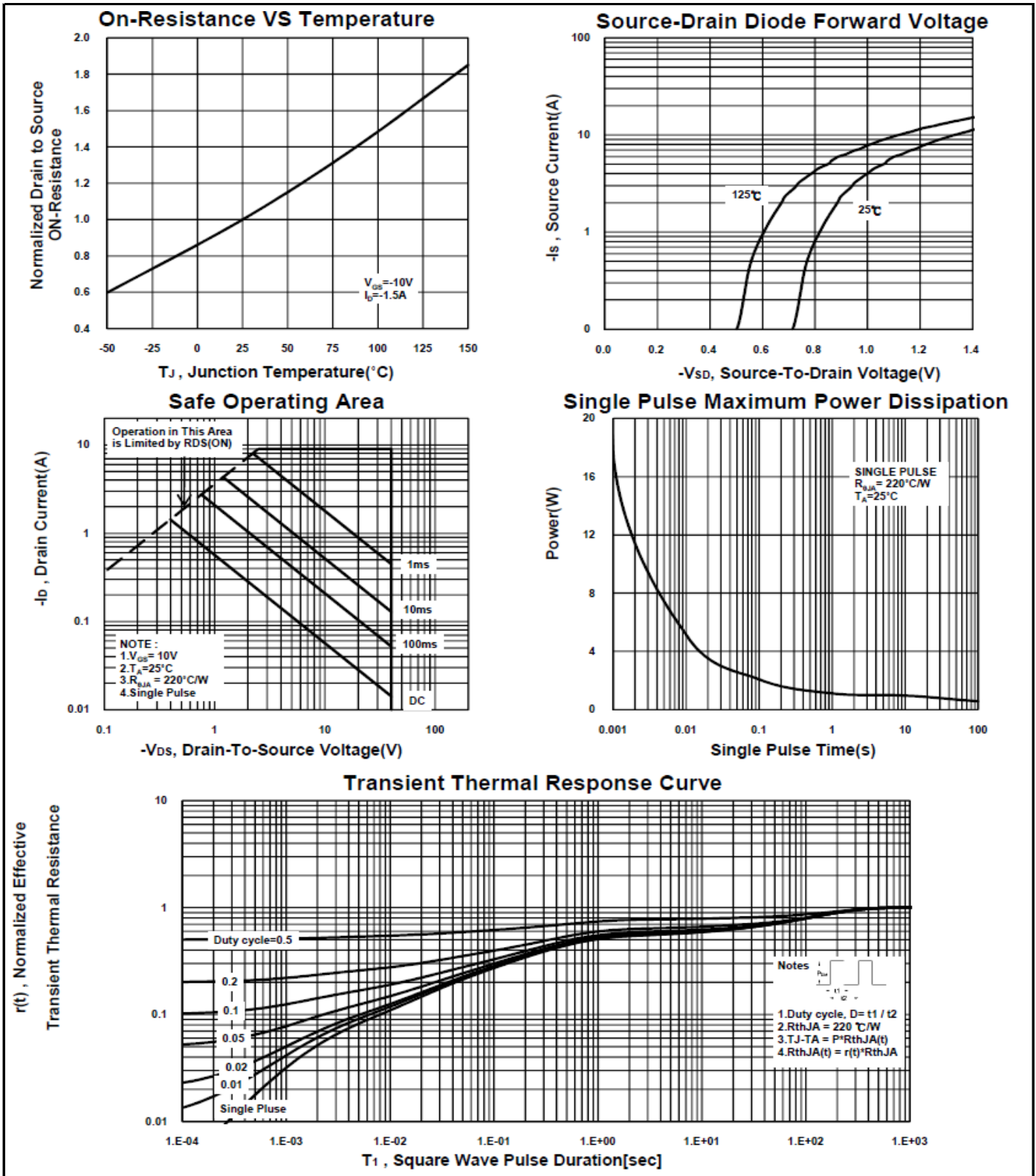
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Package Dimension

SOT-23-3 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A		1.05		H	0.1		0.2
B	2.4		3	I	0.3		0.6
C	1.4		1.73				
D	2.7		3.1				
E	1		1.31				
F	0		0.15				
G	0.3		0.5				

