

PAW3102DB HIGH RESOLUTION CMOS OPTICAL MOUSE SENSOR

General Description

The PAW3102DB is a high resolution CMOS process optical mouse sensor with DSP integration chip that serves as a non-mechanical motion estimation engine for implementing a computer mouse.

F'eat	tures	Key Specification	n	
	Single 5.0/3.3 volt power supply		Wide operating supply range	
	Built-in \pm 10% accurate oscillator, external crystal-less	Power Supply	3.0V ~ 3.6V (VDD, VREF) 4.25V ~ 5.5V (VDD)	
	Precise optical motion estimation technology	Optical Lens	1:1	
	Complete 2-D motion sensor			
	No mechanical parts	Speed	28 inches/sec	
	Accurate motion estimation over a wide range of surfaces	Resolution	800/1000(default)/1200/1600 CPI	
	High speed motion detection up to 28 inches/sec	Frame Rate	3000 frames/sec	
	High resolution up to 1600 CPI	Operating	10 mA @Mouse moving (Normal)	
	Register setting for low power dissipation	Current	5 mA @Mouse not moving (Sleep) 100 uA @Power down mode	
	Power saving mode during times of no movement	Package	Staggered DIP8	
	Serial Interface for programming and data transfer			
	I/O pin 5.0 volt tolerance			
	EFT enhancement			

Ordering Information

Order Number	I/O	Resolution
PAW3102DB	CMOS output	1000 CPI

1. Pin Configuration

1.1 Pin Description

Pin No.	Name	Туре	Definition
1	OSC_RES	IN	Internal RC oscillator for system clock without external resistor
2	NC	-	No connect
3	SDIO	I/O	Serial interface bi-direction data (5.0 volt tolerance and 3.3V output)
4	SCLK	IN	Serial interface clock (5.0 volt tolerance)
5	LED	OUT	LED control
6	VSS	GND	Chip ground
7	VDD	PWR	Chip power, $3.0V \sim 3.6V(V_{dd1})$ or $4.25V \sim 5.5(V_{dd2})$
8	VREF	BYPASS	Voltage reference, 3.3V

1.2 Pin Assignment

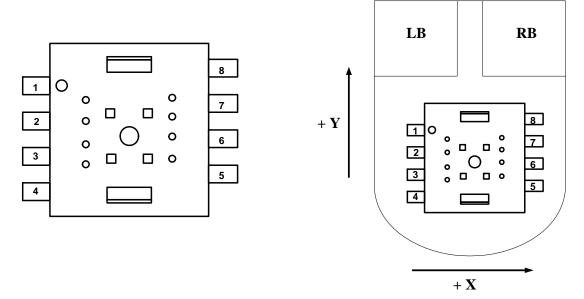


Figure 1. Top View Pinout

Figure 2. Top View of Mouse

2. Block Diagram and Operation

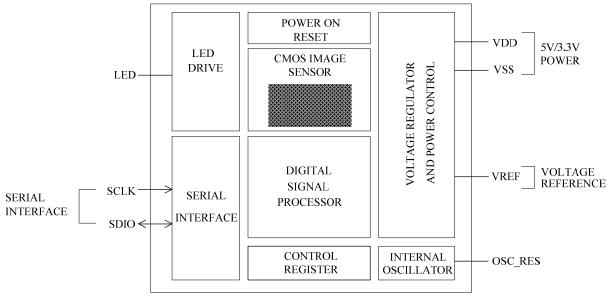


Figure 3. Block Diagram

The PAW3102DB is a high resolution CMOS-process optical mouse sensor with DSP integration chip that serves as a non-mechanical motion estimation engine for implementing a computer mouse. It is based on new optical navigation technology, which measures changes in position by optically acquiring sequential surface images (frames) and mathematically determining the direction and magnitude of movement. The mouse sensor is in an 8-pin optical package. The current X and Y information are available in registers accessed via a serial port. The word "mouse sensor," instead of PAW3102DB, is used in the document.

3. Registers and Operation

The released registers of the mouse sensor can be programmed via a serial port interface. The user can modify released DSP configuration and read motion data from these registers. Some registers not being listed are reserved not allowed to be modified.

3.1 Registers

Address	Name	R/W	Reset Value	Data Type
0x00	Operation_Mode1	R/W	0x00	Bit field
0x01	Product_ID1	R	0x01	Bit field
0x02	Delta_Y	R	-	Eight bits 2's complement number
0x03	Delta_X	R	-	Eight bits 2's complement number
0x04	Image_Quality	R	-	Eight bits unsigned integer
0x05				
	-	-	-	Reserved for future use
0x11				
0x12	Write_Protect	W	-	Bit field
0x13	-	-	-	Reserved for future use
0x14	Product_ID2	R	0x10	Eight bits [11:4] number with the product identifier
0x15	Draduat ID2	R	0x2N	Four bits [3:0] number with the product identifier
UXIS	Product_ID3	K	UXZIN	Reserved [3:0] number is reserved for future
0x16	Motion_Status	R	-	Bit field
0x17	Delta_X	R	-	Eight bits 2's complement number
0x18	Delta_Y	R	-	Eight bits 2's complement number
0x19	Image_Quality	R	-	Eight bits unsigned integer
0x1A	Operation_Mode2	R	0x0C	Bit field
0x1B	Configuration	R	0x64	Bit field
0x1C				
	-	-	-	Reserved for future use
0x3F				

3.2 Register Descriptions

0x00				Operatio	n_Mode1						
Bit	7	6	5	4	3	2	1	0			
Field	Reset	PD_enh	0	0	0	0	0	Slp_enl			
Usage	their default If <i>Slp_enl</i> =	gister $0x00$ allows the user to change the operation of the mouse sensor. Shown below are the bits, ir default values, and optional values. Stp_ent = 0, After 1 sec not moving during normal mode, the mouse sensor will enter sleep mode, the keep on sleep mode until moving is detected or wakeup is asserted.									
Notes	Field Name	Descri	iption								
	Reset	Reset Full chip reset 0 = Normal operation mode (Default) 1 = Full chip reset									
	PD_enh	Power down mode 0 = Normal operation mode (Default) 1 = Power down mode									
	Bit [5:1]	[5:1] MUST always be 00000									
	Slp_enl	$0 = \mathbf{E}\mathbf{r}$	Sleep mode enable/disable 0 = Enable (Default) 1 = Disable								
0x01				Produ	ct_ID1						
Bit	7	6	5	4 3 2 1 0							
Field		PID1[2:0]		Reserved[3:0] Opstate							
Usage	Product ID	of mouse sen	sor and opera	ation state of	the mouse se	nsor.					
Notes	Field Name	Descri	iption								
	PID1[2:0]	The pr	oduct ID is 0	00							
	Reserved[3:	[0] Reserv	ed for future	use							
	Opstate	0 = Sle	tion state eep state ormal state								
0x02				Delt	a_Y			•			
Bit	7	6	6 5		3	2	1	0			
Field	Y7	Y6	Y5	Y4	Y3	Y2	Y1	Y0			
Usage			nce last reporte –128 ~ +12	rt. Absolute v 7.	alue is deterr	nined by reso	lution. Reac	ling clears			

0x03				Delt	a_X						
Bit	7	6	5	4	3	2	1	0			
Field	X7	X6	X5	X4	Х3	X2	X1	X0			
Usage	X movement is counts since last report. Absolute value is determined by resolution. Reading clears the register. Report range $-128 \sim +127$.										
0x04	Image_Quality										
Bit	7	6	5	4	3	2	1	0			
Field				Imgq	a[7:0]	•					
Usage	Image Qual	ity is a qua	lity level of the	e mouse senso	or in the curre	ent frame. Rej	ort range 0	~ 255.			
Notes	Field Name	e Des	cription								
	Imgqa[7:0]	Ima	ge quality repo	rt range: 0(wo	orst) ~ 255(b	est).					
0x12				Write_	Protect						
Bit	7	6	5	4	3	2	1	0			
Field]	Reserved[2	:0]			WP[4:0]					
Notes		sers have to	function is disa enable the <i>Wr</i>				B. After writ	ing			
1,000	Reserved[2:	:0] Res	erved for future	e use. Must be written to zero.							
	WP[4:0]	Write protect enable/disable for 0x1A, 0x1B 00000 = Enable (Default), register 0x1A, 0x1B is read only 10101 = Disable, register 0x1A, 0x1B can be read/written									
0x14				Produ	ct_ID2						
Bit	7	6	5	4	3	2	1	0			
Field				PID2	[11:4]						
Usage	The value in link is OK.	in this regis	ter is fixed. It	can be used to	verify if the	status of the	serial comm	unications			
0x15				Produ	ct_ID3						
Bit	7	6	5	4	3	2	1	0			
Field		PI	D2[3:0]	•		Reserv	ed[3:0]	•			
Usage	communica	tions link i	ter is fixed. PII s OK. Reserved serial commun	d[3:0] is a val							

0x16				Motio	n_Status							
Bit	7	6	5	4	3	2	1	0				
Field	Motion	Reserv	ed[2:1]	DYOVF	DXOVF Reserved[0] RES[1:0]							
Usage	so, then the motion buff Reading thi reading the	user should ers have ove s register fre Delta_X and	ows the user to determine if motion has occurred since the last time it was read. If should read registers 0x17 and 0x18 to get the accumulated motion. It also tells if the ave overflowed since the last reading. The current resolution is also shown. Seter freezes the <i>Delta_X</i> and <i>Delta_Y</i> register values. Read this register beforeX and <i>Delta_Y</i> registers. If <i>Delta_X</i> and <i>Delta_Y</i> are not read before the motion second time, the data in <i>Delta_X</i> and <i>Delta_Y</i> will be lost.									
Notes	Field Name	e Descri	Description									
	Motion	0 = Nc	Motion since last report or PD 0 = No motion (Default) 1 = Motion occurred, data ready for reading in <i>Delta_X</i> and <i>Delta_Y</i> registers									
	Reserved[2	:1] Reserv	ed for future	use								
	DYOVF	Motion Delta Y overflow, ΔY buffer has overflowed since last report 0 = No overflow (Default) 1 = Overflow has occurred										
	DXOVF	0 = Nc	n Delta X ov o overflow (I verflow has o	Default)	uffer has over	rflowed since la	ast report					
	Reserved[0]	Reserv	ed for future	e use								
	RES[1:0]		00									
0x17			_	De	lta_X							
Bit	7	6	5	4	3	2	1	0				
Field	X7	X6	X5	X4	X3	X2	X1	X0				
Usage			ince last repose $-128 \sim +12$		value is deter	mined by resol	ution. Readir	ng clears				
0x18				De	lta_Y							
Bit	7	6	5	4	3	2	1	0				
Field	Y7	Y6	Y5	Y4	Y3	Y2	Y1	Y0				
Usage			ince last repose se -128 ~ +12		value is deter	mined by resol	ution. Readir	ng clears				

0x19		Image_Quality										
Bit	7 6			5	4	3		2	1	0		
Field						Imgqa[7:0]						
Usage	Image Qu	Image Quality is a quality level of the mouse sensor in the current frame. Report range $0 \sim 255$.										
Notes	Field Name Description											
	Imgqa[7:0] Image quality report range: 0(worst) ~ 255(best).											
0x1A					Oj	peration_Mo	de2					
Bit	7		6	5	4	3		2	1	0		
Field	Reset	PD	_enh	CP	[[1:0]	LEDsht_e	nh	Slp_enh	Slpmu_enh	Wakeup		
	Operation "0xx" = D "110" = F "101" = F Notes: 1. After 1 sleep n 2. Only or	Register 0x1A allows the user to change the operation of the mouse sensor. Shown below are the bits, their default values, and optional values. Operation_Mode2[2:0] "0xx" = Disable sleep mode "110" = Force enter sleep "101" = Force wakeup from sleep mode Notes: 1. After 1 sec not moving during normal mode, the mouse sensor will enter sleep mode, and keep on sleep mode until moving is detected or wakeup is asserted. 2. Only one of these two bits Slpmu_enh and Wakeup can be set to 1 at the one time, and the other one has to be set to 0. Note that these bits are self-clear.										
Notes	Field Nan	ne	Desc	ription								
	Reset		0 = N	chip reset N ormal ope r ull chip rese		le (Default)						
	PD_enh		0 = N	er down mod N ormal ope r ower down	ration mod	le (Default)						
	Output resolution setting 0 = 1000 (Default) 1 = 1200 2 = 1600 3 = 800											
	LEDsht_e	nh	$0 = \Gamma$	shutter enal Disable Cnable (Def								
	Slp_enh		$0 = \Gamma$	o mode enab Disable Cnable (Def								
	Slpmu_en	h	Manı	ıal enter sle	ep mode, se	et "1" will ente	er sl	eep and this b	it will be reset to	"0"		
	Wakeup		Manu to "0"		from sleep	mode, set "1"	wil	l enter wakeu	p and this bit will	be reset		

8

CMOS Optical Mouse Sensor

0x1B				Config	uration					
Bit	7	6	5	4	3	2	1	0		
Field	XY_exch	Y_inv	X_inv	0	0	1	0	0		
Usage	Register 0x1B allows the user to change the XY direction of the mouse sensor. Shown below are the bits, their default values, and optional values.									
	"011" = Ty $"110" = The$	Configuration[7:5] "011" = Typical direction 0° "110" = The mouse sensor turn -90° "101" = The mouse sensor turn +90°								
	Notes: When write <i>Configuration</i> [7:5], users have to keep <i>Configuration</i> [4:0] values. So the only way to do this is read <i>Configuration</i> [7:0] first, set/clear the desired bits of <i>Configuration</i> [7:5] and keep the same value of <i>Configuration</i> [4:0], then write <i>Configuration</i> [7:0].									
Notes	Field Name	Descri	ption							
	XY_exch	$0 = \mathbf{Di}$	sable (Defau	exchange fund alt) Y direction ex						
	Y_inv	Y direction inverse function inv 0 = Enable(Y direction inverse) 1 = Disable (Default)								
	X_inv	X direction inverse function 0 = Enable (X direction inverse) 1 = Disable (Default)								
	Bit [4:0]	Must	always be 00	100						

4. Specifications

4.1 Absolute Maximum Ratings

Stresses above those listed under "Absolute Maximum Rating" may cause permanent damage to the device. These are stress ratings only. Functional operation of this device at these or any other conditions above those indicated in the operational sections of this specification is not implied and exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter	Min	Max	Unit	Notes
T_{STG}	Storage temperature	-40	85	°C	
TA	Operating Temperature	-15	55	°C	
	Lead Solder Temp		260	°C	For 10 seconds, 1.6mm below seating plane.
V_{DC}	DC supply voltage	-0.5	5.5	V	
ESD			2	kV	All pins, human body model MIL 883 Method 3015
V _{IN}	DC input voltage	-0.5	V_{DC}	V	SDIO, SCLK, VDD

4.2 Recommend Operating Condition

Symbol	Parameter	Min.	Typ.	Max.	Unit	Notes
T_A	Operating Temperature	0		40	°C	
V_{dd1}	Dawar Cumply Waltage	3.0	3.3	3.6	V	VDD, VREF short
V_{dd2}	Power Supply Voltage	4.25	5.0	5.5]	VDD only
$V_N @ V_{dd1}$	Cumply paiga			50	m.V	Dools to mools within 0 90 MHz
$V_N @ V_{dd2}$	Supply noise			100	mV	Peak to peak within 0 - 80 MHz
Z	Distance from lens reference plane to surface	2.3	2.4	2.5	mm	Refer to Figure 4.
R	Resolution	800	1000	1600	CPI	
A	Acceleration			20	g	
SCLK	Serial Port Clock Frequency			10	MHz	
FR	Frame Rate		3000		frames/sec	
S	Speed	0		28	inches/sec	

4.3. AC Operating Condition

Electrical Characteristics over recommended operating conditions. Typical values at 25 °C, V_{DD} =5.0 V.

Symbol	Parameter	Min.	Тур.	Max.	Unit	Notes
$t_{ m HOLD}$	SDIO read hold time		3		us	Minimum hold time for valid data. (Refer to Figure 9)
t_{RESYNC}	Serial Interface RESYNC.	1			us	@3000frame/sec (Refer to Figure 10)
$t_{ m SIWTT}$	Serial Interface Watchdog Timer Timeout	1.7			ms	@3000frame/sec (Refer to Figure 10)
t_{PDR}	PD Pulse Register			666	us	Two frames time maximum after setting bit 6 in the <i>Operation_Mode</i> register @3000frame/sec. (Refer to Figure 11)
t _{PUPD}	Power Up from deactivate the Power Down mode	3		30.5	ms	From deactivate power down mode to valid motion data. After t _{PUPD} , all registers contain valid data from first image after deactivate power down mode. Note that an additional 90 frames for Auto-Exposure (AE) stabilization may be required if mouse movement occurred while PD bit in register was high. @3000 frame/sec
$t_{ m PU}$	Power Up from V _{DD} ↑	3		30.5	ms	From V _{DD} ↑ to valid motion data. @3000 frame/sec

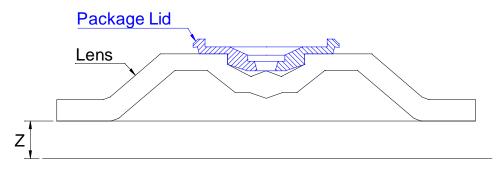
E-mail: fae_service@pixart.com.tw V4.1, Mar. 2010

4.4. DC Operating Condition

Electrical Characteristics over recommended operating conditions. Typical values at 25 °C, V_{DD} =5.0 V.

Symbol	Parameter	Min.	Typ.	Max.	Unit		
Type:	Type: PWR						
$I_{ m DDN}$	Supply Current Mouse moving (Normal)		10		mA	SCLK, SDIO = no load	
I_{DDS1}	Supply Current Mouse not moving (sleep1)		5		mA		
I_{DDPD}	Supply Current (Power Down)		100		uA	SCLK, SDIO = high	
Type: S	Type: SCLK, SDIO						
V _{IH}	Input voltage HIGH	2.0					
V _{IL}	Input voltage LOW			0.7	V		
V _{OH}	Output voltage HIGH	2.4			V	@I _{OH} = 2mA (SDIO only)	
V _{OL}	Output voltage LOW			0.6	V	@I _{OL} = 2mA (SDIO only)	
Type: L	Type: LED						
V _{OL}	Output voltage LOW			150	mV	$@I_{OL} = 25mA$	

5. Z and 2D/3D Assembly



OBJECT SURFACE

Figure 4. Distance from Lens Reference Plane to Surface

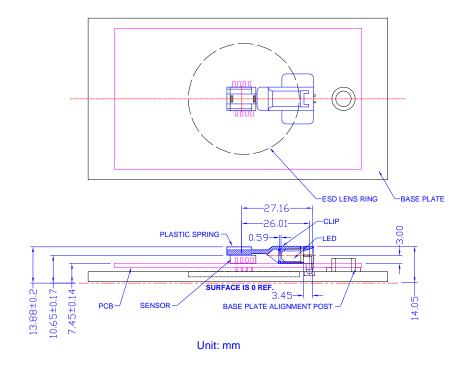
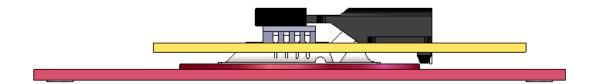


Figure 5. 2D Assembly



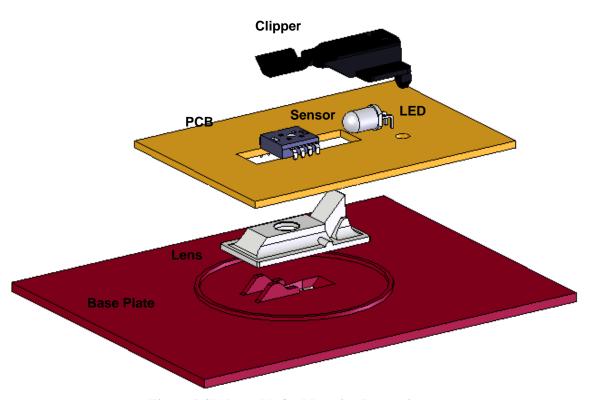


Figure 6. 3D Assembly for Mounting Instructions

6. Serial Interface

The synchronous serial port is used to set and read parameters in the mouse sensor.

SCLK: The serial clock line. It is always generated by the mouse controller.

SDIO: The serial data line is used to write and read data.

6.1 Transmission Protocol

The transmission protocol is a two-wire link, half duplex protocol between the mouse controller and the mouse sensor. All data changes on SDIO are initiated by the falling edge on SCLK. The mouse controller instead of the mouse sensor has the ability to initiates data transfers.

The transmission protocol consists of the two operation modes:

- Write Operation.
- Read Operation.

Both of the two operation modes consist of two bytes. The first byte contains the address (seven bits) and has a bit7 as its MSB to indicate data direction. The second byte contains the data.

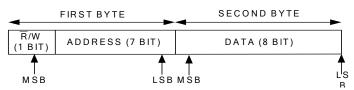


Figure 7. Transmission Protocol

6.1.1 Write Operation

A write operation, which means that data is going from the mouse controller to the mouse sensor, is always initiated by the mouse controller and consists of two bytes. The first byte contains the address (seven bits) and has a "1" as its MSB to indicate data direction. The second byte contains the data. The transfer is synchronized by SCLK. The mouse controller changes SDIO on falling edges of SCLK. The mouse sensor reads SDIO on rising edges of SCLK.

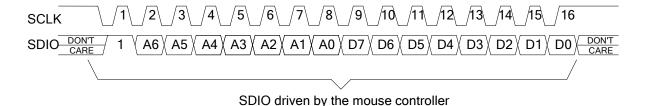
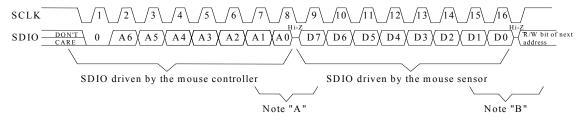


Figure 8. Write Operation

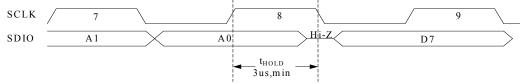
6.1.2 Read Operation

A read operation, which means that data is going from the mouse sensor to the mouse controller, is always initiated by the mouse controller and consists of two bytes. The first byte contains the address, is written by the mouse controller, and has a "0" as its MSB to indicate data direction. The second byte contains the data and is driven by the mouse sensor. The transfer is synchronized by SCLK. SDIO is changed on falling edges of SCLK and read on every rising edge of SCLK. The mouse controller must go to a high Z state after the last address data bit. The mouse sensor will go to the high Z state after the last data bit.



Note "A" 1. The mouse controller sends address to the mouse sensor.

2. The mouse controller releases and set SDIO to Hi-Z after the last address bit.



Note "B" 1. The mouse sensor sends data to the mouse controller.

2. The mouse sensor releases and set SDIO to Hi-Z after the last data bit.

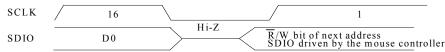


Figure 9. Read Operation

6.2 Re-Synchronous Serial Interface

If the mouse controller and the mouse sensor get out of synchronization, then the data either written or read from the registers will be incorrect. In such a case, an easy way to solve this condition is to toggle the SCLK line from high to low for least t_{RESYNC} , and then MUST toggle it from low to high to wait at least t_{SIWTT} to reach resynchronous the serial port. This method is called by "watchdog timer timeout". The mouse sensor will reset the serial port without resetting the registers and be prepared for the beginning of a new transmission.

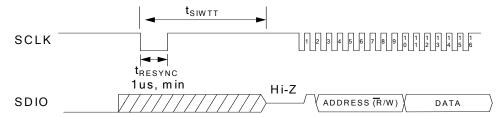


Figure 10. Re-synchronous Serial Interface Using Watchdog Timer Timeout

Note that this function is disabled when the mouse sensor is in the power down mode. If the user uses this function during the power down mode, it will get out of synchronization. The mouse sensor and the mouse controller also might get out of synchronization due to following conditions.

16

- Power On Problem The problem occurs if the mouse sensor powers up before the mouse controller sets the SCLK and SDIO lines to be output. The mouse sensor and the mouse controller might get out of synchronization due to power on problem. An easy way to solve this is to use "watchdog timer timeout".
- ESD Events The mouse sensor and the mouse controller might get out of synchronization due to ESD events. An easy way to solve this is to use "watchdog timer timeout".

6.3 Collision Detection on SDIO

The only time that the mouse sensor drives the SDIO line is during a READ operation. To avoid data collisions, the mouse controller should release SDIO before the falling edge of SCLK after the last address bit. The mouse sensor begins to drive SDIO after the next falling edge of SCLK. The mouse sensor releases SDIO of the rising SCLK edge after the last data bit. The mouse controller can begin driving SDIO any time after that. In order to maintain low power consumption in normal operation, the mouse controller should not leave SDIO floating until the next transmission (although that will not cause any communication difficulties).

6.4 Power Down Mode

The mouse sensor can be placed in a power-down mode by setting **PD_enh** bit in the **Operation_Mode** register via a serial port write operation. After setting the **Operation_Mode** register, wait at most 2 frames times. To get the chip out of the power down mode, clear **PD_enh** bit in the **Operation_Mode** register via a serial port write operation. In the power down mode, the serial interface watchdog timer (see Section 6.2) is not available. But, the serial interface still can read/write normally. For an accurate report after leave the power down mode, wait about 3ms before the mouse controller is able to issue any write/read operation to the mouse sensor.

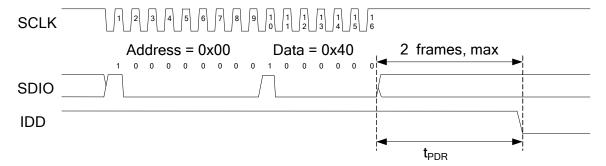


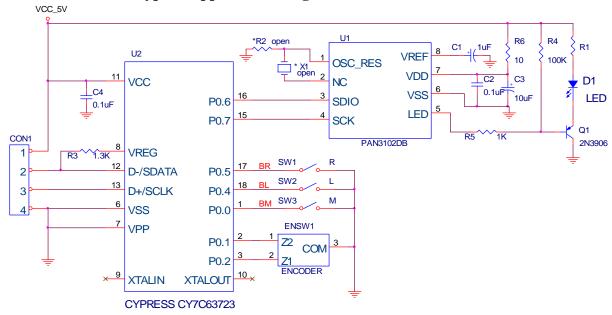
Figure 11. Power-down Configuration Register Writing Operation

6.5 Error Detection

- 1. The mouse controller can verify success of write operations by issuing a read command to the same address and comparing written data to read data.
- 2. The mouse controller can verify the synchronization of the serial port by periodically reading the product ID register

7. Referencing Application Circuit

7.1 Recommended Typical Application using External LED Control (5Volt)

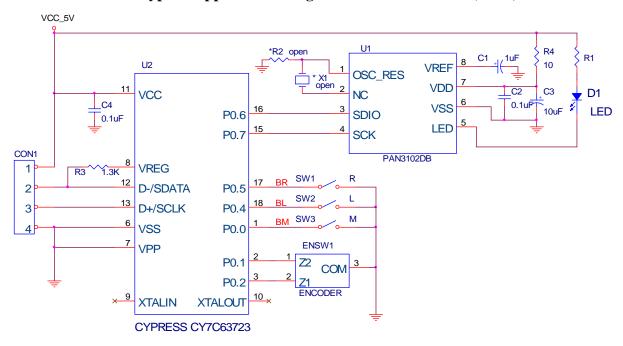


Note:

- 1. *R2 is left for future use
- 2. *X1 is left for PAN3101DB

Figure 12. Application Circuit Using External LED (5Volt)

7.2 Recommended Typical Application using Internal LED Control (5Volt)

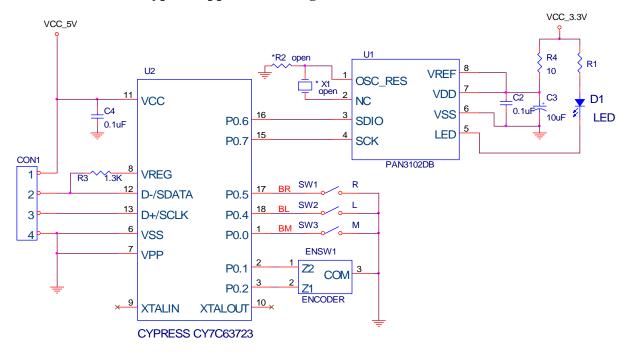


Note:

- 1. *R2 is left for future use
- 2. *X1 is left for PAN3101DB

Figure 13. Application Circuit Using Internal LED (5Volt)

7.3 Recommended Typical Application using Internal LED Control (3.3Volt)



Note:

- 1. *R2 is left for future use
- 2. *X1 is left for PAN3101DB

Figure 14. Application Circuit Using Internal LED (3.3Volt)

7.4 PCB Layout Consideration

- Caps for pins 7, 8 must have trace lengths less than **5 mm**.
- The trace lengths of OSC RES, NC must less than **6 mm**.

7.5 Recommended Value for R1

 Radiometric intensity of LED Bin limits (mW/Sr at 20 mA)

LED Bin Grade	Min.	Тур.	Max.
N	14.7		17.7
P	17.7		21.2
Q	21.2		25.4

Note: Tolerance for each bin will be $\pm 15\%$

7.5.1 Application for 5V

• R1 value (ohm) for external LED control, VDD = 5.0V (refer to Figure 12)

LED Bin Grade	Min.	Тур.	Max.
N	27	36	
P	27	36	
Q	27	36	

• R1 value (ohm) for internal LED control, VDD = 5.0V (refer to Figure 13)

LED Bin Grade	Min.	Тур.	Max.
N	47	100	
P	47	100	
Q	47	100	

7.5.2 Application for 3.3V

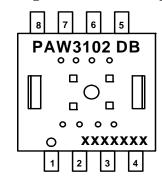
• R1 value (ohm) for internal LED control, VDD = VREF = VDD_LED = 3.3V (refer to Figure 14)

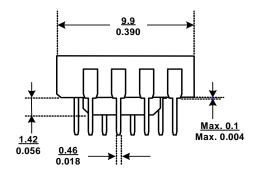
LED Bin Grade	Min.	Тур.	Max.
N	22	100	
P	22	100	
Q	22	100	

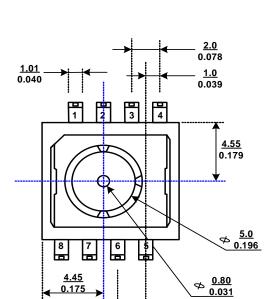
21

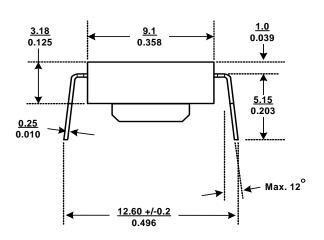
8. Package Information

8.1 Package Outline Drawing







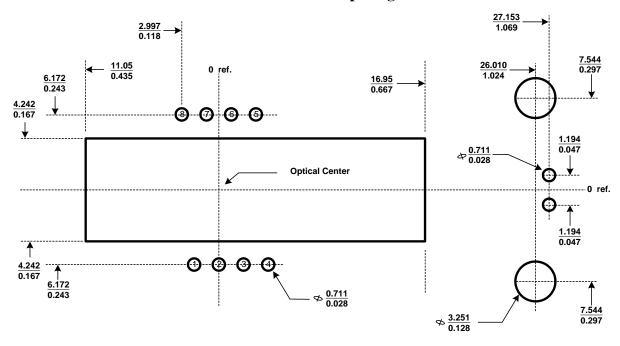


NOTES:

- 1. All dimensions in MM/INCH.
- 2. All dimensions tolerance: +/- 0.10mm
- 3. Maxmumflash: +0.2mm
- 4. Angular tolerance: +/- 3.0degress

2.0 0.078 Figure 15. Package Outline Drawing

8.2 Recommended PCB Mechanical Cutouts and Spacing



All Dimensions: mm / inch

Figure 16. Recommended PCB Mechanical Cutouts and Spacing

9. Update History

Version	Update	Date	
V1.0	Creation, Preliminary 1 st version	09/12/2006	
	3.2 Register Descriptions.		
	4.3 AC Operating Condition		
V1.1	5 Z and 2D/3D Assembly	07/27/2007	
V 1.1	6.4 Power Down Mode	07/27/2007	
	7.3 PCB Layout Consideration		
	7.4 Recommended Value for R1		
V3.0	Content revise	10/23/2008	
V4.0	Add 3.3Volt work range and relative information	01/16/2009	
V4.1	Added patent announcement per page-end	03/03/2010	

Note: The Part No. of the Mouse Product with Prefix "PAN" shall NOT be made, sold, offered to sell, imported or used in or into USA, Canada, Japan and EU. For "PAN", PixArt has only gained territory-limited patent license from Avago. Avago reserve right to take legal action against our customers who fails to comply the above term. PLEASE NOTE THAT PixArt will NOT defend, indemnify, or provide any assistance to our customers who fail to comply the term. IF YOU DO NOT AGREE THE TERM, PIXART WILL NOT DELIVER "PAN" PRODUCTS TO YOU.