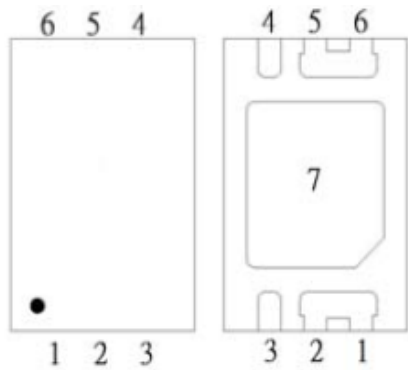


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Dual N-Channel Enhancement Mode MOSFET

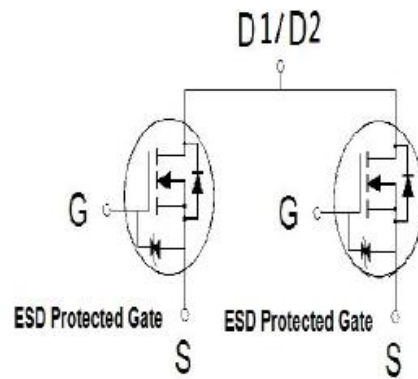
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
20V	9.5m Ω @ $V_{GS} = 4.5V$	12A



TDFN 2X3-6

1,2:S1
3:G1
4:G2
5,6:S2
7:D1/D2



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	± 10	V
Continuous Drain Current	$T_A = 25\text{ }^{\circ}\text{C}$	I_D	12	A
	$T_A = 70\text{ }^{\circ}\text{C}$		9.4	
Pulsed Drain Current ¹		I_{DM}	42	
Avalanche Current		I_{AS}	23	
Avalanche Energy ³		E_{AS}	26	mJ
Power Dissipation	$T_A = 25\text{ }^{\circ}\text{C}$	P_D	2.4	W
	$T_A = 70\text{ }^{\circ}\text{C}$		1.5	
Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		52	$^{\circ}\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}\text{C}$.

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Dual N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	20			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.4	0.66	1	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 8V$			± 10	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16V, V_{GS} = 0V$			1	μA
		$V_{DS} = 10V, V_{GS} = 0V, T_J = 125^{\circ}C$			10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 3A$	6.1	7.8	9.5	m Ω
		$V_{GS} = 3.8V, I_D = 3A$	6.4	8.2	10	
		$V_{GS} = 3.1V, I_D = 3A$	6.7	8.6	10.5	
		$V_{GS} = 2.5V, I_D = 3A$	7.3	9.4	11.5	
		$V_{GS} = 1.8V, I_D = 3A$	8	12	16	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 3A$		32		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 10V, f = 1MHz$		1553		pF
Output Capacitance	C_{oss}			216		
Reverse Transfer Capacitance	C_{rss}			166		
Total Gate Charge ²	Q_g	$V_{DS} = 10V, I_D = 3A$ $V_{GS} = 4.5V$		18.3		nC
Gate-Source Charge ²	Q_{gs}			1.6		
Gate-Drain Charge ²	Q_{gd}			4.6		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 10V$ $I_D \cong 3A, V_{GS} = 4.5V, R_{GS} = 6\Omega$		38		nS
Rise Time ²	t_r			42		
Turn-Off Delay Time ²	$t_{d(off)}$			60		
Fall Time ²	t_f			25		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^{\circ}C$)						
Continuous Current	I_S				2	A
Forward Voltage ¹	V_{SD}	$I_F = 3A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 3A, dI_F/dt = 100A / \mu S$		20		nS
Reverse Recovery Charge	Q_{rr}			9		nC

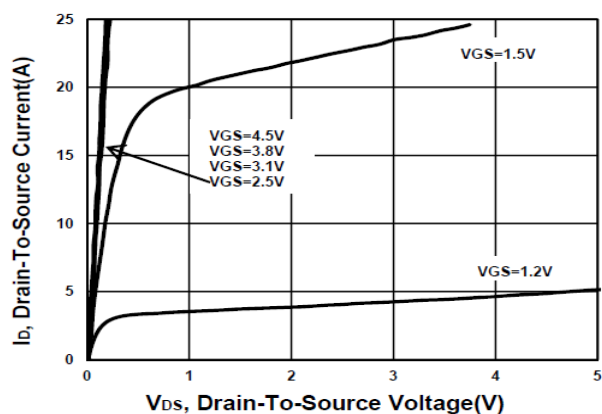
¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

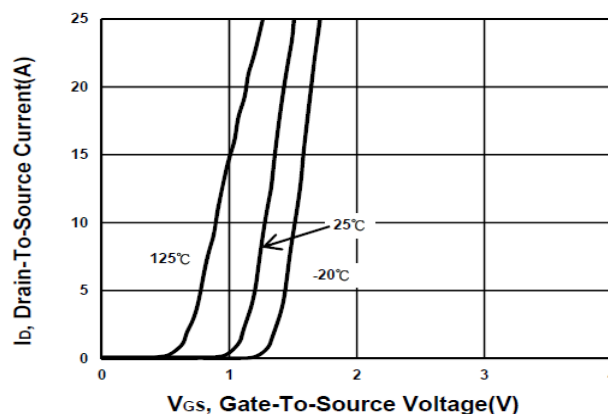
PB5G2JU

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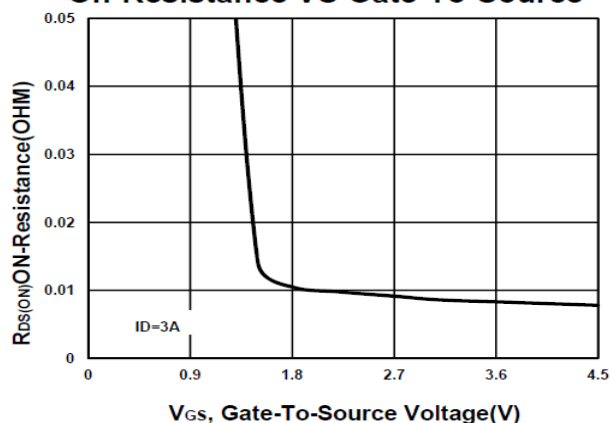
Output Characteristics



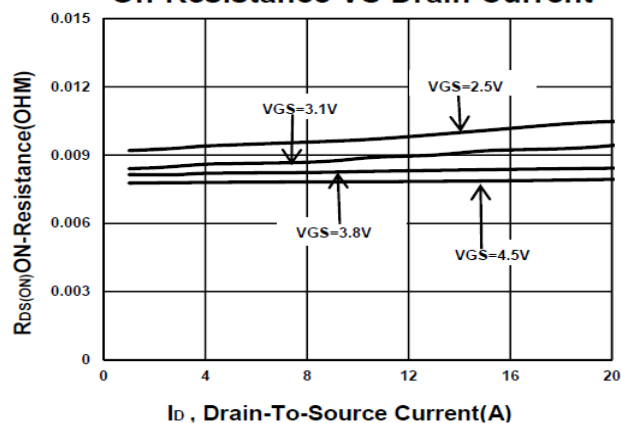
Transfer Characteristics



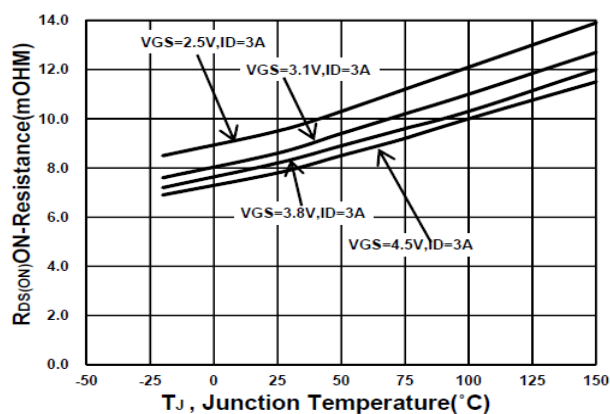
On-Resistance VS Gate-To-Source



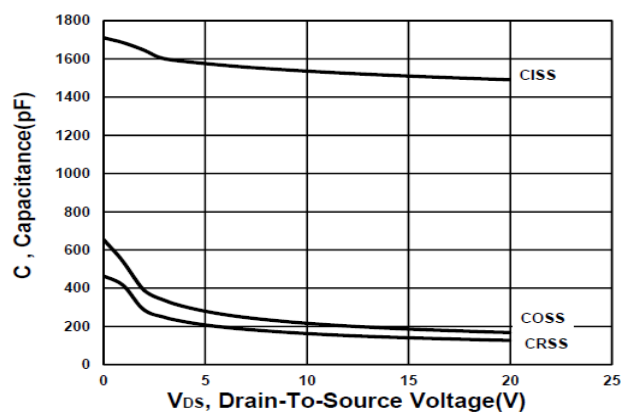
On-Resistance VS Drain Current



On-Resistance VS Temperature



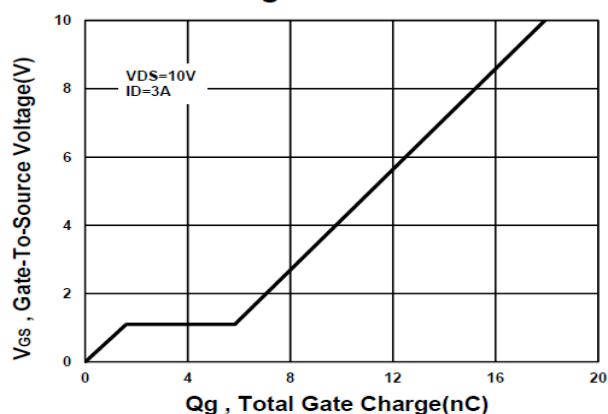
Capacitance Characteristic



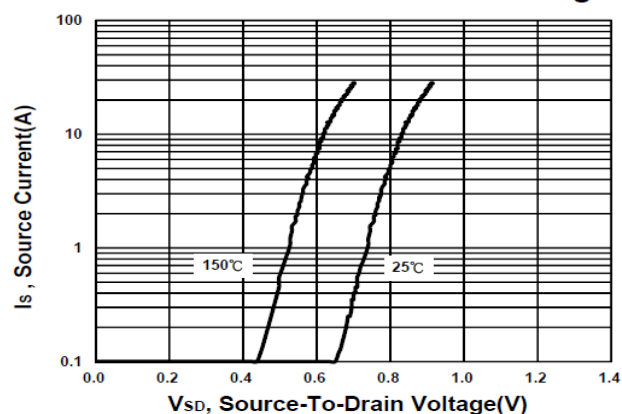
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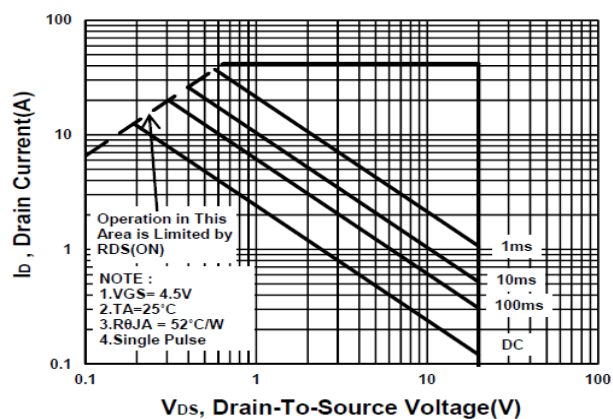
Gate charge Characteristics



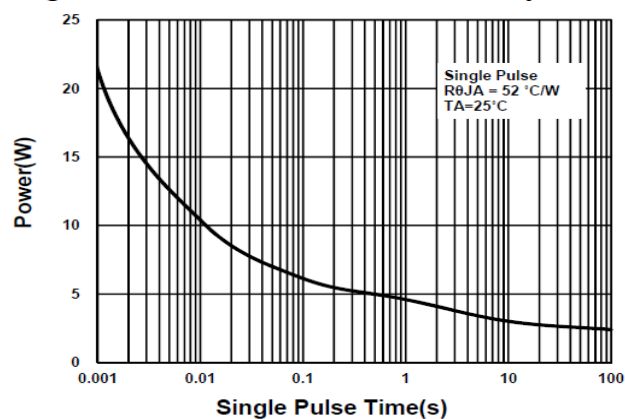
Source-Drain Diode Forward Voltage



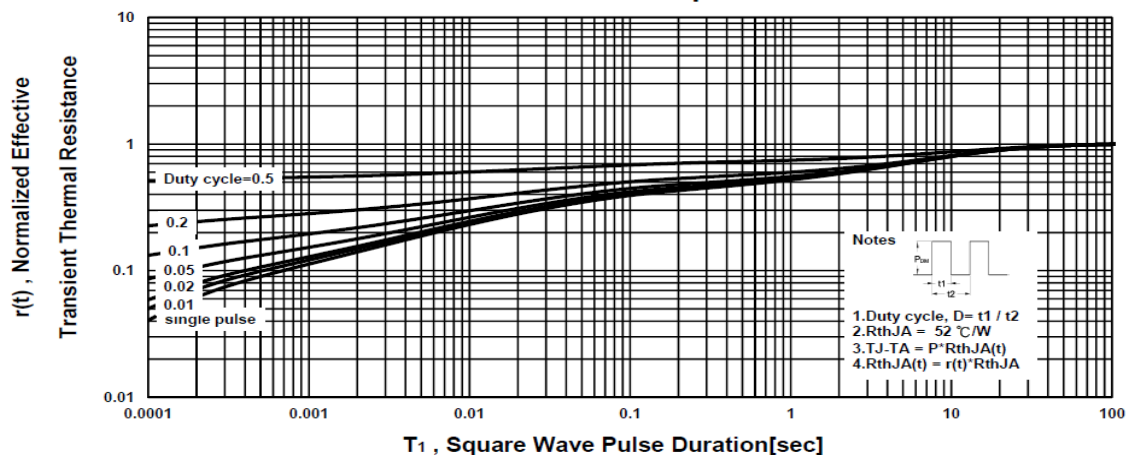
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



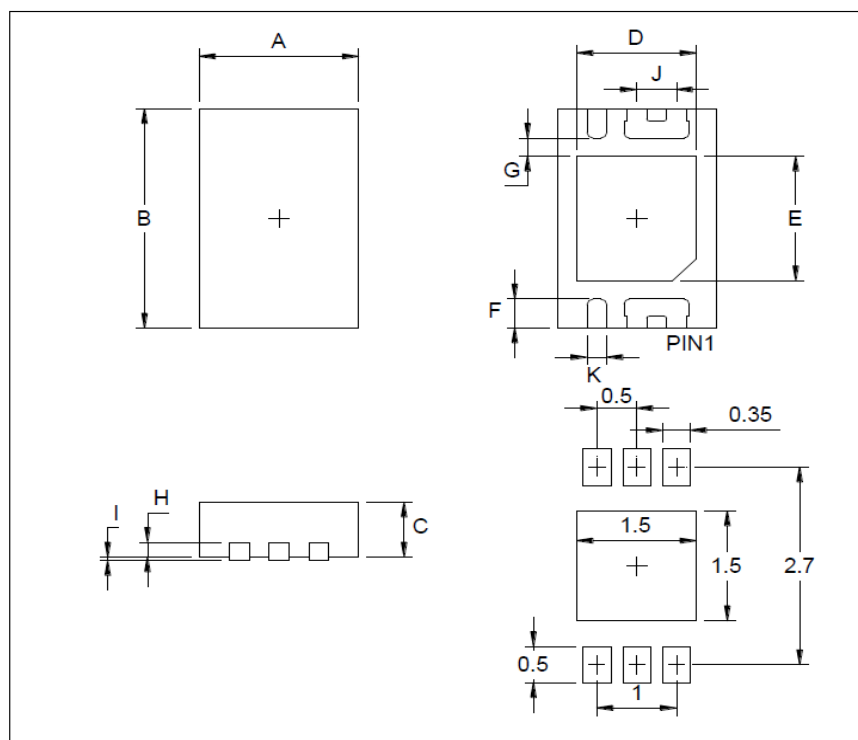
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Dual N-Channel Enhancement Mode MOSFET

Package Dimension

TDFN 2x3-6 MECHANICAL DATA

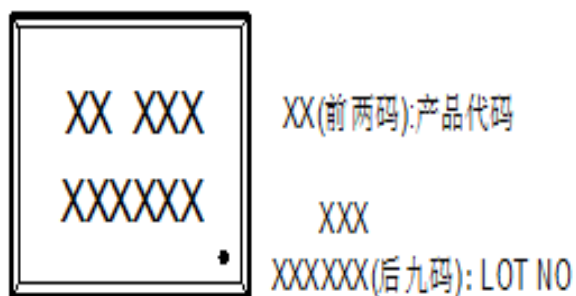
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	1.95	2.00	2.05	H	0.195	0.203	0.211
B	2.95	3.00	3.05	I	0		0.05
C	0.70	0.75	0.8	J		0.50	
D	1.45	1.50	1.55	K	0.20	0.25	0.30
E	1.65	1.70	1.75				
F	0.35	0.40	0.45				
G	0.25						



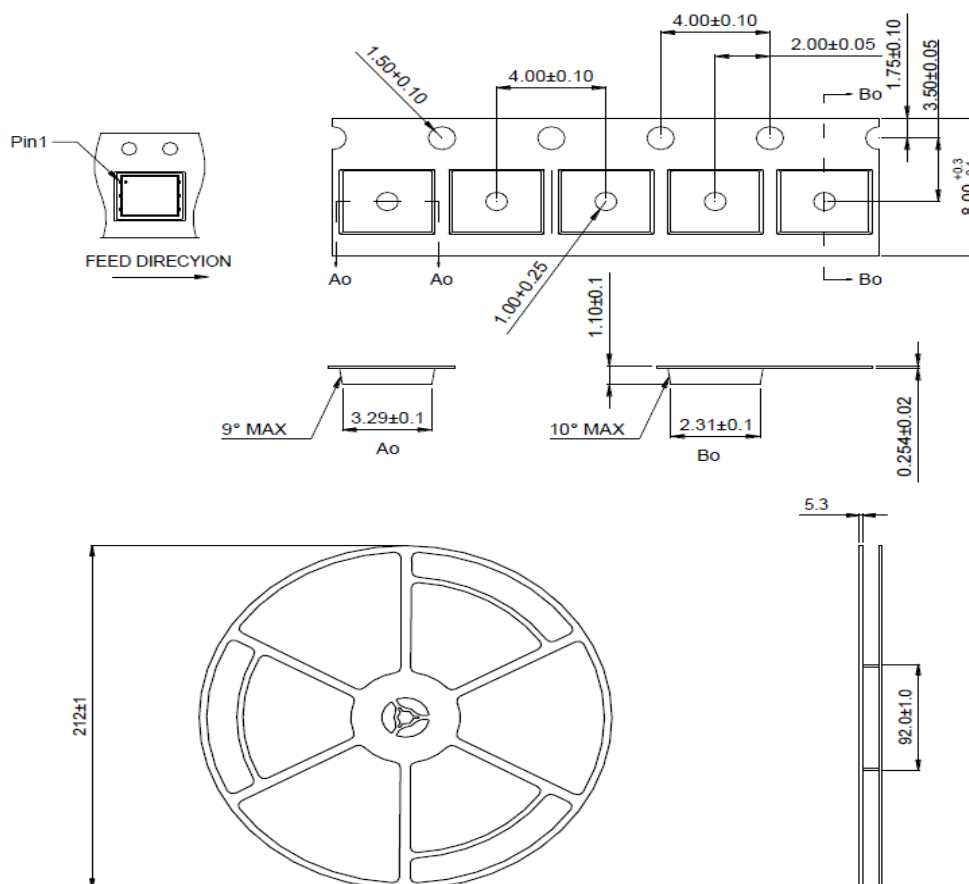
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Dual N-Channel Enhancement Mode MOSFET

A. Marking Information (此产品代码为：D8)



B. Tape&Reel Information:3000pcs/Reel



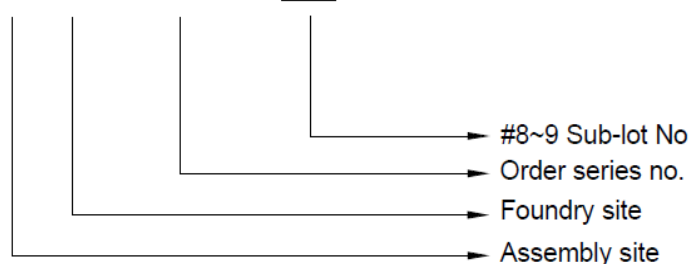
PB5G2JU

Dual N-Channel Enhancement Mode MOSFET

C. Lot.No. & Date Code rule

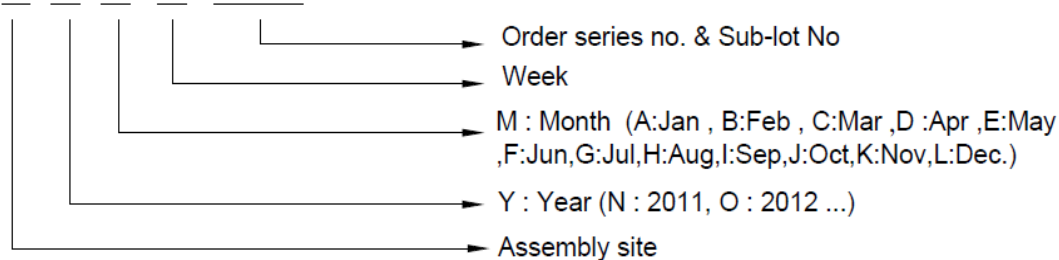
1.LOT.NO.

M N 15M21 03



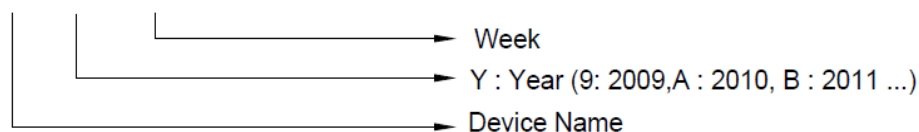
2.Date Code

D Y M X XXX



3.Date Code (for Small package)

XX Y WW



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Dual N-Channel Enhancement Mode MOSFET

D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文"0"和数字"0", "G"和"Q"的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert " / " between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least