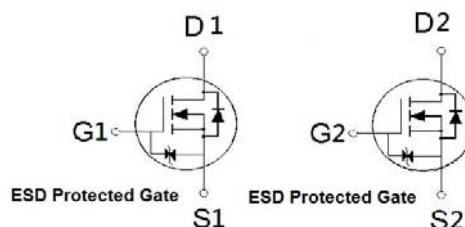


PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
20V	35mΩ	5A

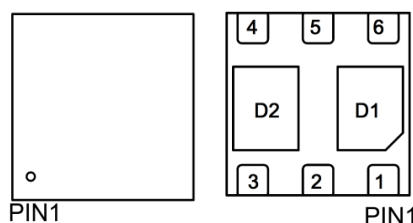


Features

- Pb-Free, Halogen Free and RoHS compliant.
- Low $R_{DS(on)}$ to Minimize Conduction Losses.
- Ohmic Region Good $R_{DS(on)}$ Ratio.
- Optimized Gate Charge to Minimize Switching Losses.
- Products Integrated ESD diode with ESD Protected up to 2KV.

Applications

- Protection Circuits Applications.
- Logic/Load Switch Circuits Applications.



- 1 : S1. 4 : S2.
- 2 : G1. 5 : G2.
- 3 : D2. 6 : D1.

100% RG Test , 100% UIL Test

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current	$T_A = 25\text{ }^\circ\text{C}$	I_D	5	A
	$T_A = 70\text{ }^\circ\text{C}$		3.9	
Pulsed Drain Current ¹		I_{DM}	20	
Power Dissipation	$T_A = 25\text{ }^\circ\text{C}$	P_D	1.4	W
	$T_A = 70\text{ }^\circ\text{C}$		0.9	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		86	°C/W

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Coppe.

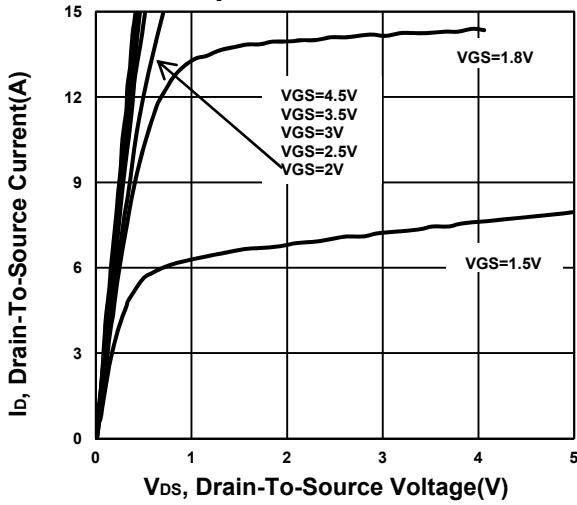
ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT		
			MIN	TYP	MAX			
STATIC								
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	20			V		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.5	0.7	1			
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±8V			±10	μA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 16V, V _{GS} = 0V			1	μA		
		V _{DS} = 10V, V _{GS} = 0V, T _J = 55 °C			10			
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 5A		22	35	mΩ		
		V _{GS} = 2.5V, I _D = 4.5A		27	38			
		V _{GS} = 1.8V, I _D = 2A		35	55			
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 5A		30		S		
DYNAMIC								
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 10V, f = 1MHz		506		pF		
Output Capacitance	C _{oss}			82				
Reverse Transfer Capacitance	C _{rss}			66				
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.8		Ω		
Total Gate Charge ²	Q _{g(VGS=4.5V)}	V _{DS} = 10V, I _D = 5A		7		nC		
	Q _{g(VGS=2.5V)}			4.3				
Gate-Source Charge ²	Q _{gs}			0.6				
Gate-Drain Charge ²	Q _{gd}			2.3				
Turn-On Delay Time ²	t _{d(on)}		V _{DD} = 10V I _D ≅ 5A, V _{GEN} = 4.5V, R _G = 6Ω		12			nS
Rise Time ²	t _r				35			
Turn-Off Delay Time ²	t _{d(off)}				24			
Fall Time ²	t _f			16				
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)								
Continuous Current	I _S				1.4	A		
Forward Voltage ¹	V _{SD}	I _F = 5A, V _{GS} = 0V			1	V		
Reverse Recovery Time	t _{rr}	I _F = 5A, dI _F /dt = 100A / μS		8.8		nS		
Reverse Recovery Charge	Q _{rr}			1.4		nC		

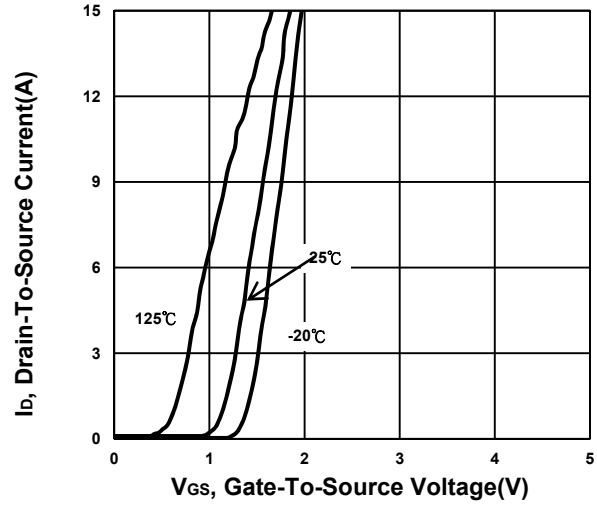
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

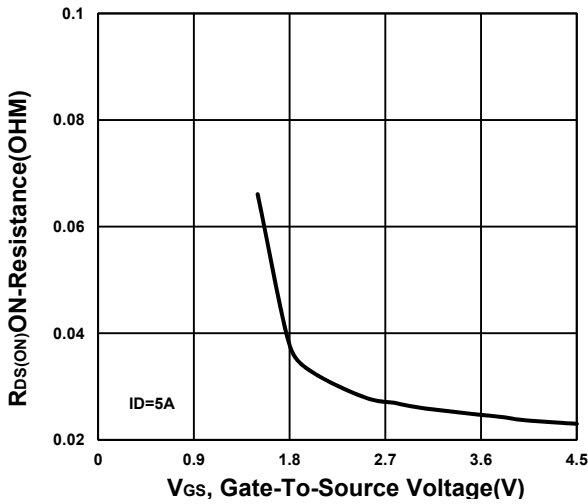
Output Characteristics



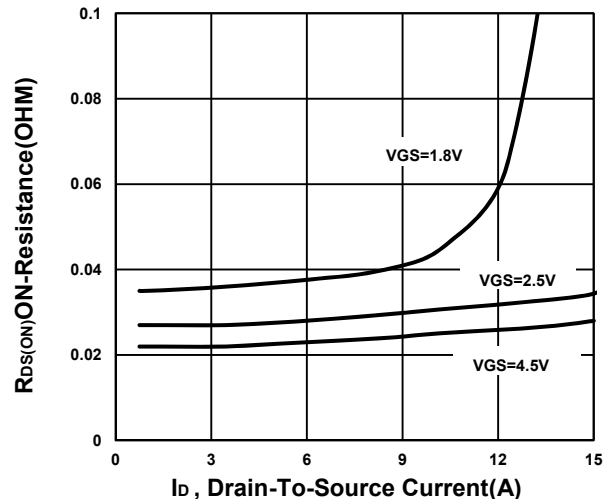
Transfer Characteristics



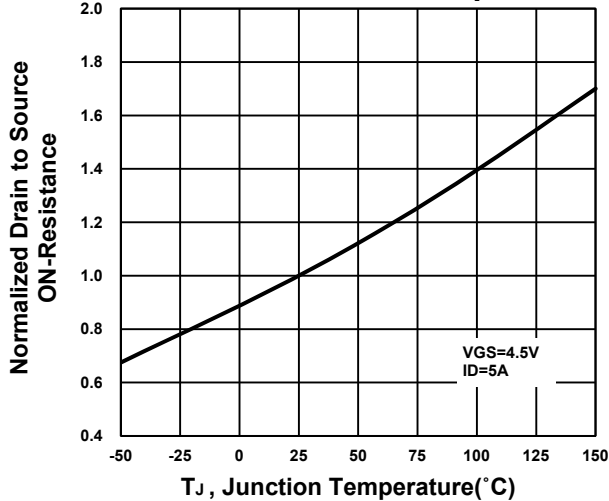
On-Resistance VS Gate-To-Source



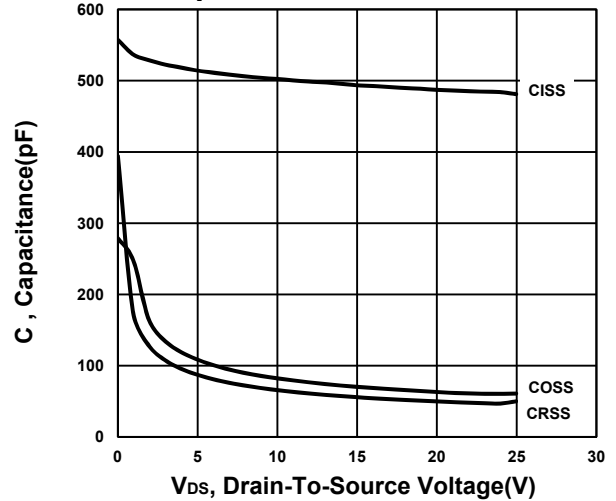
On-Resistance VS Drain Current



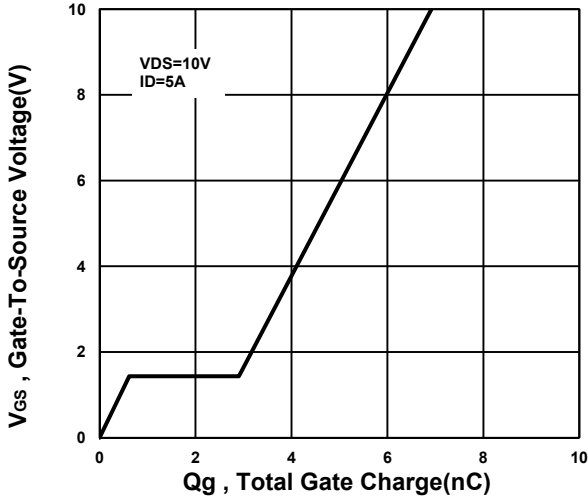
On-Resistance VS Temperature



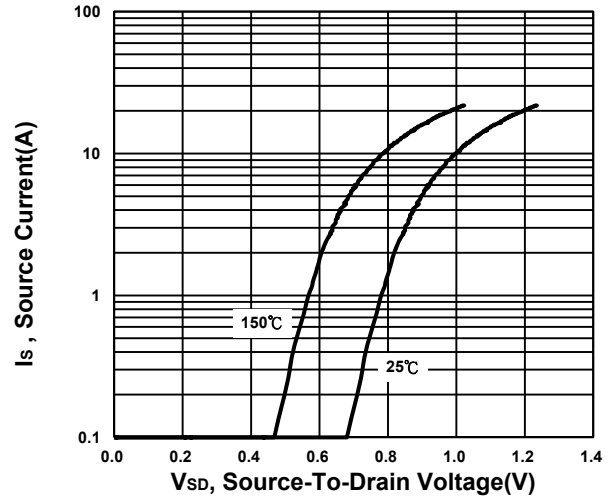
Capacitance Characteristic



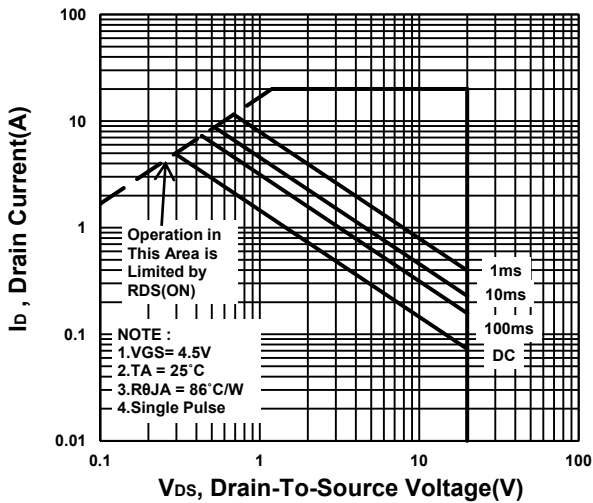
Gate charge Characteristics



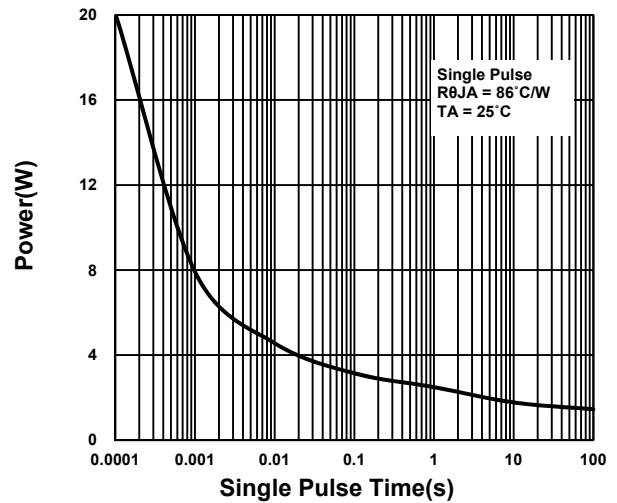
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

