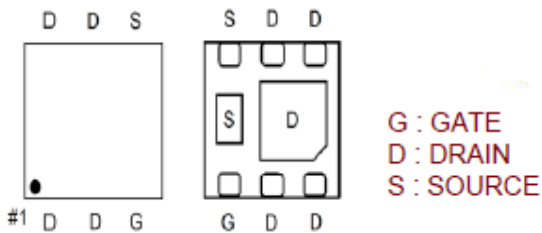


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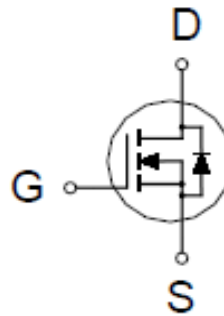
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	16mΩ @ $V_{GS} = 10V$	7.8A



PDFN 2X2S



100%RG TEST
100%UIL TEST

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	$T_A = 25\text{ °C}$	I_D	7.8	A
	$T_A = 70\text{ °C}$		6.2	
Pulsed Drain Current ¹		I_{DM}	21	
Avalanche Current		I_{AS}	12.8	
Avalanche Energy	L = 0.1 mH	E_{AS}	8.2	mJ
Power Dissipation	$T_A = 25\text{ °C}$	P_D	1.5	W
	$T_A = 70\text{ °C}$		1	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		80	°C / W

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Coppe.

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N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

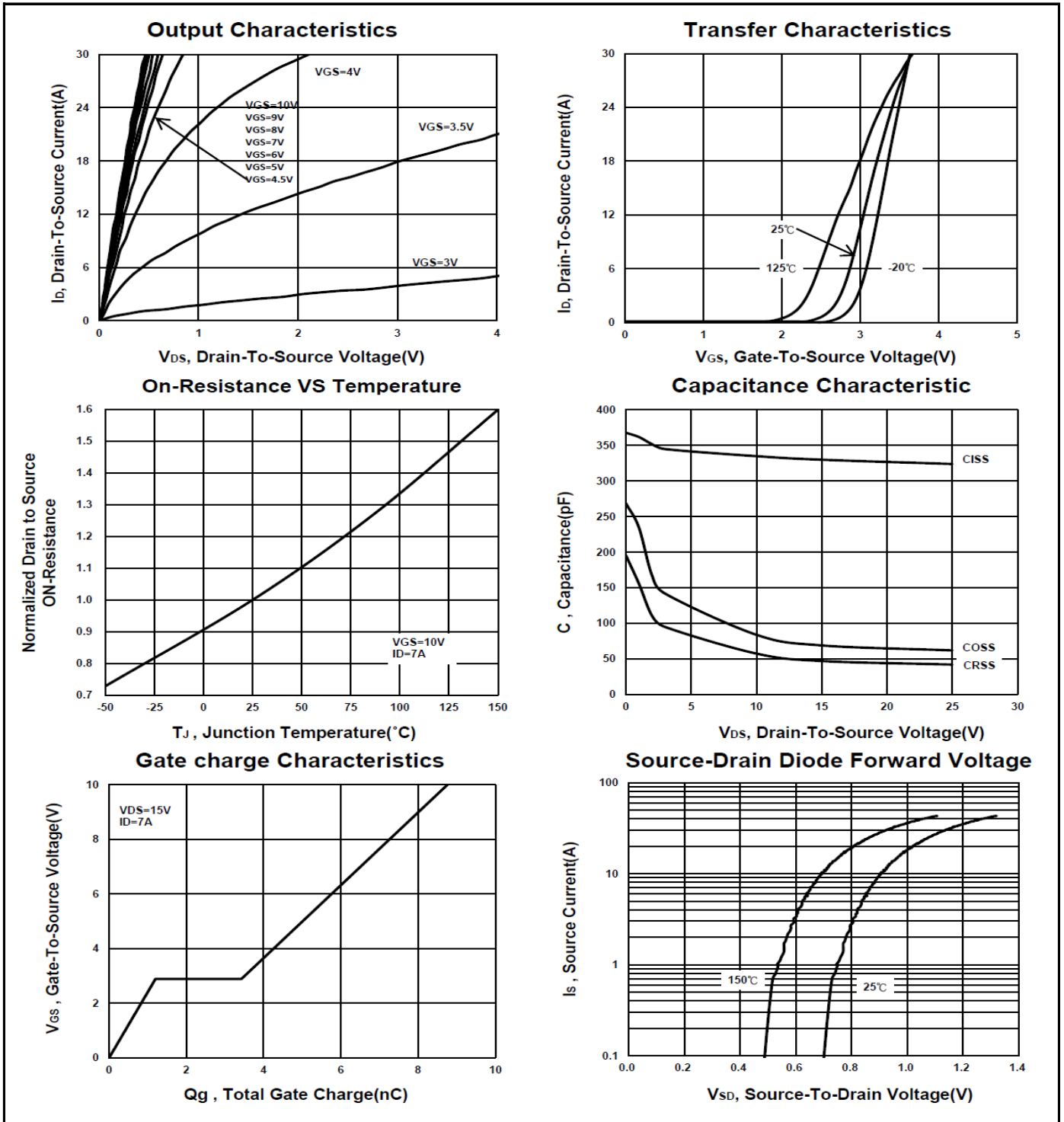
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.3	1.75	2.3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	μA
		V _{DS} = 20V, V _{GS} = 0V, T _J = 55°C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 6A		19	24	mΩ
		V _{GS} = 10V, I _D = 7A		13	16	
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 7A		31		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		329		pF
Output Capacitance	C _{oss}			68		
Reverse Transfer Capacitance	C _{rss}			48		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		2.6		Ω
Total Gate Charge ²	Q _g (V _{GS} =10V)	V _{DS} = 15V, I _D = 7A		8.1		nC
	Q _g (V _{GS} =4.5V)			4.5		
Gate-Source Charge ²	Q _{gs}			1.1		
Gate-Drain Charge ²	Q _{gd}			2.4		
Turn-On Delay Time ²	t _{d(on)}		V _{DD} = 15V I _D ≅ 7A, V _{GEN} = 10V, R _G = 6Ω		17	
Rise Time ²	t _r			17		
Turn-Off Delay Time ²	t _{d(off)}			37		
Fall Time ²	t _f			18		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				1.5	A
Forward Voltage ¹	V _{SD}	I _F = 7A, V _{GS} = 0V			1	V
Reverse Recovery Time	t _{rr}	I _F = 7A, di _F /dt = 100A / μS		9.6		nS
Reverse Recovery Charge	Q _{rr}			2.9		nC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

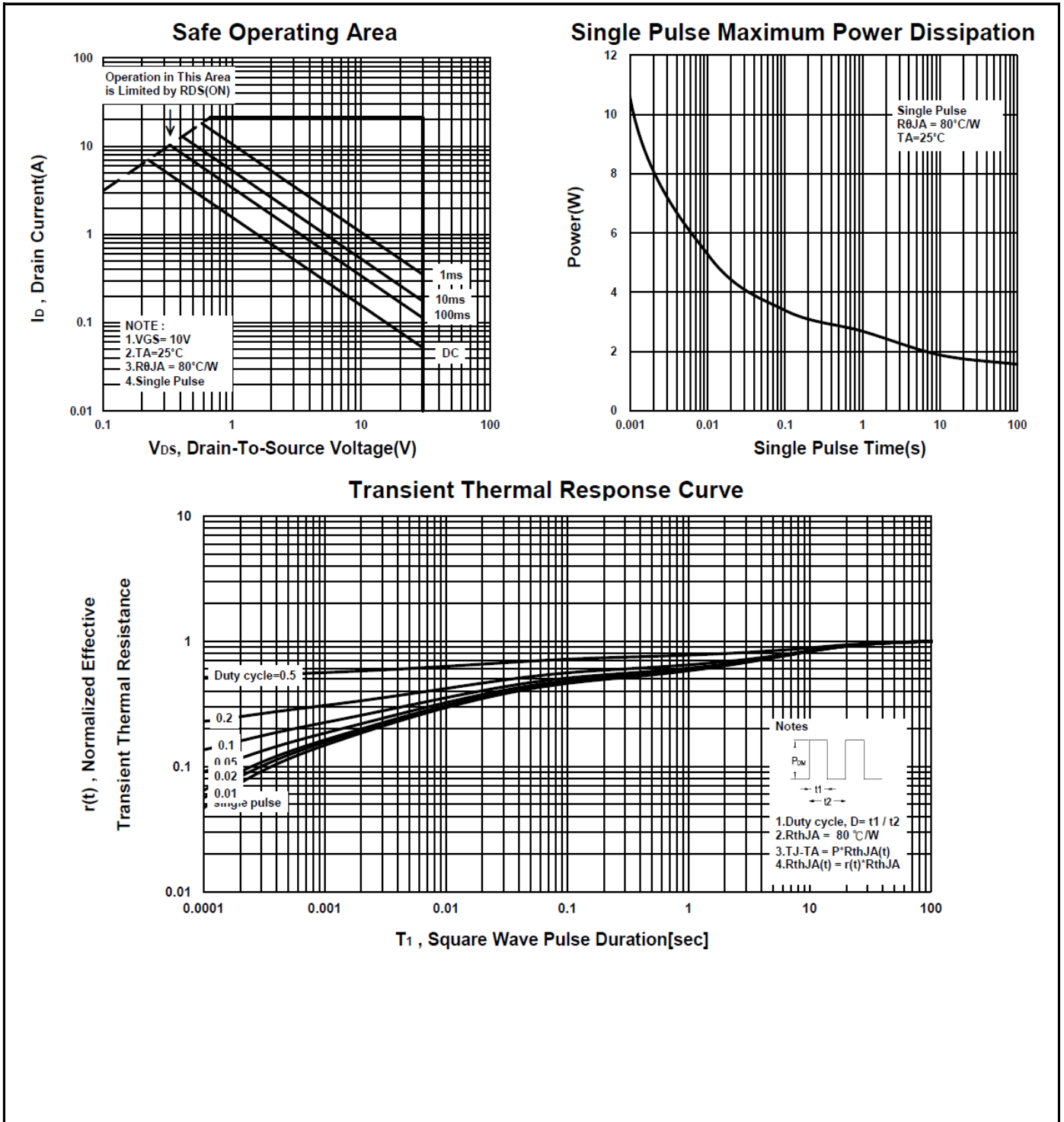
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Package Dimension

PDFN 2x2S MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	1.9		2.1	H	0.8		1.1
B	1.9		2.1	I	0		0.05
C	0.55	0.65	0.75	J		0.203	
D	0.85		1.1	K	0.7		0.8
E	0.174	0.25	0.326	L	0.2		0.4
F	0.25		0.35	M	0.46		0.85
G		0.2					

