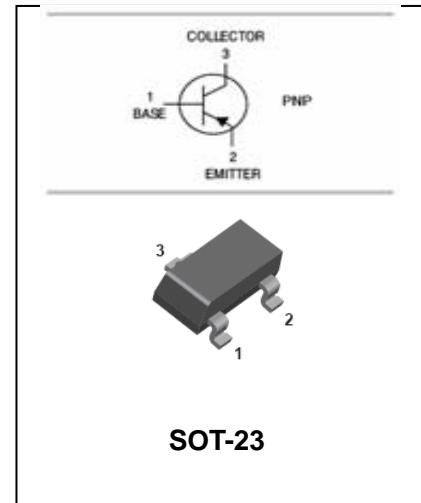


## PNP Low $V_{CEsat}$ (BISS) Transistor

## PBSS5160T

### FEATURES

- Low collector-emitter saturation voltage  $V_{CEsat}$
- High collector current capability  $I_C$  and  $I_{CM}$
- High efficiency, reduces heat generation
- Reduces printed-circuit board area required



### APPLICATIONS

- Major application segments
- Power management
- Peripheral driver

### ORDERING INFORMATION

Type No.	Marking	Package Code
PBSS5160T	U6	SOT-23

### MAXIMUM RATING @ $T_a=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	-80	V
$V_{CEO}$	Collector-Emitter Voltage	-60	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current -Continuous	-0.9 (note 1) -1 (note 2)	A
$I_{CM}$	peak collector current ( $t = 1 \text{ ms}$ or limited by $T_{J(max)}$ )	-2	A
$I_B$	base current (DC)	-300	mA
$I_{BM}$	peak base current ( $t_p \leq 300 \text{ ms}$ ; $\delta \leq 0.02$ )	-1	A
$P_C$	Collector Dissipation $T_{amb} \leq 25^\circ\text{C}$ ; note 1 $T_{amb} \leq 25^\circ\text{C}$ ; note 2 $T_{amb} \leq 25^\circ\text{C}$ ; notes 1 and 3	270 400 1.25	mW mW W
$T_{stg}, T_{amb}$	Storage and operating ambient temperature	-65 to +150	$^\circ\text{C}$
$T_J$	Junction temperature	150	$^\circ\text{C}$

#### Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, tin-plated, standard



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footprint.

2. Device mounted on an FR4 printed-circuit board, single-sided copper, tin-plated, 1 cm<sup>2</sup> collector mounting pad.
3. Operated under pulsed conditions: duty cycle  $\delta \leq 20\%$ , pulse width  $t_p \leq 10$  ms.

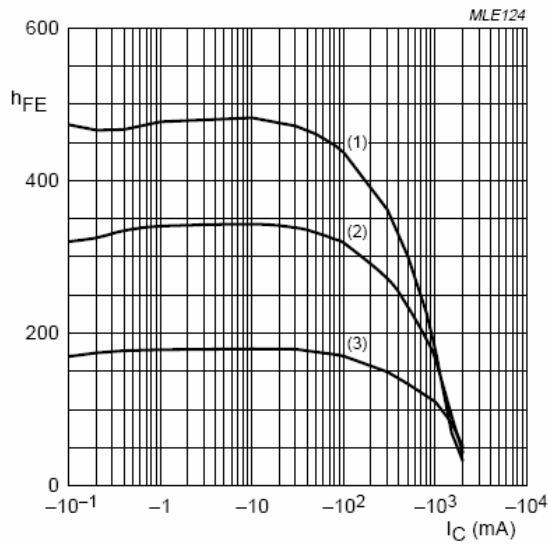
**ELECTRICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified**

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C=100\mu A, I_E=0$	-80			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C=10mA, I_B=0$	-60			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E=100\mu A, I_C=0$	-5			V
Collector cut-off current	$I_{CBO}$	$V_{CB}=-60V, I_E=0$			-0.1	$\mu A$
collector-emitter cut-off current	$I_{CES}$	$V_{CE}=-60V, I_E=0$			-0.1	$\mu A$
Emitter cut-off current	$I_{EBO}$	$V_{EB}=-5V, I_C=0$			-0.1	$\mu A$
DC current gain	$h_{FE}$	$V_{CE}=-5V, I_C=-1mA$	200	350		
		$V_{CE}=-5V, I_C=-500mA$	150	250		
		$V_{CE}=-5V, I_C=-1A$	100	160		
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=-100mA, I_B=-1mA$ $I_C=-500mA, I_B=-50mA$ $I_C=-1A, I_B=-100mA$		-110 -120 -220	-160 -175 -330	mV
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C=-1A, I_B=-50mA$		-0.95	-1.1	V
Base-emitter voltage	$V_{BE(on)}$	$I_C=-1A, V_{CE}=-5V$		-0.82	-0.9	V
Transition frequency	$f_T$	$V_{CE}=-10V, I_C=-50mA$ $f=100MHz$	150	220		MHz
Collector capacitance	$C_C$	$V_{CB}=-10V, f=1MHz$		9	15	pF

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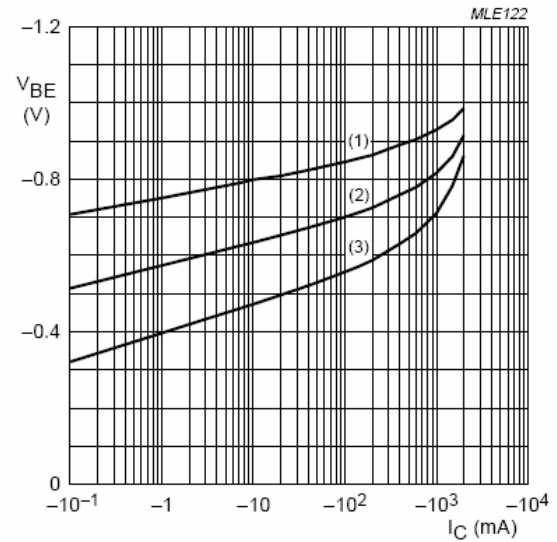
**PBSS5160T**

**TYPICAL CHARACTERISTICS @  $T_a=25^\circ\text{C}$  unless otherwise specified**



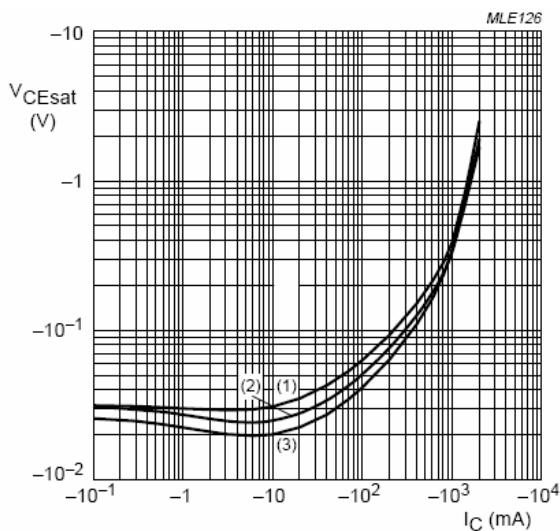
$V_{CE} = -5\text{ V.}$   
 (1)  $T_{amb} = 100^\circ\text{C.}$   
 (2)  $T_{amb} = 25^\circ\text{C.}$   
 (3)  $T_{amb} = -55^\circ\text{C.}$

**Fig.1 DC current gain as a function of collector current; typical values.**



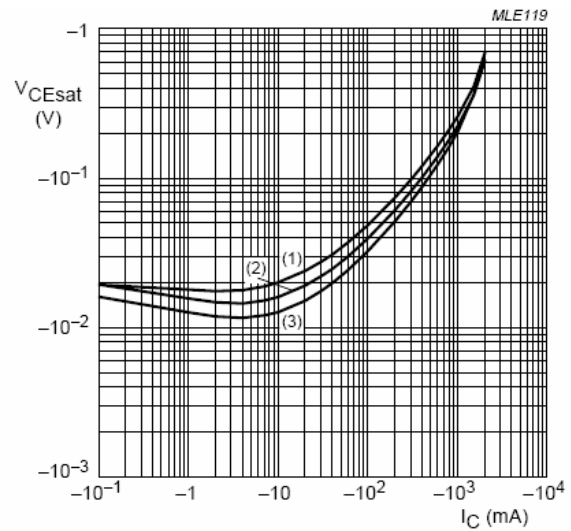
$V_{CE} = -5\text{ V.}$   
 (1)  $T_{amb} = -55^\circ\text{C.}$   
 (2)  $T_{amb} = 25^\circ\text{C.}$   
 (3)  $T_{amb} = 100^\circ\text{C.}$

**Fig.2 Base-emitter voltage as a function of collector current; typical values.**



$I_C/I_B = 20.$   
 (1)  $T_{amb} = 100^\circ\text{C.}$   
 (2)  $T_{amb} = 25^\circ\text{C.}$   
 (3)  $T_{amb} = -55^\circ\text{C.}$

**Fig.3 Collector-emitter saturation voltage as a function of collector current; typical values.**

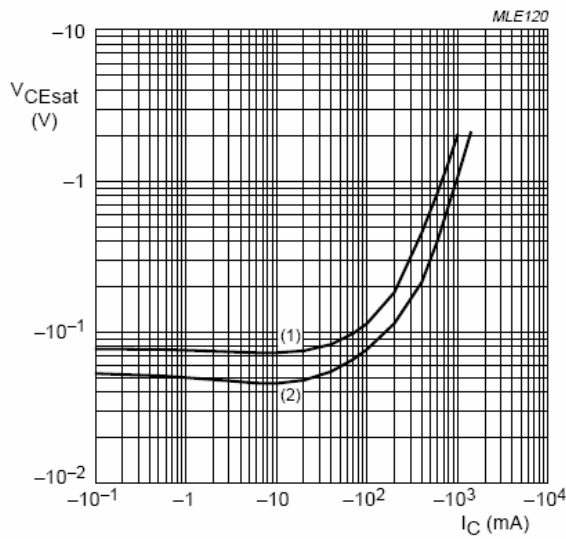


$I_C/I_B = 10.$   
 (1)  $T_{amb} = 100^\circ\text{C.}$   
 (2)  $T_{amb} = 25^\circ\text{C.}$   
 (3)  $T_{amb} = -55^\circ\text{C.}$

**Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values.**

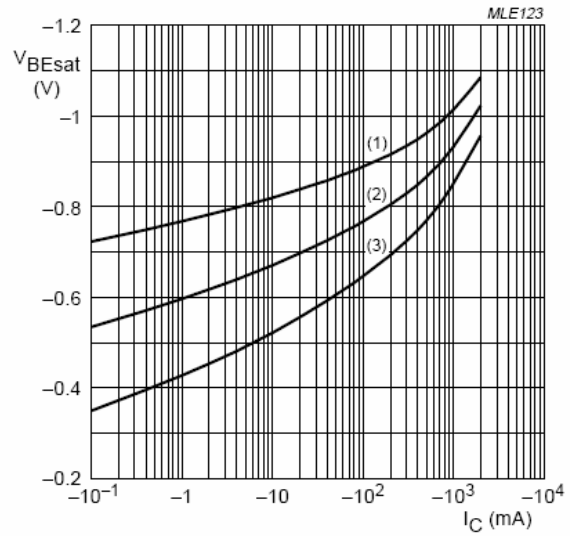
**PNP Low  $V_{CEsat}(BISS)$  Transistor**

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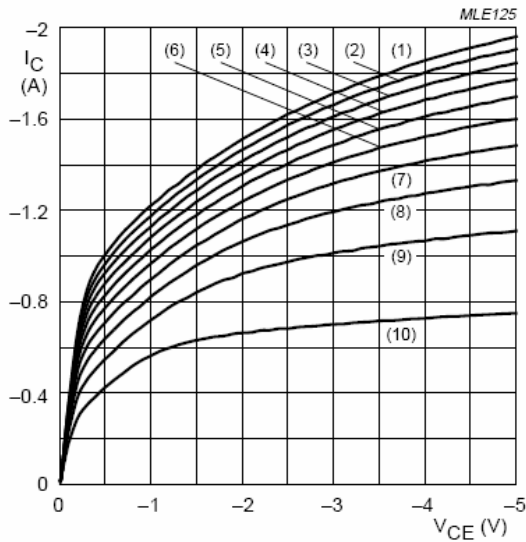
$T_{amb} = 25\text{ }^{\circ}\text{C}$ .  
(1)  $I_C/I_B = 100$ .  
(2)  $I_C/I_B = 50$ .

Fig. 5 Collector-emitter saturation voltage as a function of collector current; typical values.



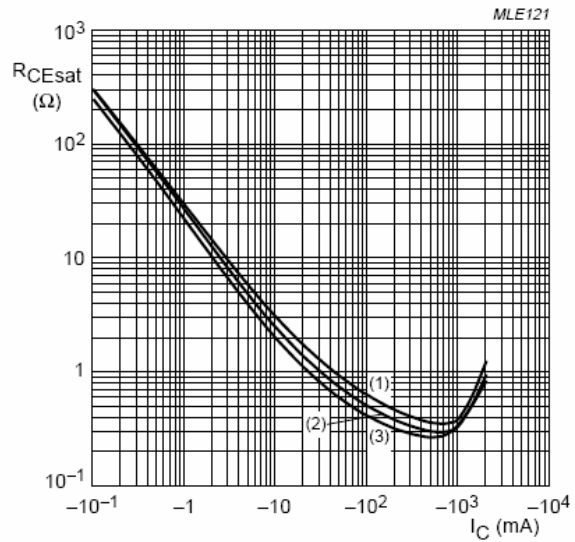
$I_C/I_B = 20$ .  
(1)  $T_{amb} = -55\text{ }^{\circ}\text{C}$ .  
(2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$ .  
(3)  $T_{amb} = 100\text{ }^{\circ}\text{C}$ .

Fig. 6 Base-emitter saturation voltage as a function of collector current; typical values.



$T_{amb} = 25\text{ }^{\circ}\text{C}$ .  
(1)  $I_B = -40\text{ mA}$ . (5)  $I_B = -24\text{ mA}$ . (9)  $I_B = -8\text{ mA}$ .  
(2)  $I_B = -36\text{ mA}$ . (6)  $I_B = -20\text{ mA}$ . (10)  $I_B = -4\text{ mA}$ .  
(3)  $I_B = -32\text{ mA}$ . (7)  $I_B = -16\text{ mA}$ .  
(4)  $I_B = -28\text{ mA}$ . (8)  $I_B = -12\text{ mA}$ .

Fig. 7 Collector current as a function of collector-emitter voltage; typical values.



$I_C/I_B = 20$ .  
(1)  $T_{amb} = 100\text{ }^{\circ}\text{C}$ . (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$ . (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$ .

Fig. 8 Equivalent on-resistance as a function of collector current; typical values.

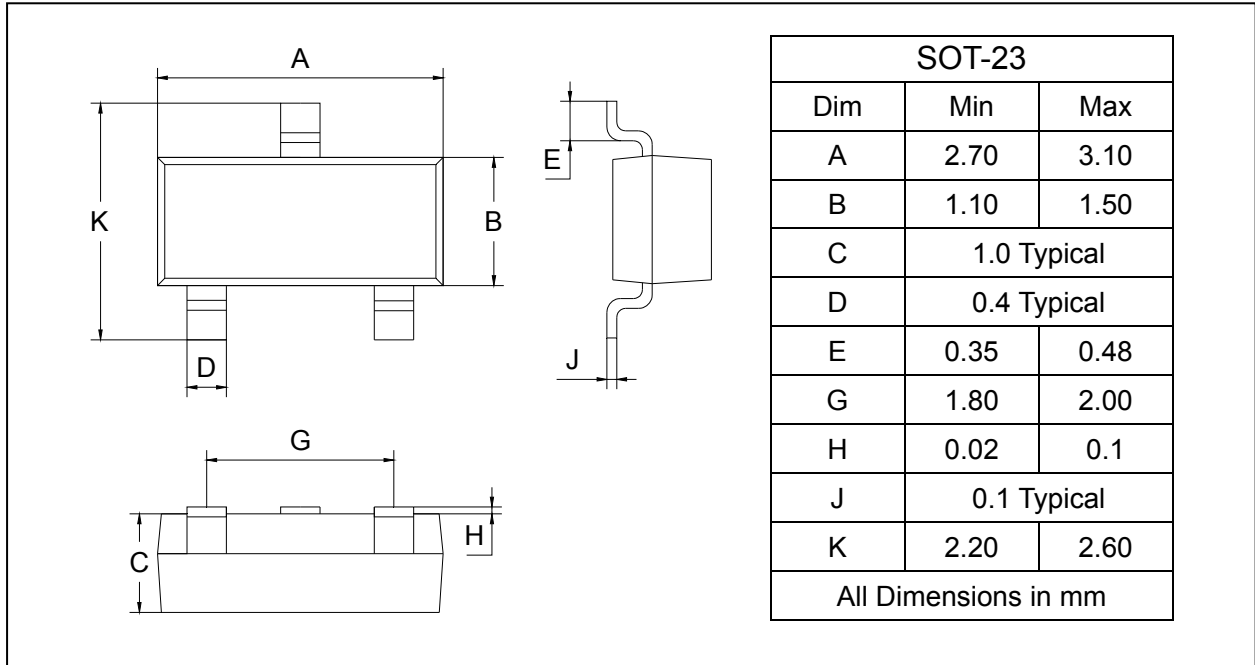
**PNP Low  $V_{CEsat}$ (BISS) Transistor**

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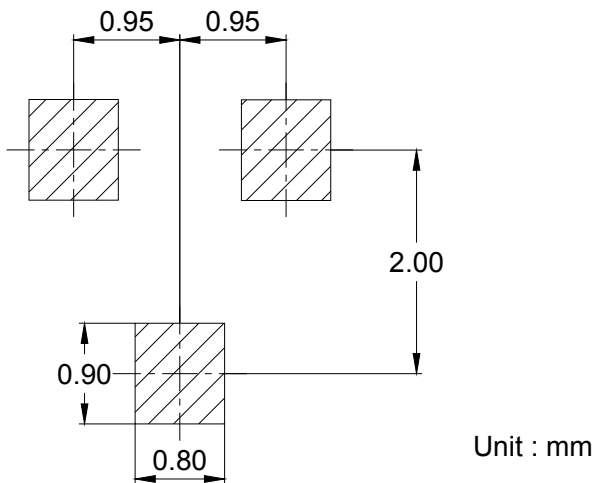
**PACKAGE OUTLINE**

Plastic surface mounted package

SOT-23



**SOLDERING FOOTPRINT**



**PACKAGE INFORMATION**

Device	Package	Shipping
PBSS5160T	SOT-23	3000/Tape&Reel