

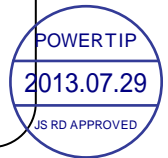


SPECIFICATIONS

CUSTOMER	:	HCN040
SAMPLE CODE	:	SC1601LRS-AWA-B-Q
MASS PRODUCTION CODE	:	PC1601LRS-AWA-B-Q
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	001
DRAWING NO. (Ver.)	:	JLMD-PC1601LRS-AWA-B-Q_001
PACKAGING NO. (Ver.)	:	JPKG-PC1601LRS-AWA-B-Q_001

Customer Approved

Date:



Approved	Checked	Designer
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- Preliminary specification for design input
- Specification for sample approval

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Note : For detailed information please refer to IC data sheet : [ST7066U](#)

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*1 characters
LCD Type	STN,Gray, Transflective, Positive ,Normal Temp.
Driver Condition	LCD Module : 1/16 Duty , 1/4 Bias
Viewing Direction	6 O'clock
Backlight	Yellow-Green LED B/L
Weight	35.8g
Interface	-
Other(controller / driver IC)	ST7066U
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web site : http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	80.0(L) * 36.0(w) * 14.0(H)	mm
Viewing Area	64.5(L) *13.8(w)	mm
Active Area	59.62(L) *6.56(w)	mm
Character Size	0.55(L) *0.75(w)	mm
Character Pitch	0.63(L) *0.83(w)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V _{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	V _{LCD}	-	V _{DD} -10.0	V _{DD} +0.3	V
Input Voltage	V _{IN}	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	-	0	50	°C
Storage Temperature	T _{ST}	-	-20	70	°C
Storage Humidity	H _D	Ta < 40 °C	-	90	%RH

1.4 DC Electrical Characteristics

$V_{DD}=5.0 \pm 0.5V$, $V_{SS}=0V$, $T_a = 25^\circ C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	V_{DD}	-	4.5	5.0	5.5	V
“H” Input Voltage	V_{IH}	-	$0.7 V_{DD}$	-	V_{DD}	V
“L” Input Voltage	V_{IL}	-	-0.3	-	0.6	V
“H” Output Voltage	V_{OH}	$I_{OH}=-0.1mA$	3.9	-	V_{DD}	V
“L” Output Voltage	V_{OL}	$I_{OL}=+0.1mA$	-	-	0.4	V
Supply Current	$I_{DD} *1$	$V_{DD}=5.0V$	-	1.5	3.0	mA
LCM Driver Voltage	$V_{OP} *2$	$0^\circ C$	-	-	-	V
		$25^\circ C$	4.3	4.5	4.7	
		$50^\circ C$				

NOTE: *1 The Maximum current display

*2 The VOP test point is $V_{DD}-V_0$

1.5 Optical Characteristics

LCD Panel : 1/16 Duty , 1/4Bias , $V_{LCD}=4.5V$, $T_a = 25^\circ C$

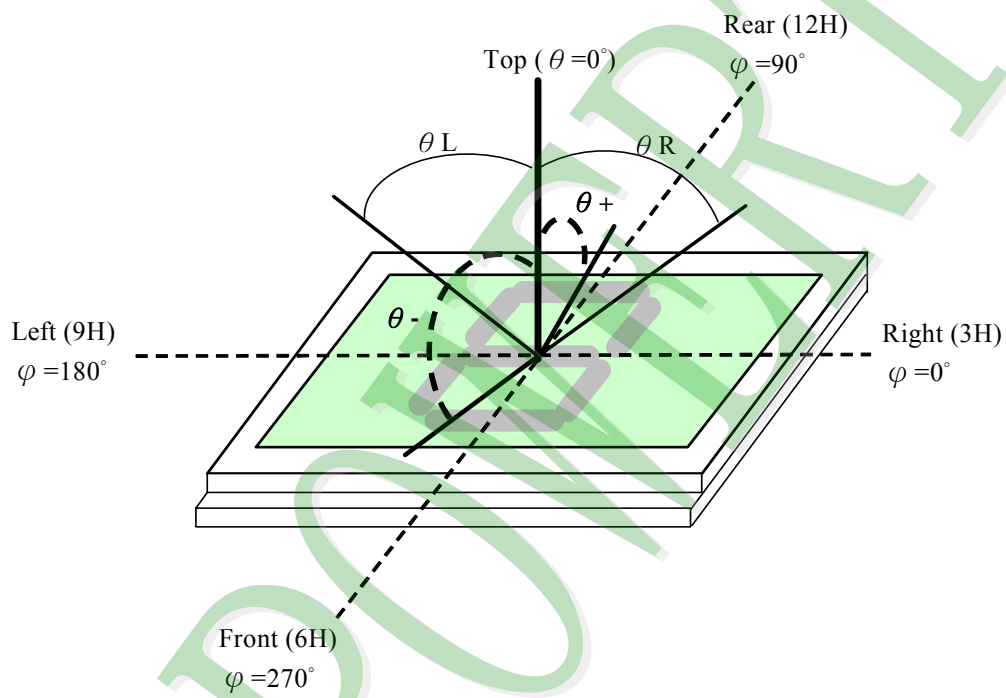
Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	Reference	
Response Time	Rise	t_r	-	150	-	ms	Note2	
	Fall	t_f	-	300	-			
Viewing angle range	Top	$\Theta+$	$C > 2.0$, $\varnothing = 270^\circ$	0	-	40	Deg.	Note 1
	Bottom	$\Theta-$		0	-	40		
	Left	Θ_L		0	-	45		
	Right	Θ_R		0	-	45		
Contrast Ratio	C	$\theta = 0^\circ$, $\varnothing = 270^\circ$	5	7	-	-	Note 3	



Note 1.

Optical characteristics-2

Viewing angle

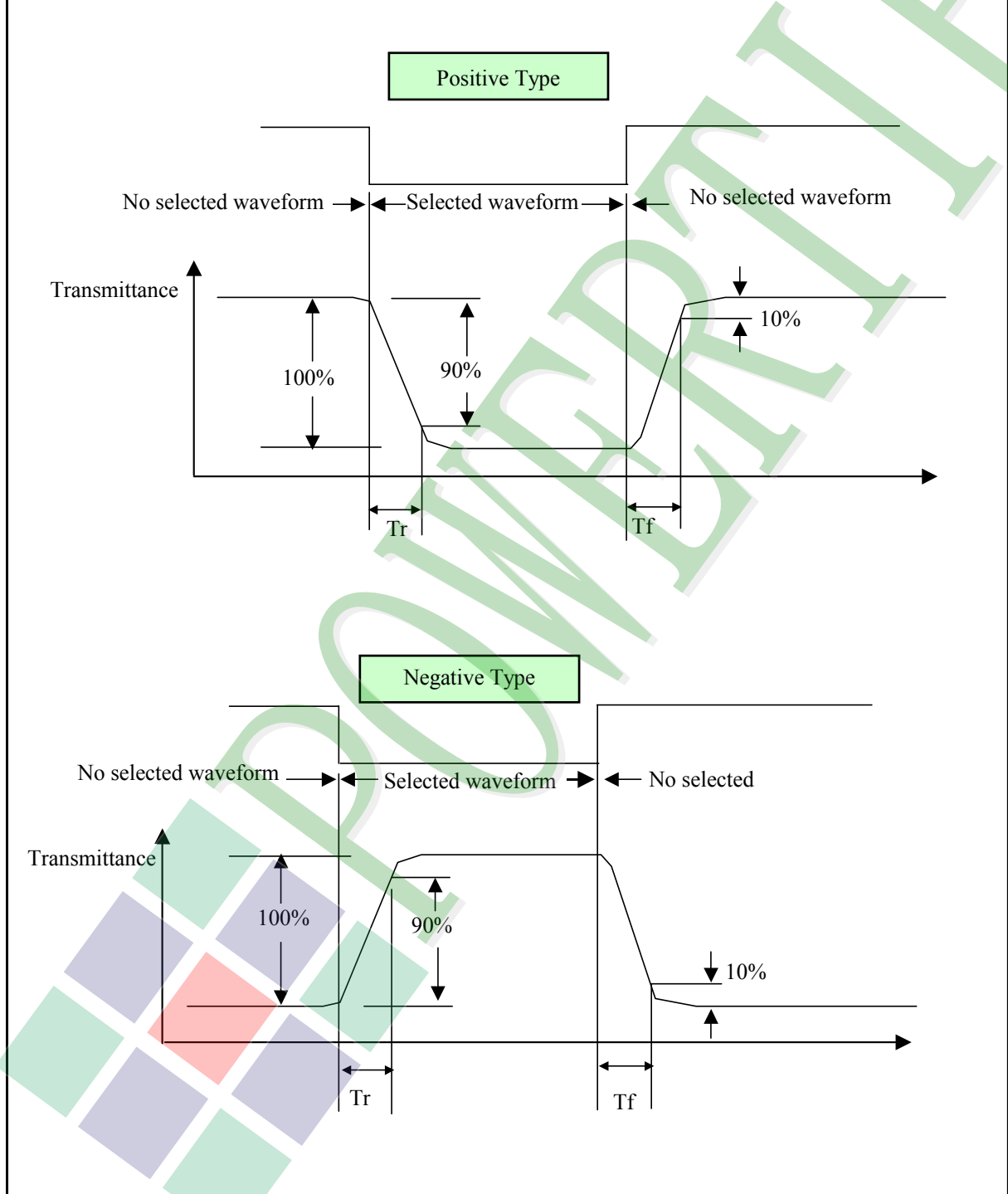


Viewing angle

Note 2.

Optical characteristics-3

Fig.2 Definition of response time





Electrical characteristics-2

※2 Drive waveform

V_{op} : Drive voltage

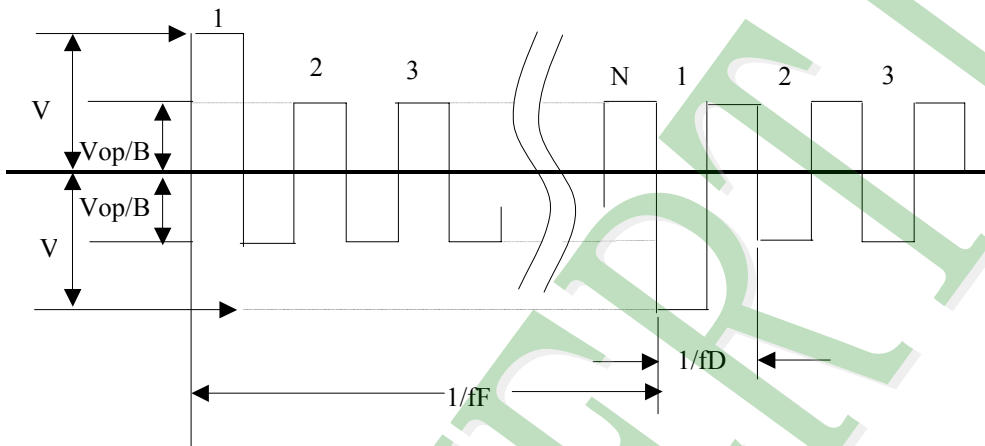
$1/B$: Bias

N : Duty

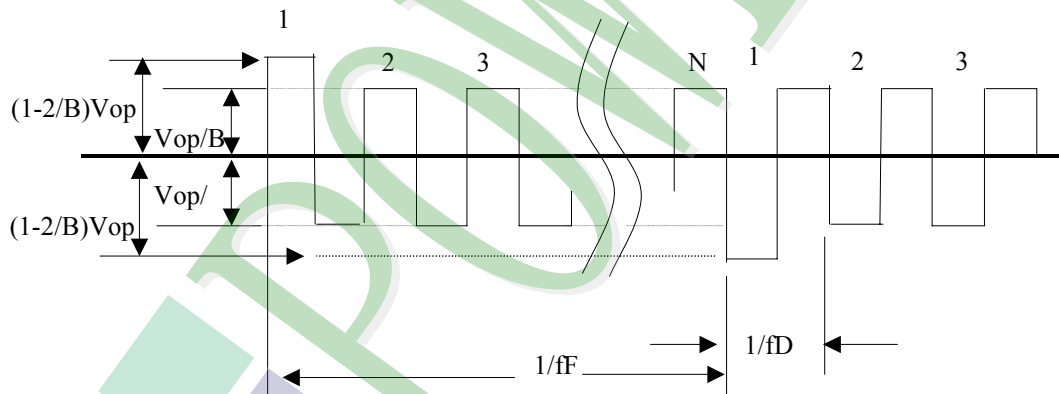
f_F : Frame frequency

f_D : Drive frequency

(1) Selected waveform



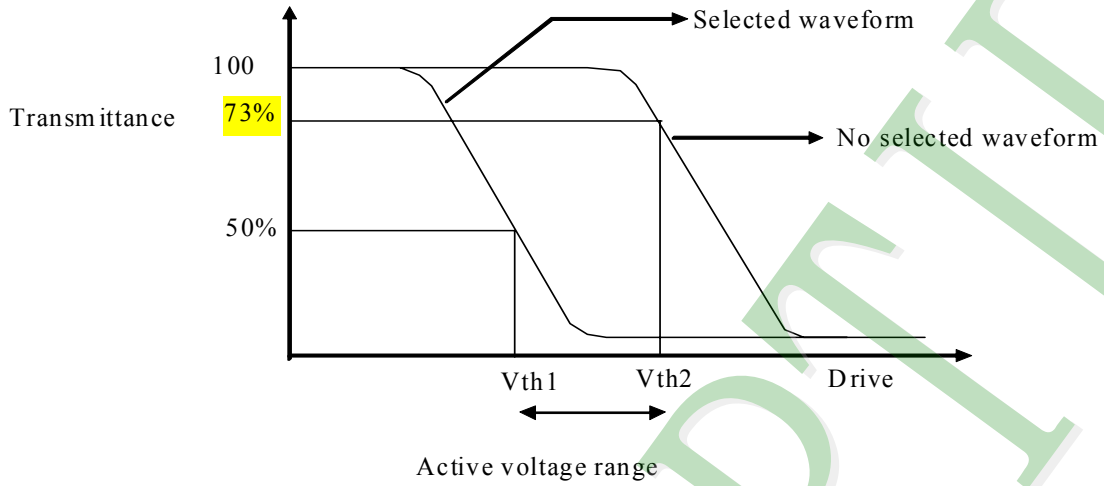
(2) Non- Selected wave form



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak / 2 = 1 period

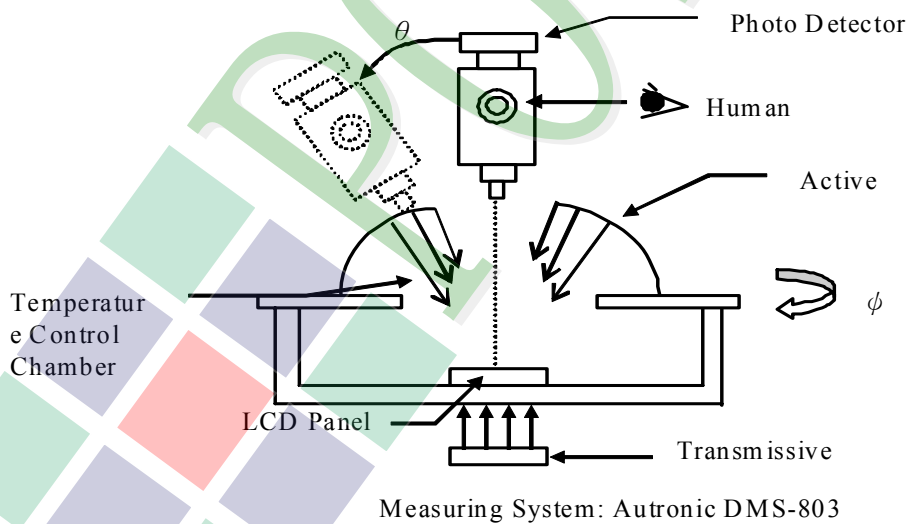
Note 3. : Definition of Vth



	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio
= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System





1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	720	mA
Reverse Voltage	VR	Ta =25°C	-	10	V
Power Dissipation	PO	Ta =25°C	-	1.104	W

Electrical / Optical Characteristics

Ta =25°C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 120mA	3.9	4.2	4.6	V
Reverse Current	IR	VR=10V	-	-	1	mA
Average Brightness (without LCD)	IV	IF= 150mA	220	-	-	cd/m2
Wavelength	Hue	IF=150mA	569	-	576	nm
Color	Yellow-Green					

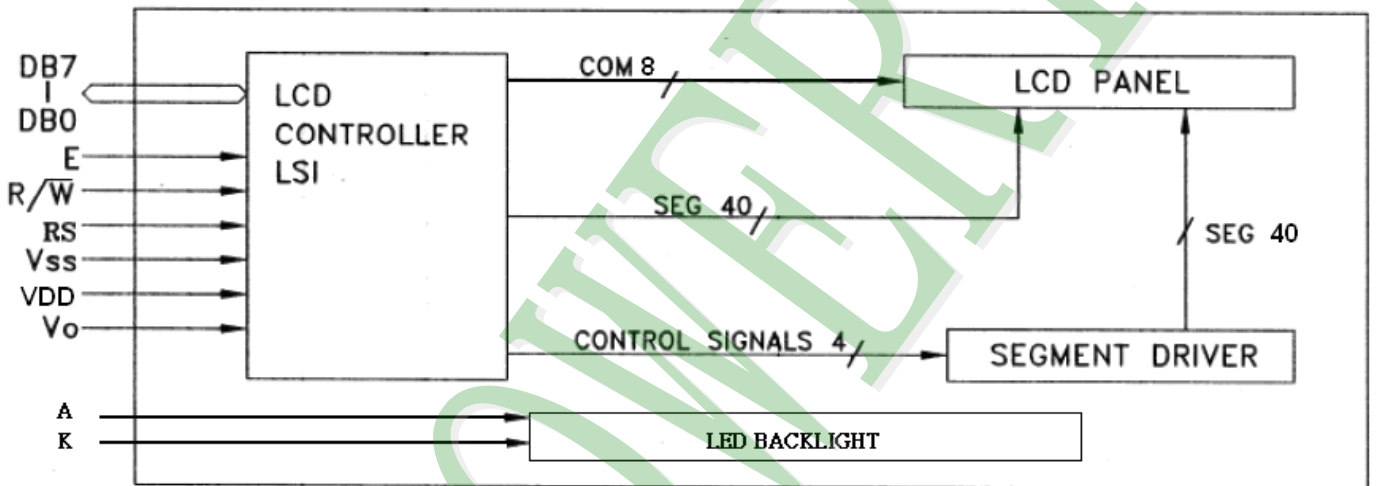
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram

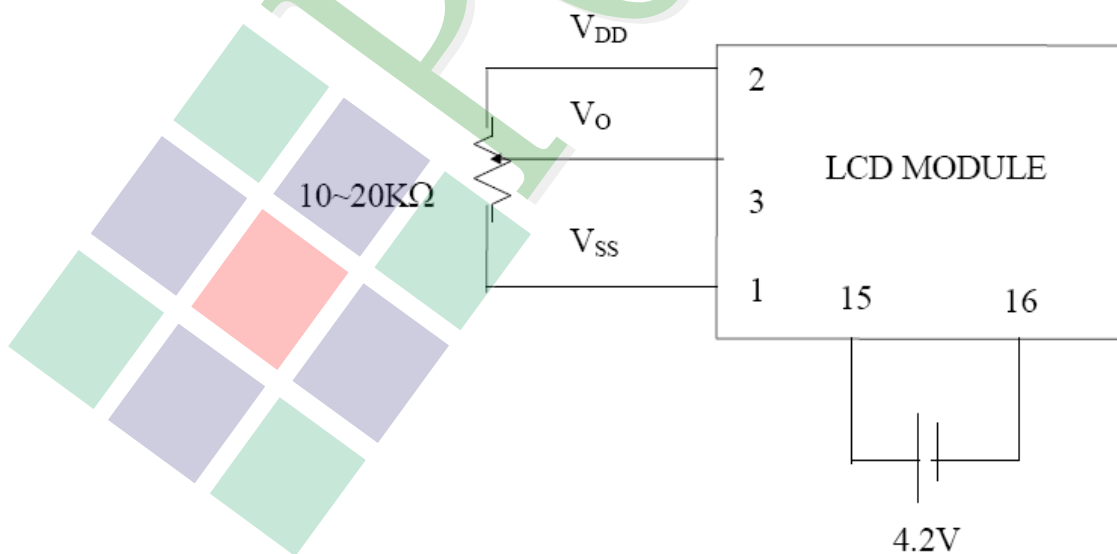


2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	VSS	Power Supply ($V_{SS}=0$)
2	VDD	Power Supply ($V_{DD}>V_{SS}$)
3	VO	Operating voltage for LCD.
4	RS	Register Selection input. High = Data register ; Low = Instruction register (for write). Busy flag address counter (for read)
5	$\overline{R/W}$	Read/Write signal input is used to select the read / write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7~10	DB0~DB3	Four low order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. These four are not used during 4-bit operation.
11~14	DB4~DB7	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. DB7 can be used as a busy flag.
15	A	Power supply for LED backlight (+)
16	K	Power supply for LED backlight (-)

2.2.1 Application Notes

Contrast Adjust





2.2.2 Refer Initial code

```
void initial()
{
    delay(40);

    write_com(0x3A);        // function set: 8-bit operation & dual line

    delay(5);

    write_com(0x02);        //display/cursor return home

    delay(5);

    write_com(0x0c);        //display on (D = C = 1, B = 0)

    delay(5);

    write_com(0x06);        //cursor shift right

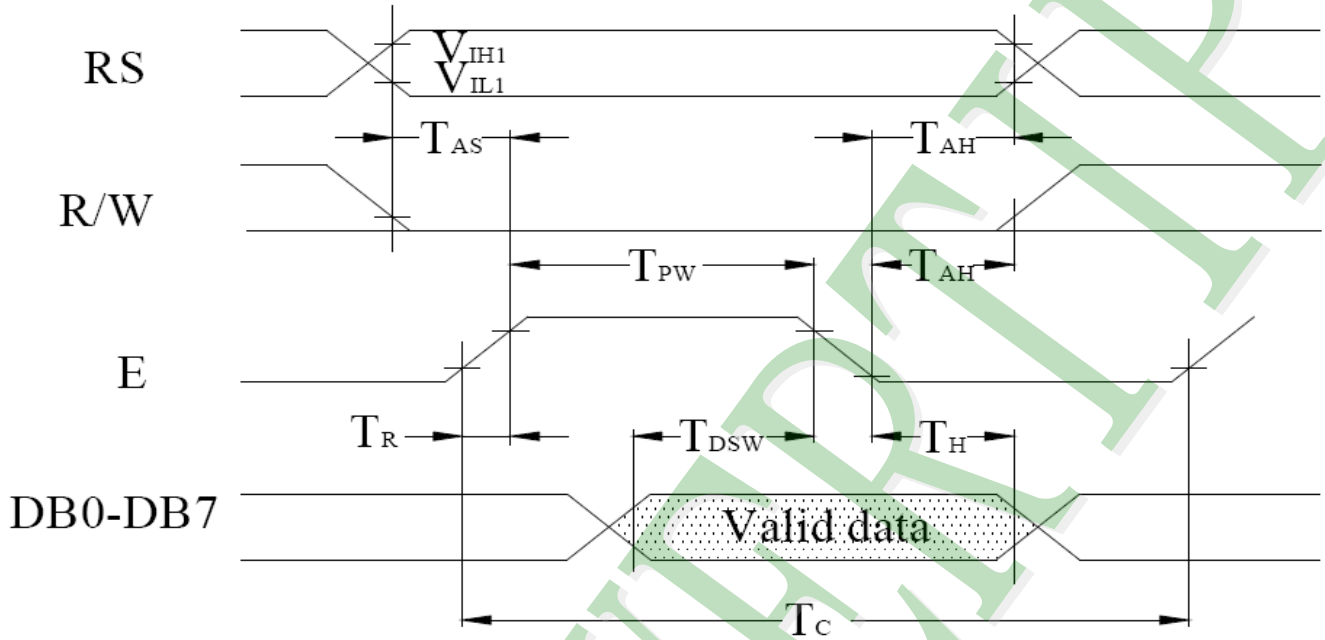
    delay(5);

    write_com(0x01);        // clear display

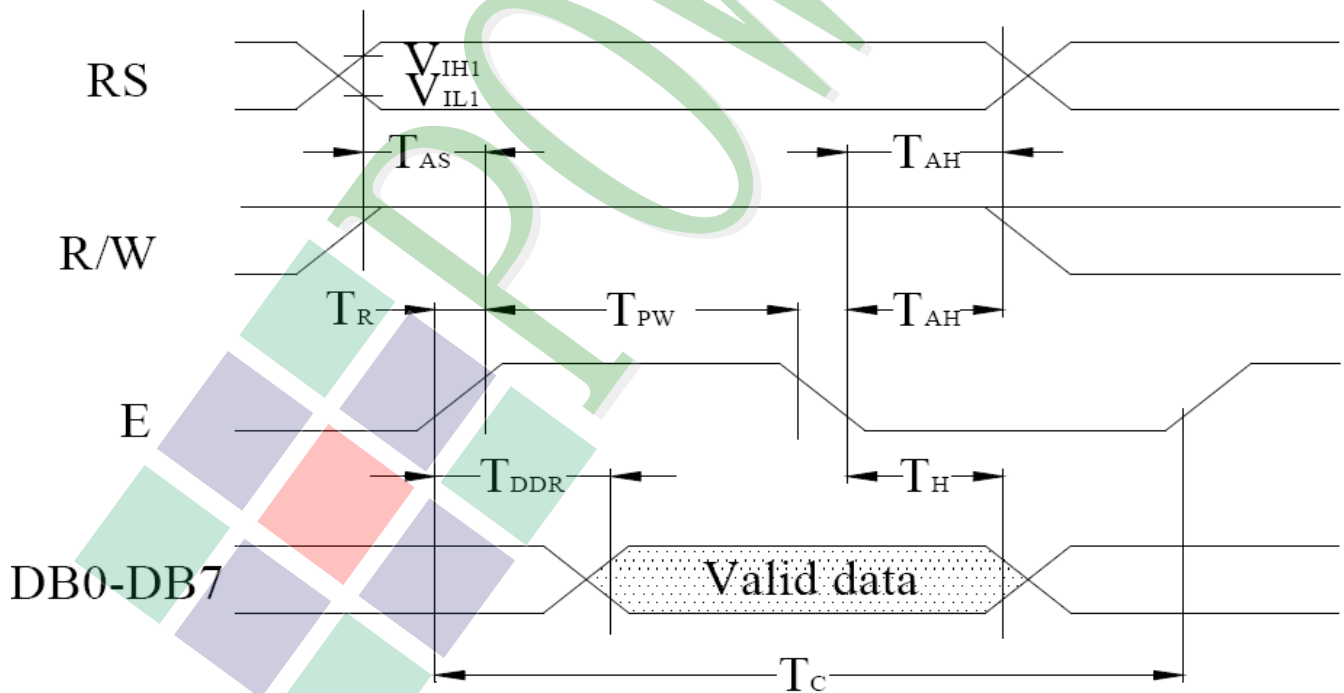
    delay(5);
}
```

2.3 Timing Characteristics

- Writing data from MPU to ST7066U



- Reading data from ST7066U to MPU





POWER TIP

• Write Mode (Writing data from MPU to ST7066U)

(VDD= +5V±10%, Ta=25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DSW}	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

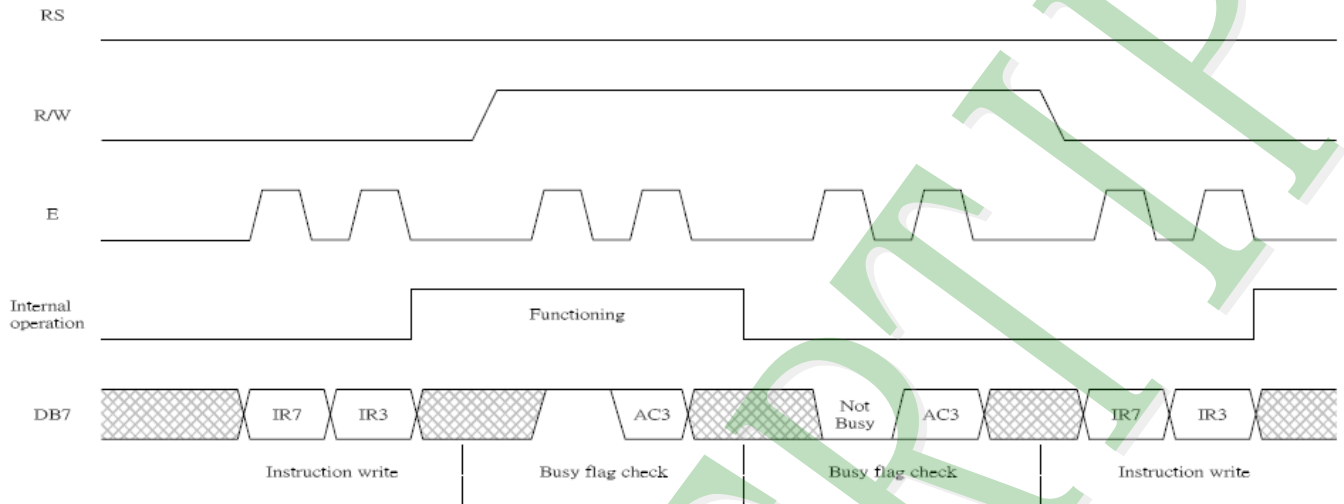
• Read Mode (Reading data from ST7066U to MPU)

(VDD = +5V±10%, Ta=25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

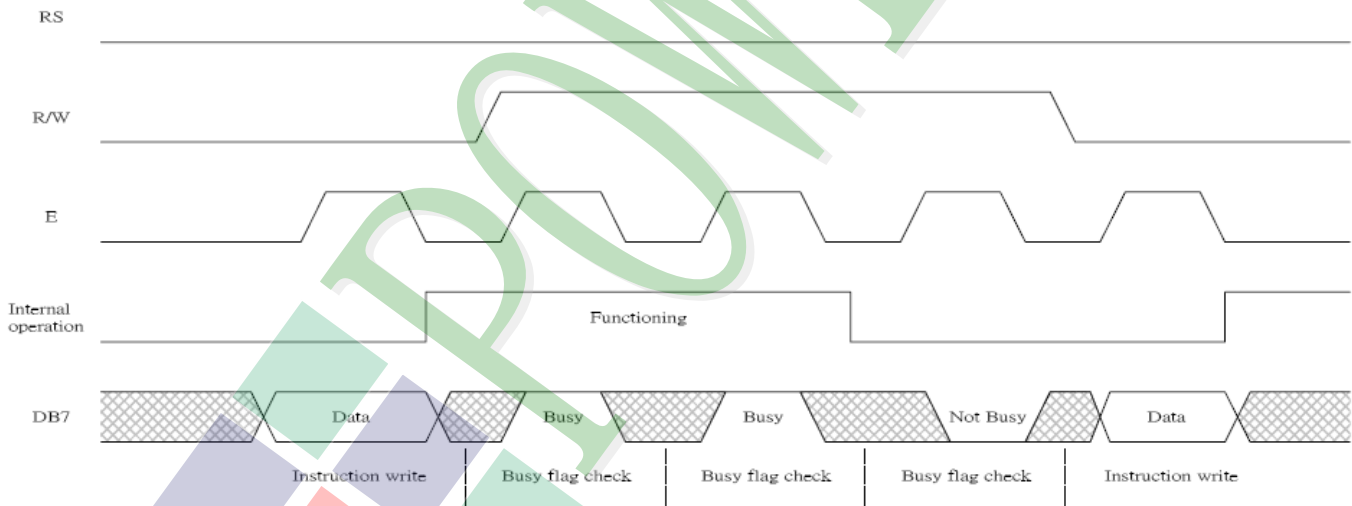
For 4-bit interface data, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface data, all eight bus lines (DB0 to DB7) are used.

Example of busy flag check timing sequence



2.4 Display Command

Instruction Table:

Instruction	Instruction Code										Description	Description Time (270KHz)	
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0			
Clear Display	0	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM, and set DDRAM address to "00H" from AC	1.52 ms
Return Home	0	0	0	0	0	0	0	0	0	1	x	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52 ms
Entry Mode Set	0	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37 us
Display ON/OFF	0	0	0	0	0	0	0	1	D	C	B	D=1:entire display on C=1:cursor on B=1:cursor position on	37 us
Cursor or Display Shift	0	0	0	0	0	0	1	S/C	R/L	x	x	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	37 us
Function Set	0	0	0	0	0	1	DL	N	F	x	x	DL:interface data is 8/4 bits N:number of line is 2/1 F:font size is 5x11/5x8	37 us
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0		Set CGRAM address in address counter	37 us
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0		Set DDRAM address in address counter	37 us
Read Busy flag and address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0		Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 us
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0		Write data into internal RAM (DDRAM/CGRAM)	37 us
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0		Read data from internal RAM (DDRAM/CGRAM)	37 us

Note:

Be sure the ST7066U is not in the busy state (BF = 0) before sending an instruction from the MPU to the ST7066U. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to Instruction Table for the list of each instruction execution time.



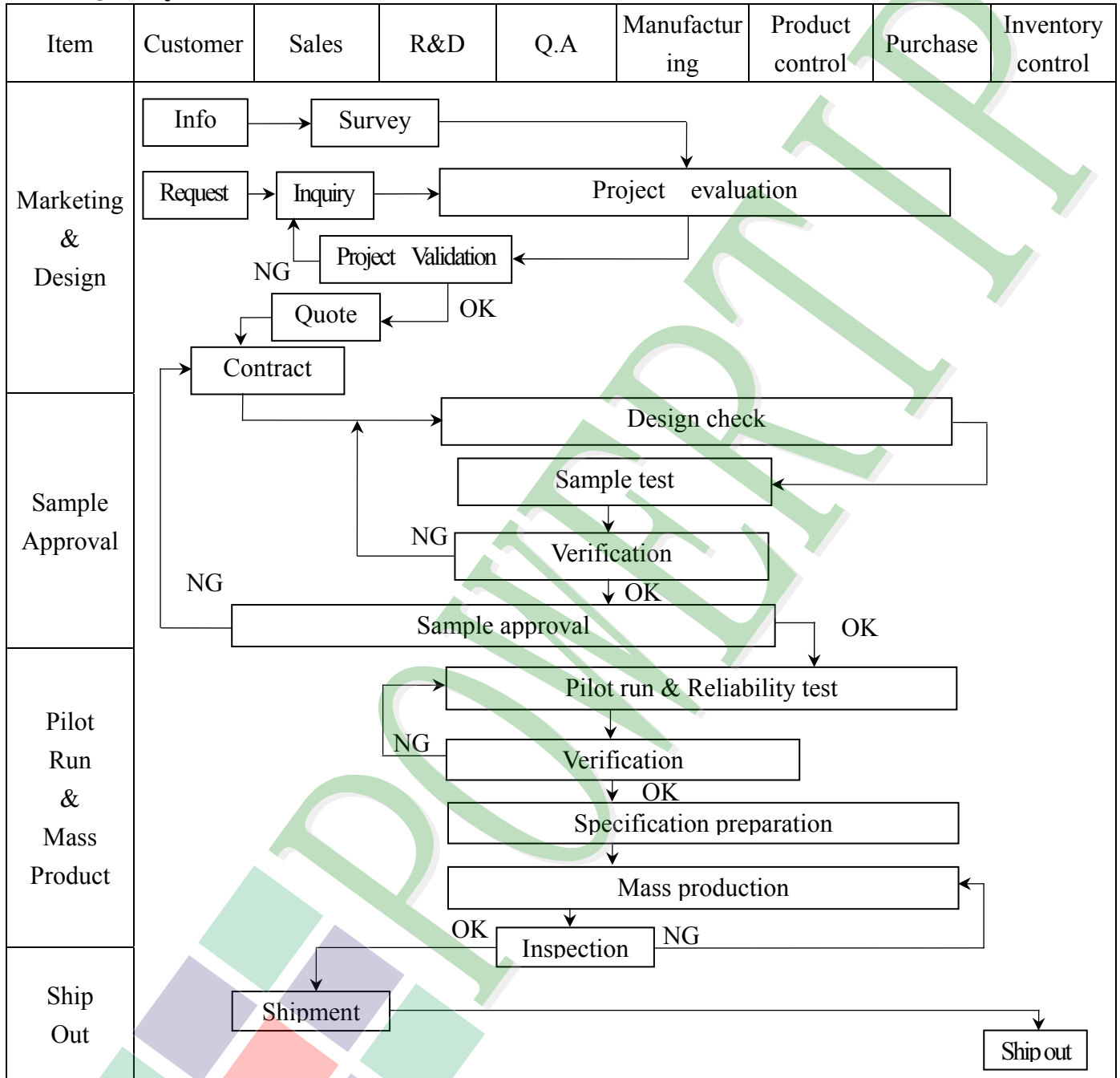
2.5 Character Pattern

b7-b4 b3-b0	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)			0	a	P	~	F				一	ヲ	エ	o	p
0001	(2)	!	1	A	Q	a	q				。	フ	ワ	△	g	q
0010	(3)	"	2	B	R	b	r				フ	イ	ウ	×	P	Q
0011	(4)	#	3	C	S	c	s				1	ウ	テ	E	e	×
0100	(5)	\$	4	D	T	d	t				レ	エ	ト	ト	H	o
0101	(6)	%	5	E	U	e	u				*	オ	大	1	o	o
0110	(7)	&	6	F	V	f	v				ヲ	カ	ニ	ヨ	P	Q
0111	(8)	'	7	B	W	g	w				ヲ	*	又	ヲ	g	w
1000	(1)	(8	H	X	h	x				レ	ウ	*	ウ	r	×
1001	(2))	9	I	Y	i	y				ウ	ク	ノ	ル	~	y
1010	(3)	*	:	J	Z	j	z				エ	コ	ン	レ	j	*
1011	(4)	+	;	K	L	k	l				*	ウ	ヒ	ロ	×	*
1100	(5)	,	<	L	*	l	l				ウ	ヒ	フ	フ	o	m
1101	(6)	=	=	M	I	m	y				△	又	△	ウ	レ	+
1110	(7)	.	>	N	O	n	*				ヨ	ヒ	ホ	△	n	
1111	(8)	/	@	O	L	o	e				ウ	ウ	マ	"	o	■

Note: This model is designed by 5x8. The 2 bottom columns does not exist in fact.

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart





Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	<pre> graph TD Info[Info] --> Claim[Claim] Claim --> Failure[Failure analysis] Failure --> Report[Analysis report] Failure --> Action[Corrective action] Action --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2 Inspection Specification

- ◆ Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).
- ◆ Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II .
- ◆ Equipment : Gauge 、 MIL-STD 、 Powertip Tester 、 Sample
- ◆ Defect Level : Major Defect AQL : 0.4 ; Minor Defect : AQL : 1.5 .
- ◆ OUT Going Defect Level : Sampling .
- ◆ Manner of appearance test :
 - (1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.
 - (2). Standard of inspection : (Unit : mm)
 - (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (4). Definition of area . (Fig. 2)

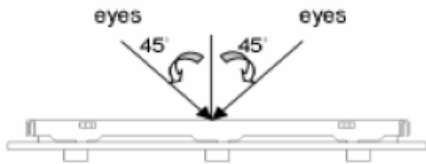


Fig.1

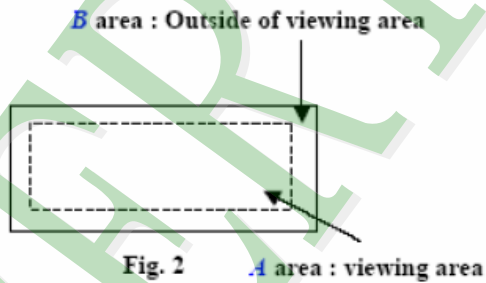
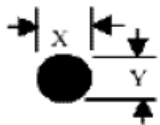
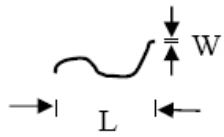
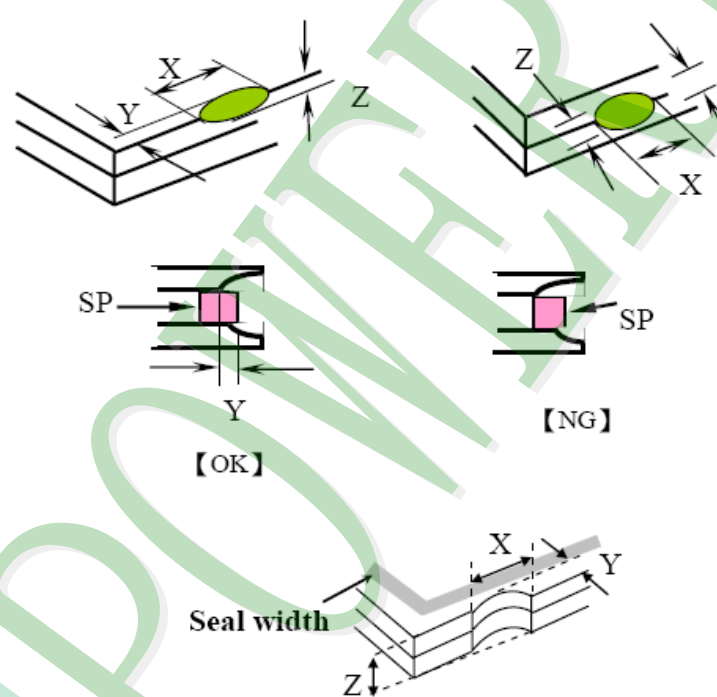


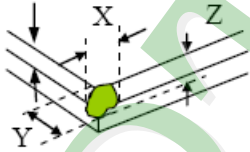
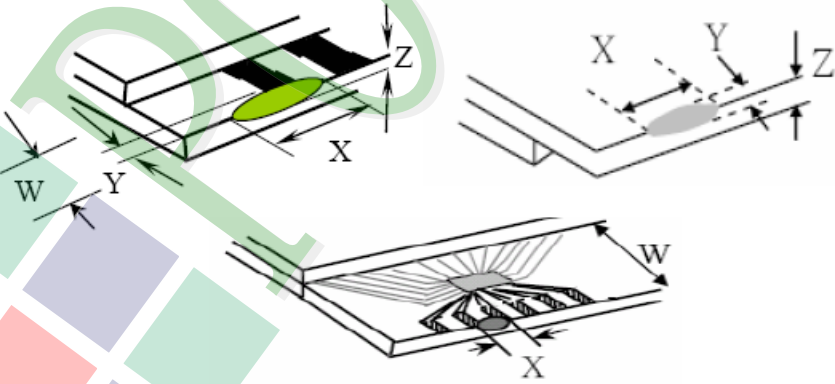
Fig. 2

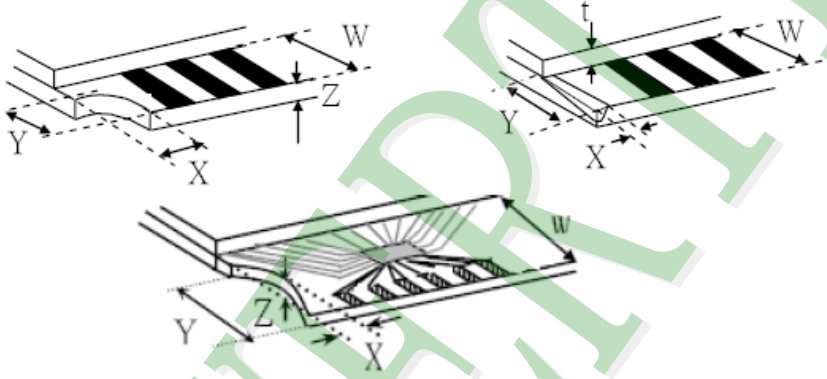
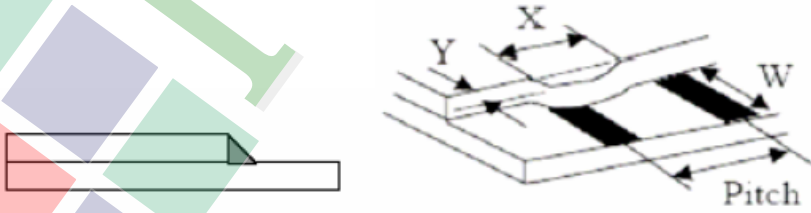
◆ Specification:

NO	Item	Criterion	Level
01	Product condition	1. 1 The part number is inconsistent with work order of Production.	Major
		1. 2 Mixed production types.	Major
		1. 3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3. 1 Product dimension and structure must conform to Structure diagram.	Major
04	Electrical Testing	4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
		4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major

NO	Item	Criterion	Level																																					
05	<p>Black or white dot、scratch、contamination</p> <p>Round type</p>  <p>$\Phi = (x+y)/2$</p> <p>Line type</p> 	<p>5. 1 Round type:</p> <p>5. 1. 1 display only :</p> <ul style="list-style-type: none"> • White and black spots on display ≤ 0.30 mm , no more than 4 white or black spots present. • Densely spaced : NO more than two spots or lines within 3 mm. <p>5. 1. 2 Non-display :</p> <table border="1"> <thead> <tr> <th rowspan="2">Dimension (diameter : Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td colspan="2">Accept no dense</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.20$</td> <td>3</td> <td rowspan="2">Ignore</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.30$</td> <td>2</td> </tr> <tr> <td>Total quantity</td> <td colspan="2">4</td> </tr> </tbody> </table> <p>5. 1. 3 Line type:</p> <table border="1"> <thead> <tr> <th colspan="2">Dimension</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>Length (L)</th> <th>Width (W)</th> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.03$</td> <td>Accept no dense</td> <td rowspan="3">Ignore</td> </tr> <tr> <td>$L \leq 3.0$</td> <td>$0.03 < W \leq 0.05$</td> <td rowspan="2">4</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.05 < W \leq 0.075$</td> </tr> <tr> <td>---</td> <td>$W > 0.075$</td> <td colspan="2">As round type</td> </tr> </tbody> </table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.10$	Accept no dense		$0.10 < \Phi \leq 0.20$	3	Ignore	$0.20 < \Phi \leq 0.30$	2	Total quantity	4		Dimension		Acceptance (Q'ty)		Length (L)	Width (W)	A area	B area	---	$W \leq 0.03$	Accept no dense	Ignore	$L \leq 3.0$	$0.03 < W \leq 0.05$	4	$L \leq 2.5$	$0.05 < W \leq 0.075$	---	$W > 0.075$	As round type		Minor
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---	$W > 0.075$	As round type																																						
06	<p>Polarizer</p> <p>Bubble</p>	<table border="1"> <thead> <tr> <th rowspan="2">Dimension (diameter : Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.20$</td> <td>Accept no dense</td> <td rowspan="4">Ignore</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.50$</td> <td>3</td> </tr> <tr> <td>$0.50 < \Phi \leq 1.00$</td> <td>2</td> </tr> <tr> <td>$\Phi > 1.00$</td> <td>0</td> </tr> <tr> <td>Total quantity</td> <td colspan="2">4</td> </tr> </tbody> </table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.20$	Accept no dense	Ignore	$0.20 < \Phi \leq 0.50$	3	$0.50 < \Phi \leq 1.00$	2	$\Phi > 1.00$	0	Total quantity	4		Minor																				
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07	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length</p> <hr/> <p>7.1 General glass chip :</p> <p>7.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="502 1534 1300 1825"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq a$</td> <td>Crack can't enter viewing area</td> <td>$\leq 1/2 t$</td> </tr> <tr> <td>$\leq a$</td> <td>Crack can't exceed the half of SP width.</td> <td>$1/2 t < Z \leq 2 t$</td> </tr> </tbody> </table>	X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$	$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	Minor
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<p>7.2 Protrusion over terminal :</p> <p>7.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="467 1729 1254 1899"> <thead> <tr> <th></th> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>Front</td> <td>$\leq a$</td> <td>$\leq 1/2 W$</td> <td>$\leq t$</td> </tr> <tr> <td>Back</td> <td colspan="3">Neglect</td> </tr> </tbody> </table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	Neglect		
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		<p>7.2.2 Non-conductive portion :</p>  <table border="1" data-bbox="580 1088 1206 1240"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq 1/3 a$</td> <td>$\leq W$</td> <td>$\leq t$</td> </tr> </tbody> </table> <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>7.2.3 Glass remain :</p>  <table border="1" data-bbox="501 1771 1190 1910"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq a$</td> <td>$\leq 1/3 W$</td> <td>$\leq t$</td> </tr> </tbody> </table>		X	Y	Z	$\leq 1/3 a$	$\leq W$	$\leq t$	X	Y	Z
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◆ Specification For Monotype and Color STN :

(Ver. B01)

NO	Item	Criterion	Level
08	Backlight elements	8. 1 Backlight can't work normally.	Major
		8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
09	General appearance	9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
		9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤ 1.5 mm.	Minor



POWERTIP

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

LCM包裝規格書

LCM Packaging Specifications

Approve	Check	Contact
Ryan	Eddy	Terry

Documents NO. JPKG-PC1601LRS-AWA-B-Q

1. 包裝材料規格表 (Packaging Material) : (per carton)

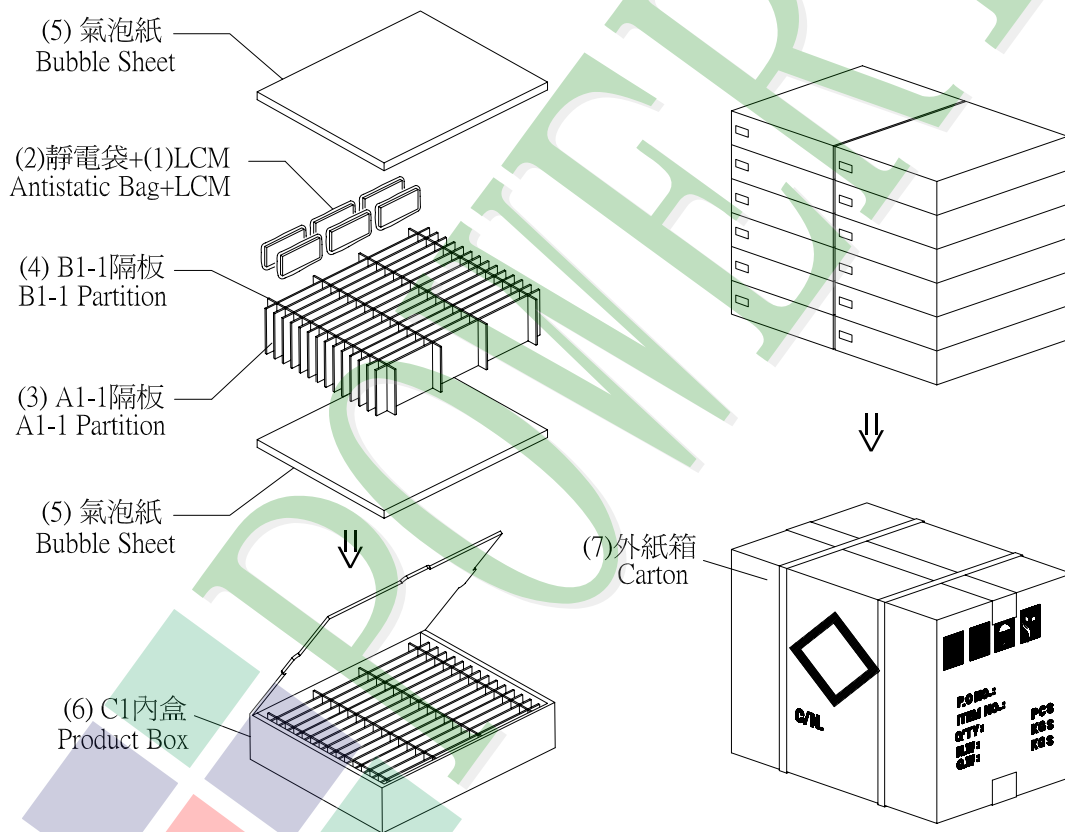
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (1)LCM	PC1601LRS-AWA-B-Q	80.0X36.0X14.0	0.0358	432	15.4656
2	靜電袋(2)Antistatic Bag	BAG100100ARABA	100 X 100	0.0011	432	0.4752
3	A1-1隔板(3)A1-1 Partition	BX29500047BZBA	295 X 47 X 3	0.0078	168	1.3104
4	B1-1隔板(4)B1-1 Partition	BX24500047BZBA	245 X 47 X 3	0.0065	48	0.312
5	氣泡紙(5)Bubble Sheet	BAG280240BWABA	280 X 240	0.006	24	0.144
6	C1內盒(6)Product Box	BX31025555AABA	310 X 255 X 55	0.13	12	1.56
7	外紙箱(7)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
8						
9						

2. 一整箱總重量 (Total LCD Weight in carton) : 20.1 Kg±10%

3. 單箱數量規格表 (Packaging Specifications and Quantity) :

(1)Quantity Of Spacer : A1-1隔板 X 14 , B1-1隔板 X 4

(2)Total LCM quantity in carton : quantity per box 36 x no of boxes 12 = 432



特記事項 (REMARK)

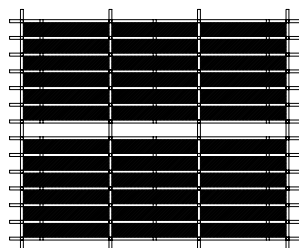
4. Label Specifications :

參照廠內作業標準

5. LCM排放示意圖(前後間隔不放置):

5. LCM placed as figure showing:

(First and last slot should be empty)



▨ 模組(LCM) X 1pcs.

前、中、后各空一格