POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	.	
SAMPLE CODE (Ver.)	.	
MASS PRODUCTION CODE (Ver.)	:	PC1602AR-DWA-A-Q **(Rev.0)**
DRAWING NO. (Ver.)		**PC-95002**

Customer Approved

Date:

Approved	QC Confirmed	Designer
技術部 2006-3-28 陳銘煊		蔡木金 3/28-01

■ Approval For Specifications Only.

* This specification is subject to change without notice.

Please contact Powertip or it's representative before designing your product based on this specification.

☐ Approval For Specifications and Sample.

POWERTIP TECH. CORP.

Headquarters: No.8, 6th Road, Taichung Industrial Park, Taichung, Taiwan
台中市 407 工業區六路 8 號

TEL: 886-4-2355-8168
FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw
Http://www.powertip.com.tw

RECORDS OF REVISION

Date	Rev.	Description	Note	Page
2006/3/28	**0**	PC1602AR-DWA-A-Q is the ROHS compliant part number based on Powertip's standard PC1602AR-DWA-A		

Total：19 Page

Contents

Note：For detailed information please refer to IC data sheet：ST7066U,KS0065B

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	TN Gray Positive Reflective Normal Temp.
Driver Condition	LCD Module：1/16 Duty，1/4 Bias
Viewing Direction	6 O'clock
Backlight	-
Weight	25 g
Interface	—
Other	—

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	80.0 (L) *36.0 (w) *10.2 (H)(Max)	mm
Viewing Area	66.0 (L) *16.2(w)	mm
Active Area	56.21(L) * 11.5 (w)	mm
Dot Size	0.56 (L) * 0.66 (w)	mm
Dot Pitch	0.60(L) * 0.70 (w)	mm

Note：For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	—	-0.3	7.0	V
LCD Driver Supply Voltage	V_{LCD}	—	V_{DD}-10.0	V_{DD}+0.3	V
Input Voltage	V_{IN}	—	-0.3	V_{DD}+0.3	V
Operating Temperature	T_{OP}	-	0	50	℃
Storage Temperature	T_{ST}	-	-20	70	℃
Storage Humidity	H_D	Ta＜40 ℃	-	90	%RH

1.4 DC Electrical Characteristics

V_{DD} = 5.0 V ± 10%，V_{SS} = 0V，Ta = 25℃

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	V_{DD}	—	4.5	5.0	5.5	V
"H" Input Voltage	V_{IH}	—	0.7 V_{DD}	-	V_{DD}	V
"L" Input Voltage	V_{IL}	—	-0.3	-	0.6	V
"H" Output Voltage	V_{OH}	IOH=-0.205mA	3.9	-	V_{DD}	V
"L" Output Voltage	V_{OL}	IOL=1.2mA	-	-	0.4	V
Supply Current	I_{DD}	V_{DD} = 5.0 V	-	2.0	3.0	mA
LCM Driver Voltage	V_{OP}	0℃	-	-	-	V
		25℃*1	4.0	4.2	4.4	
		50℃	-	-	-	

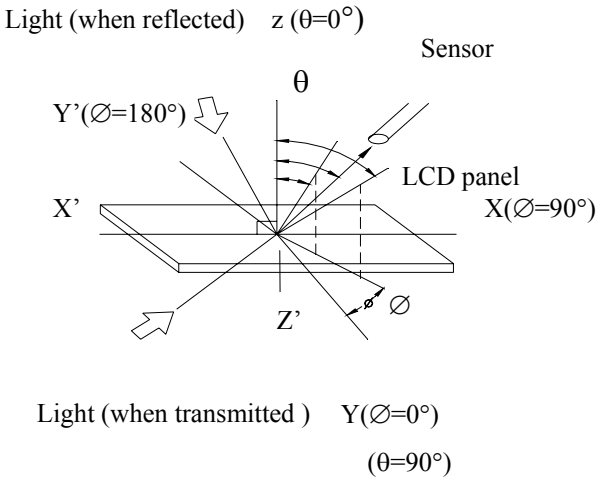
Note:*1.The Vop test point is Vdd-Vo.

1.5 Optical Characteristics

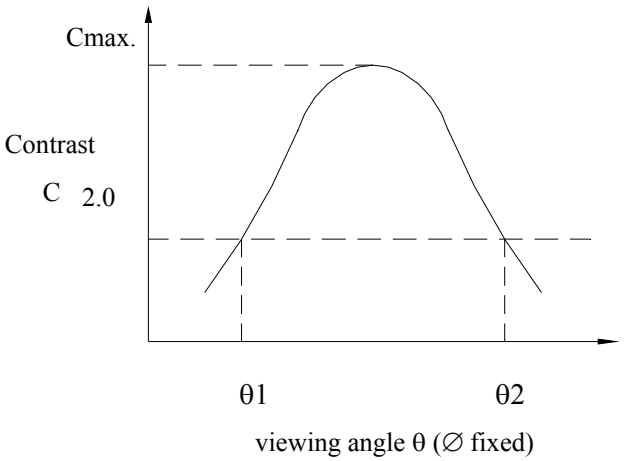
LCD Panel：1/16 Duty，1/4 Bias，V_{LCD} =4.18V，Ta = 25℃

Item	Symbol	Conditions	Min.	Typ.	Max.	Reference
View Angle	θ	C≥2.0, ∅ = 0°	15°	-	50°	Notes 1 & 2
Contrast Ratio	C	θ = 5°, ∅ = 0°	2	5	-	Note 3
Response Time(rise)	tr	θ = 5°, ∅ = 0°	-	80 ms	120 ms	Note 4
Response Time(fall)	tf	θ = 5°, ∅ = 0°	-	130 ms	180 ms	Note 4

Note 1: Definition of angles θ and ∅



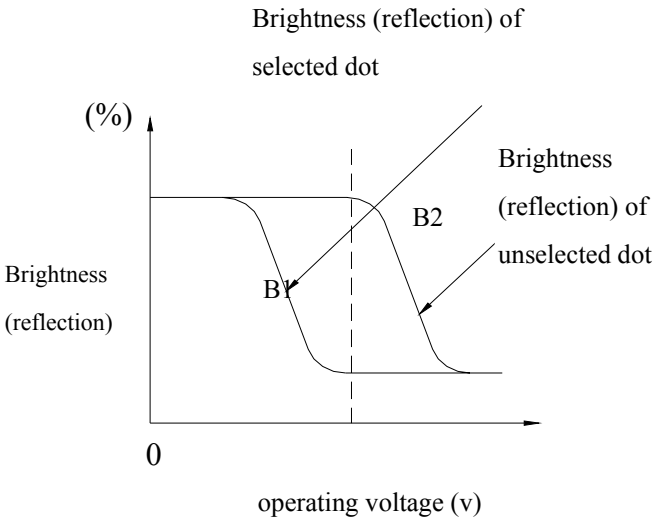
Note 2: Definition of viewing angles θ1 and θ2



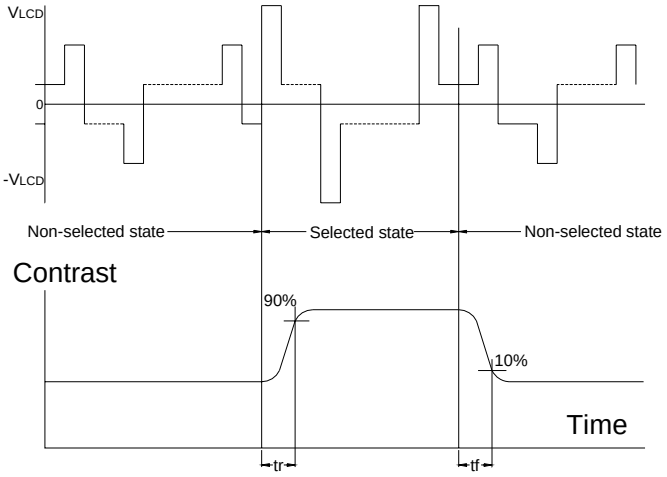
Note : Optimum viewing angle with the naked eye and viewing angle θ at Cmax. Above are not always the same

Note 3: Definition of contrast C

$$C = \frac{\text{Brightness (reflection) of unselected dot (B2)}}{\text{Brightness (reflection) of selected dot (B1)}}$$



Note 4: Definition of response time



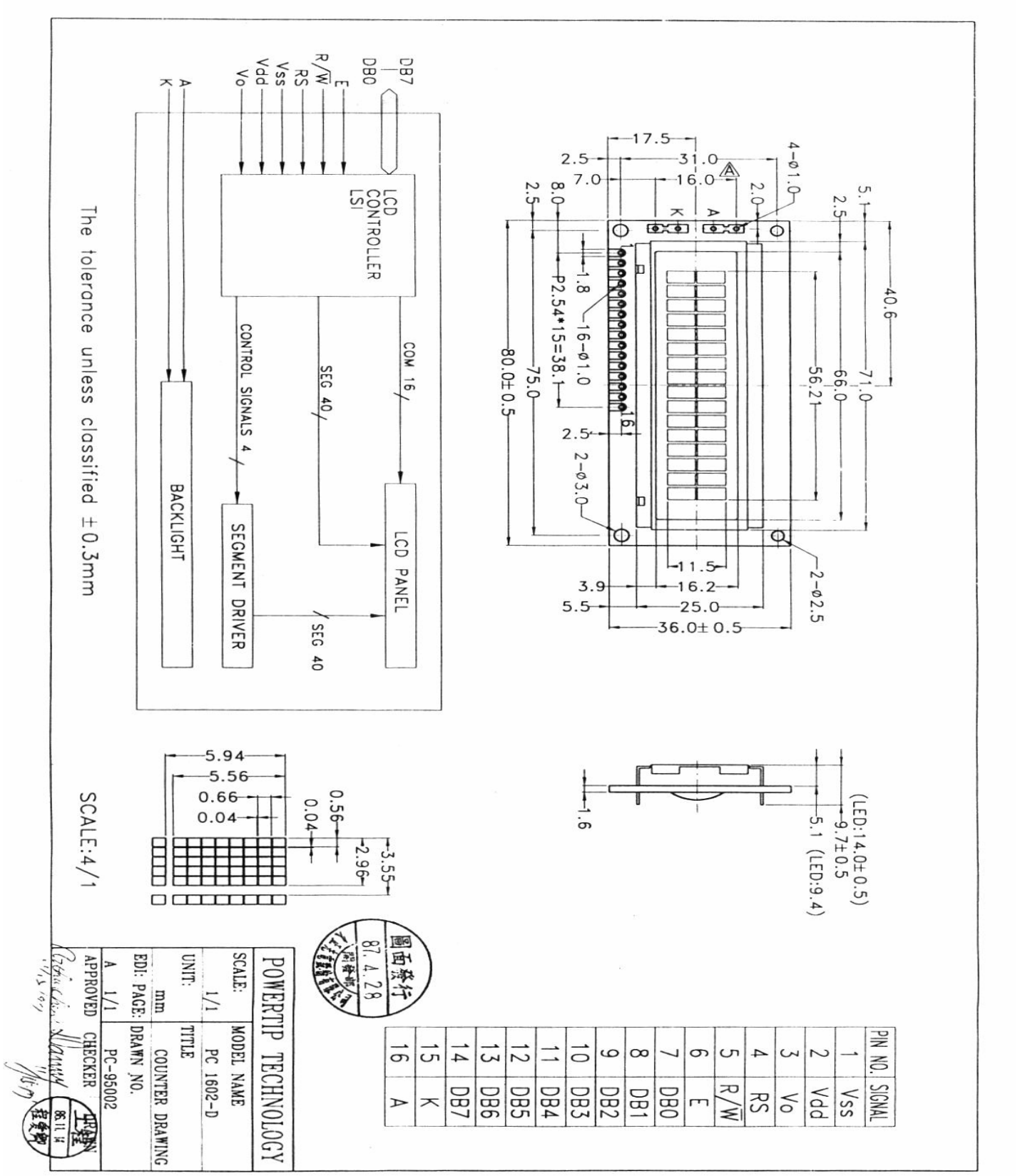
Note: Measured with a transmissive LCD panel which is displayed 1 cm^2

V_{LCD} : Operating voltage f_{FRM} : Frame frequency

t_r : Response time (rise) t_f : Response time (fall)

2. MODULE STRUCTURE

2.1 Counter Drawing



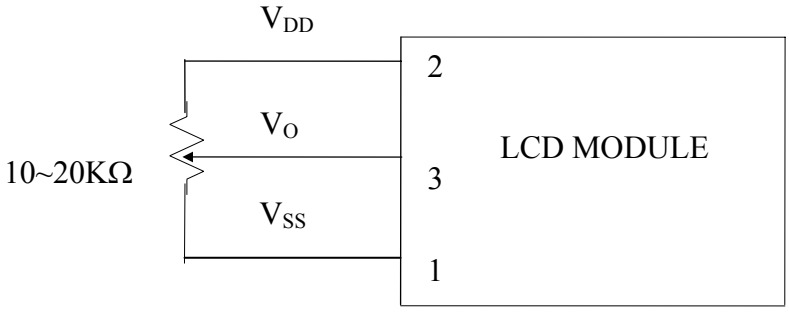
PIN NO.	SIGNAL
1	Vss
2	Vdd
3	Vo
4	RS
5	R/W
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	K
16	A

POWERTIP TECHNOLOGY		
SCALE: 1/1	MODEL NAME: PC 1602-D	
UNIT: mm	TITLE: COUNTER DRAWING	
EDI: PAGE: A 1/1	DRAWN NO.: PC-95002	
APPROVED	CHECKER	DRAWN

2.2 Interface Pin Description

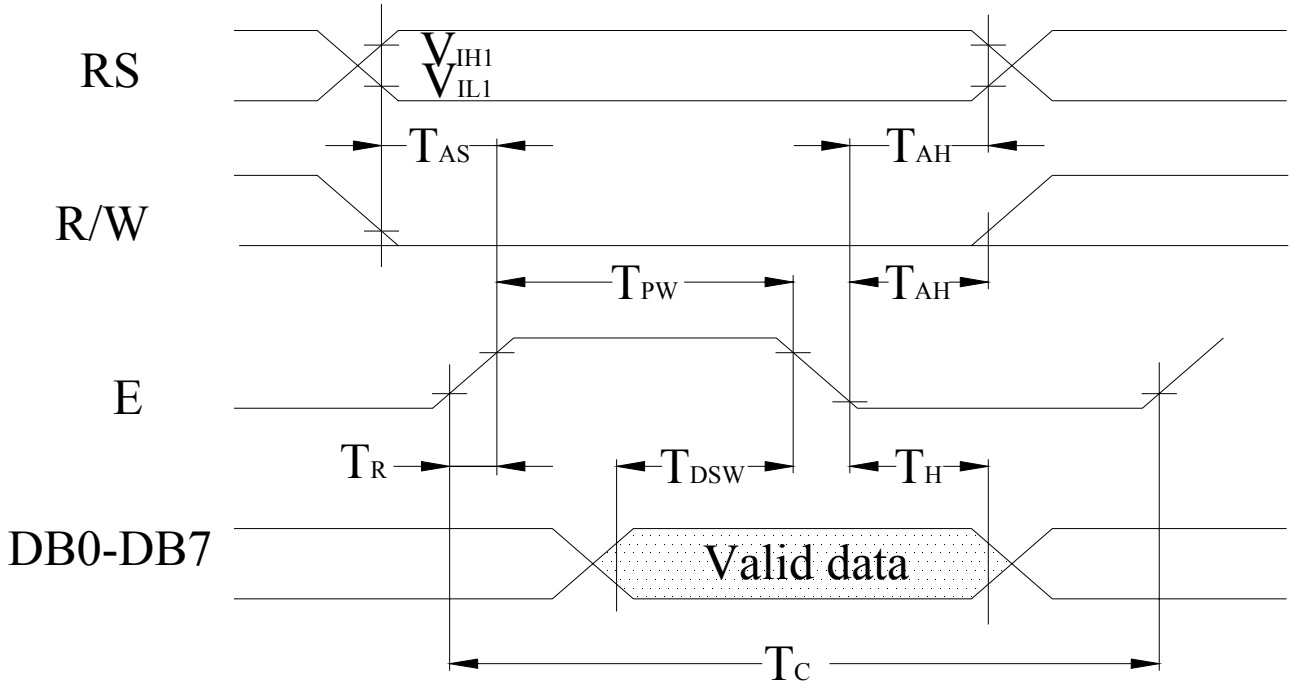
Pin No.	Symbol	Signal Description
1	V_{SS}	Power Supply (Vss=0)
2	V_{DD}	Power Supply (V_{DD}>V_{SS})
3	V_O	Operating voltage for LCD
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	$\overline{R/W}$	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7~10	DB0 ~ DB3	Four low order bi-directional three-state data bus lines. Use for data transfer between the MPU and the LCD module. These four are not used during 4-bit operation.
11~14	DB4~DB7	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. DB7 can be used as a busy flag.
15	K	Power supply for LED B/L (-)
16	A	Power supply for LED B/L (+)

Contrast Adjust

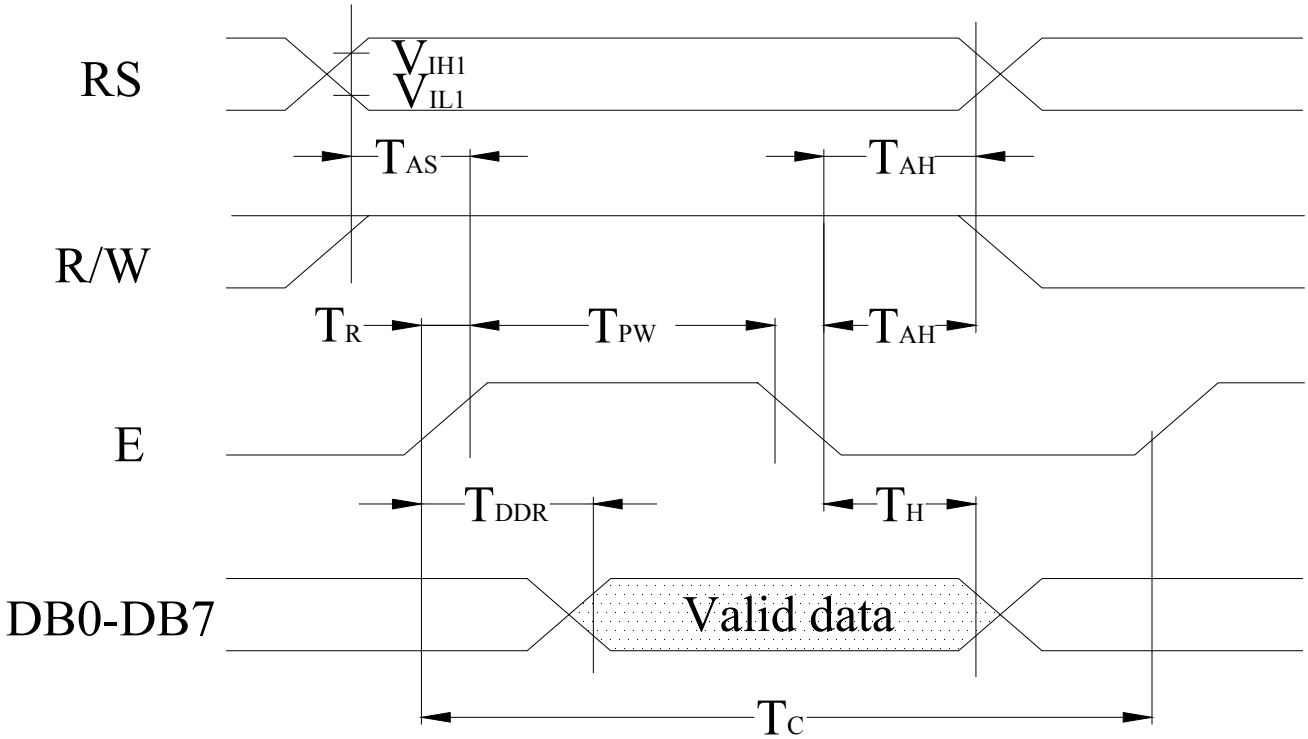


2.3 Timing Characteristics

- Writing data from MPU to ST7066U



- Reading data from ST7066U to MPU



- Write Mode (Writing data from MPU to ST7066U)

(V_{DD}=5.0V±0.5V;VSS=0V,Ta=25℃)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R , T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DSW}	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
T_H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

- Read Mode (Reading data from ST7066U to MPU)

(V_{DD}=5.0V±0.5V;VSS=0V,Ta=25℃)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R , T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T_H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

2.4 Display Command

Instructions	Instruction Code										Description	Description Time (270KHz)
	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read .	37μs
Display ON/OFF	0	0	0	0	0	0	1	D	C	B	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	37μs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37μs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37μs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37μs

Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0μs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37μs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37μs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag , the time between the first instruction and next instruction will take much longer than the instruction time itself.

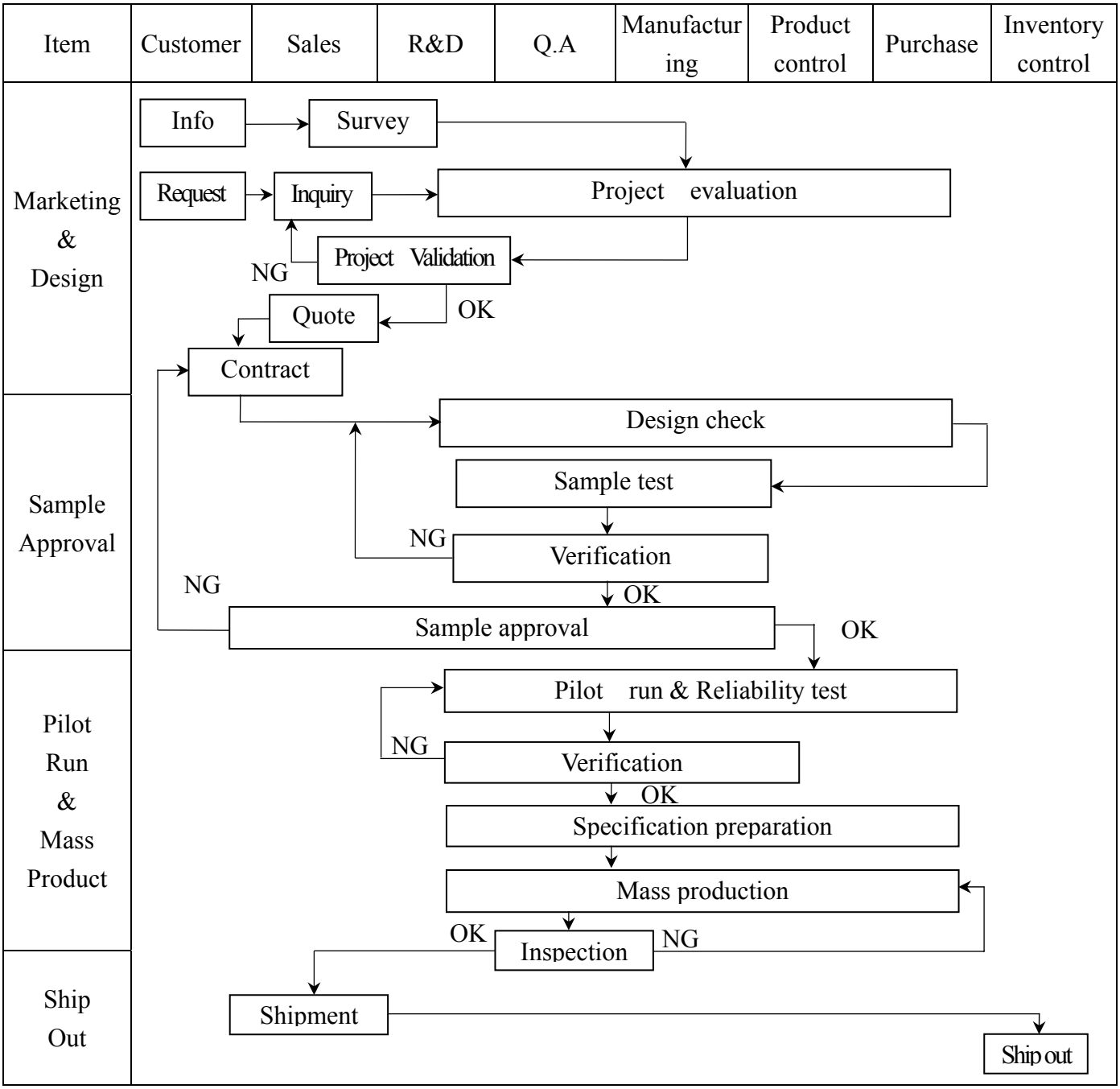
Refer to Instruction Table for the list of each instruction execution time .

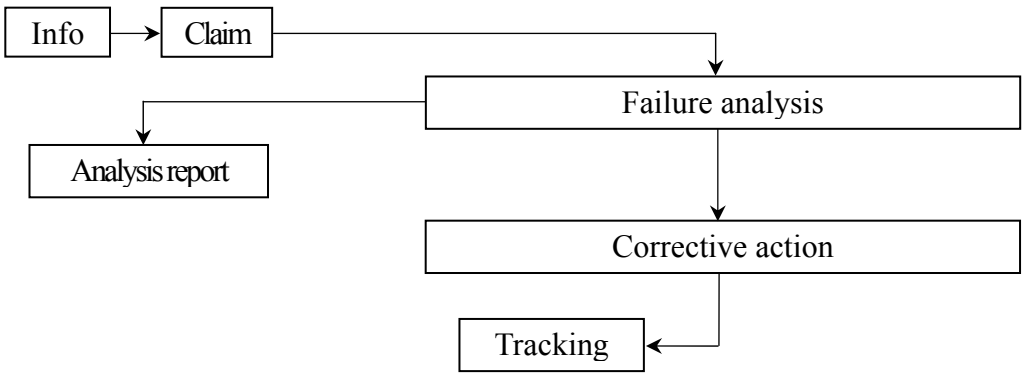
2.5 Character Pattern

CHARACTER PATTERN(SO/HO/EA,WA)

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart

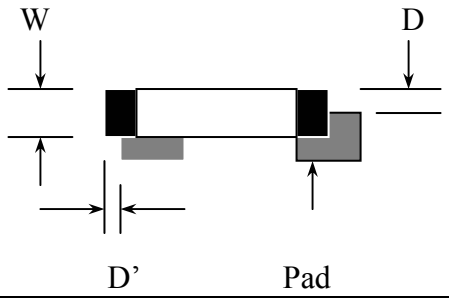


Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2 Inspection Specification

Inspection Standard：MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ。
Equipment：Gauge、MIL-STD、Powertip Tester、Sample。
IQC Defect Level：Major Defect AQL 0.4; Minor Defect AQL 1.5。
FQC Defect Level：100% Inspection。
OUT Going Defect Level：Sampling。
Specification：

NO	Item	Specification	Judge	Level
1	Part Number	The part number is inconsistent with work order of production	N.G.	Major
2	Quantity	The quantity is inconsistent with work order of production	N.G.	Major
3	Electronic characteristics of LCM A=(L + W)÷2	The display lacks of some patterns.	N.G.	Major
		Missing line.	N.G.	Major
		The size of missing dot, A is＞1/2 Dot size	N.G.	Major
		There is no function.	N.G.	Major
		Output data is error	N.G.	Major
4	Appearance of LCD A=(L + W)÷2 Dirty particle (Including scratch、bubble)	Material is different with work order of production	N.G.	Major
		LCD is assembled in inverse direction	N.G.	Major
		Bezel is assembled in inverse direction	N.G.	Major
		Shadow is within LCD viewing area + 0.5 mm	N.G.	Major
		The diameter of dirty particle, A is＞0.4 mm	N.G.	Minor
		Dirty particle length is ＞3.0mm, and 0.01mm＜width ≦0.05mm	N.G.	Minor
		Display is without protective film	N.G.	Minor
		Conductive rubber is over bezel 1mm	N.G.	Minor
		Polarizer exceeds over viewing area of LCD	N.G.	Minor
		Area of bubble in polarizer, A＞1.0mm, the number of bubble is ＞1 piece.	N.G.	Minor
		0.4mm＜Area of bubble in polarizer, A＜1.0mm, the number of bubble is ＞4 pieces.	N.G.	Minor
5	Appearance of PCB A=(L + W)÷2	Burned area or wrong part number is on PCB	N.G.	Major
		The symbol, character, and mark of PCB are unidentifiable.	N.G	Minor
		The stripped solder mask , A is＞1.0mm	N.G.	Minor
		0.3mm＜stripped solder mask or visible circuit, A ＜ 1.0mm, and the number is ≧4 pieces	N.G.	Minor
		There is particle between the circuits in solder mask	N.G	Minor
		The circuit is peeled off or cracked	N.G	Minor
		There is any circuits risen or exposed.	N.G	Minor
		0.2mm＜Area of solder ball, A is ≦0.4mm The number of solder ball is ≧3 pieces	N.G	Minor
		The magnitude of solder ball, A is ＞0.4mm.	N.G	Minor

NO	Item	Specification	Judge	Level
6	Appearance of molding A=(L + W)÷2	The shape of modeling is deformed by touching.	N.G.	Major
		Insufficient epoxy: Circuit or pad of IC is visible	N.G.	Minor
		Excessive epoxy: Diameter of modeling is ＞20mm or height is ＞2.5mm	N.G.	Minor
		The diameter of pinhole in modeling, A is ＞0.2mm.	N.G.	Minor
7	Appearance of frame A=(L + W)÷2	The folding angle of frame must be ＞45° +10°	N.G.	Minor
		The area of stripped electroplate in top-view of frame, A is ＞1.0mm.	N.G.	Minor
		Rust or crack is (Top view only)	N.G.	Minor
		The scratched width of frame is ＞0.06mm. (Top view only)	N.G.	Minor
8	Electrical characteristic of backlight A=(L + W)÷2	The color of backlight is nonconforming	N.G.	Major
		Backlight can't work normally.	N.G.	Major
		The LED lamp can't work normally	N.G.	Major
		The unsoldering area of pin for backlight, A is ＞1/2 solder joint area.	N.G.	Minor
		The height of solder pin for backlight is ＞2.0mm	N.G.	Minor
10	Assembly parts A=(L + W)÷2	The mark or polarity of component is unidentifiable.	N.G.	Minor
		The height between bottom of component and surface of the PCB is floating ＞0.7mm	N.G.	Minor
		D＞1/4W 	N.G.	Minor
		End solder joint width, D' is ＞50% width of component termination or width of pad	N.G.	Minor
		Side overhang, D is ＞25% width of component termination.	N.G.	Minor
		Component is cracked, deformed, and burned, etc.	N.G.	Minor
		The polarity of component is placed in inverse direction.	N.G.	Minor
		Maximum fillet height of solder extends onto the component body or minimum fillet height is ＜0.5mm.	N.G.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition

NO	Item	Test Condition	
1	High Temperature Storage	Storage at 70 ±2℃ 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs	
2	Low Temperature Storage	Storage at -20 ±2℃ 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs	
3	High Temperature /Humidity Storage	1.Storage 96~100 hrs 60±2℃, 90~95%RH surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer). or 2.Storage 96~100 hrs 40±2℃, 90~95%RH surrounding temperature, then storage at normal condition 4 hrs.	
4	Temperature Cycling	-20℃ → 25℃ → 70℃ → 25℃ (30mins) (5mins) (30mins) (5mins) 10 Cycle	
5	Vibration	10~55Hz (1 minute) 1.5mm X,Y and Z direction * (each 2hrs)	
6	ESD Test	Air Discharge: Apply 6 KV with 5 times discharge for each polarity +/-	Contact Discharge: Apply 250V with 5 times discharge for each polarity +/-
		Testing location: Around the face of LCD	Testing location: 1.Apply to bezel. 2.Apply to Vdd, Vss.
7	Drop Test	Packing Weight (Kg)	Drop Height (cm)
		0 ~ 45.4	122
		45.4 ~ 90.8	76
		90.8 ~ 454	61
		Over 454	46

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands , this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320±10℃and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25℃ ±5℃ and the humidity is below 65% RH.

5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company’s specification as a part for use in your company’s general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.