SPECIFICATIONS

CUSTOMER	:	OKAYA
SAMPLE CODE	:	PS1602LRS-JWA-B-01
MASS PRODUCTION CODE	:	PC1602LRS-JWA-B-Q
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	001
DRAWING NO. (Ver.)	:	JLMD-PC1602LRS-JWA-B-Q_001
PACKAGING NO. (Ver.)	:	JPKG-PC1602LRS-JWA-B-Q_001

Customer Approved

Date:

POWERTIP
2011.07.22
JS RD APPROVED

Approved	Checked	Designer
閆偉	閆偉	劉進

□ Preliminary specification for design input

■ Specification for sample approval

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RECORDS OF REVISION

Date	Ver	Edi	Description	Page	Design by
10/30/2004	0	-	New sample		
07/14/2011	01	001	Update Specification	-	劉進

Total：29 Pages

Contents

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 characters
LCD Type	STN , Gray, Positive , Transflective , Normal Temp
Driver Condition	LCD Module: 1/16uty , 1/4Bias
Viewing Direction	6 O'clock
Backlight	Yellow-green LED Backlight
Weight	35 g
Interface	4 bit or 8 bit MPU interface
Driver IC	ST7066U,KS0065B
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web side : http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	85.0（L）*30.0（W）*13.6max（H）	mm
Viewing Area	64.5(L)*17.2(W)	mm
Active Area	57.7(L)*9.4(W)	mm
Dot Size	0.50(L)*0.55(W)	mm
Dot Pitch	0.55(L)*0.60(W)	mm

Note：For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	VDD	—	-0.3	7.0	V
LCD Driver Supply Voltage	VLCD	—	VDD-10.0	VDD+0.3	V
Input Voltage	VIN	—	-0.3	VDD+0.3	V
Operating Temperature	TOP	Excluded B/L	0	50	℃
Storage Temperature	TST	Excluded B/L	-20	70	
Storage Humidity	Hd	Ta<40 ℃	-	90	%RH

1.4 DC Electrical Characteristics

VDD =5.0±10%V,Vss=0V，Ta=25℃

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	VDD	-	4.5	5.0	5.5	V
“H” Input Voltage	V_{IH}	-	0.7VDD	-	VDD	V
“L” Input Voltage	V_{IL}	-	-0.3	-	0.6	V
“H” Output Voltage	VOH	IOH=-0.1mA	3.9	-	VDD	V
“L” Output Voltage	VOL	IOH=0.1mA	-	-	0.4	V
Supply Current	I_{DD}	VDD=5.0V	-	1.5	-	mA
LCM Driver Voltage	VOP*3	0℃	-	-	-	V
		25℃*1	-	3.6	-	
		50℃	-	-	-	

NOTE: *1.THEV_{OP} test point is VDD-V0

1.5 Optical Characteristics

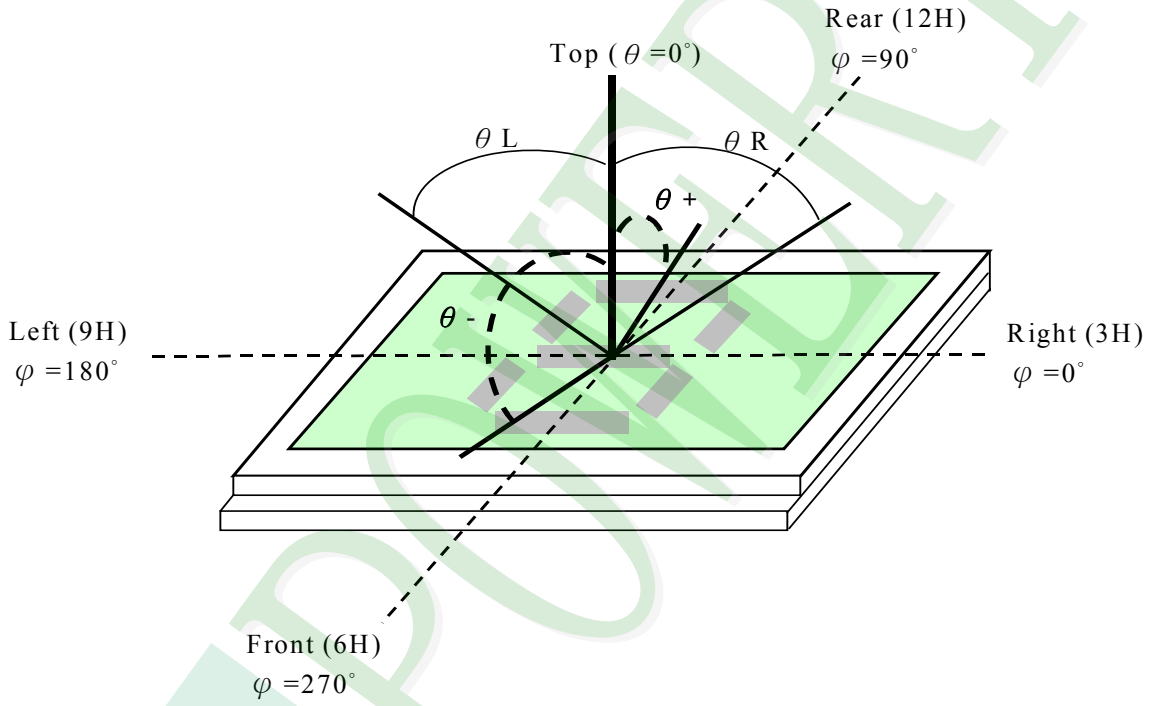
LCD Panel：1/16Duty，1/5Bias，Vlcd=4.8V,Ta=25 ℃

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	
Viewing angle range	Θ	$C \geq 2.0, \varnothing = 270^\circ$	45	-	-	Deg.	Note 1
Contrast ratio	CR	$\theta = 0^\circ, \varnothing = 270^\circ$	-	7	-	-	Note 3
Response time（Rise）	Tr	$C \geq 2.0, \varnothing = 270^\circ$	-	200	-	ms	Note 2
Response time（Fall）	Tf		-	300	-	ms	Note 2

Note 1.

Optical characteristics-2

Viewing angle

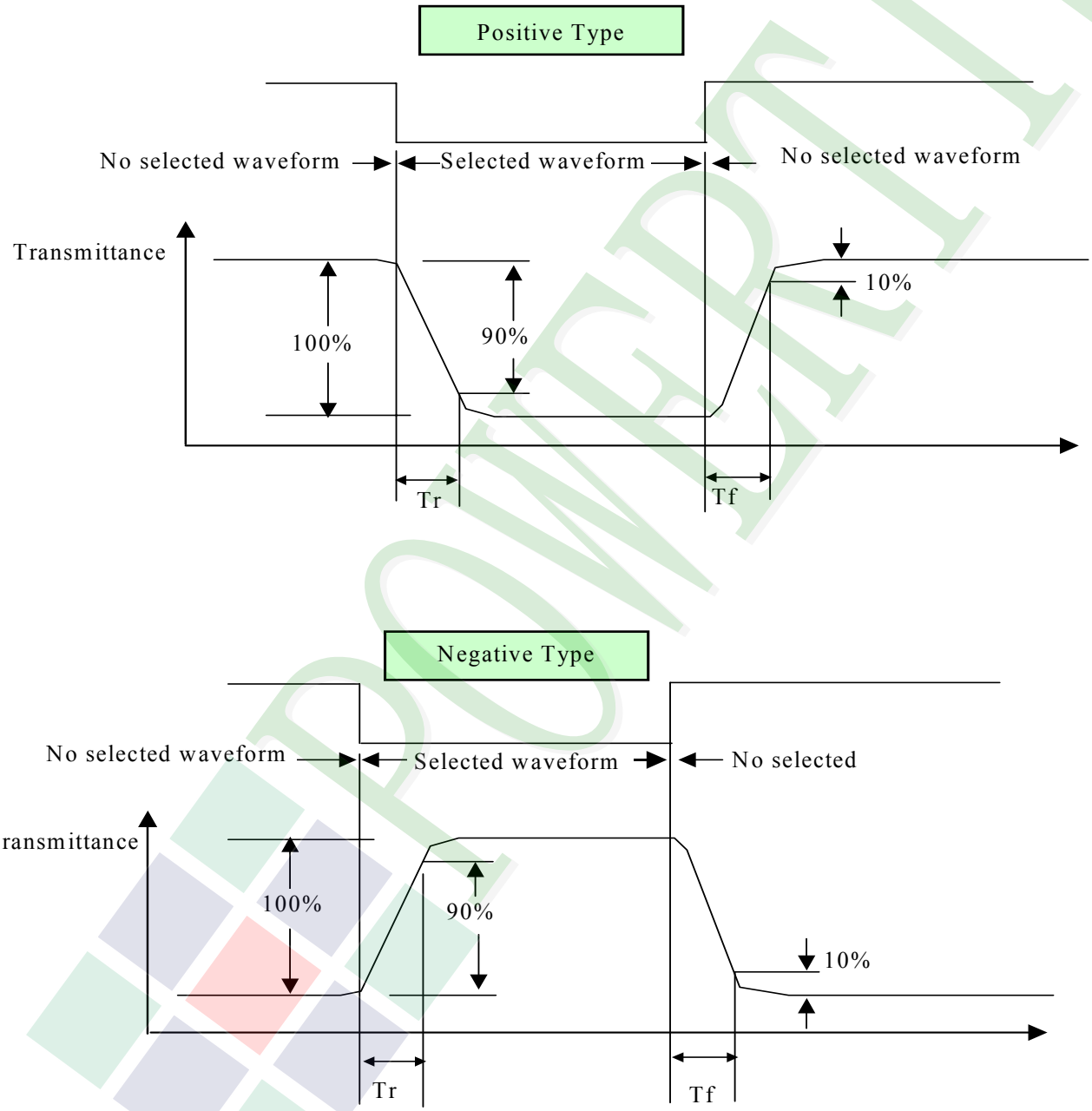


Viewing angle

Note 2.

Optical characteristics-3

Fig.2 Definition of response time



Electrical characteristics-2

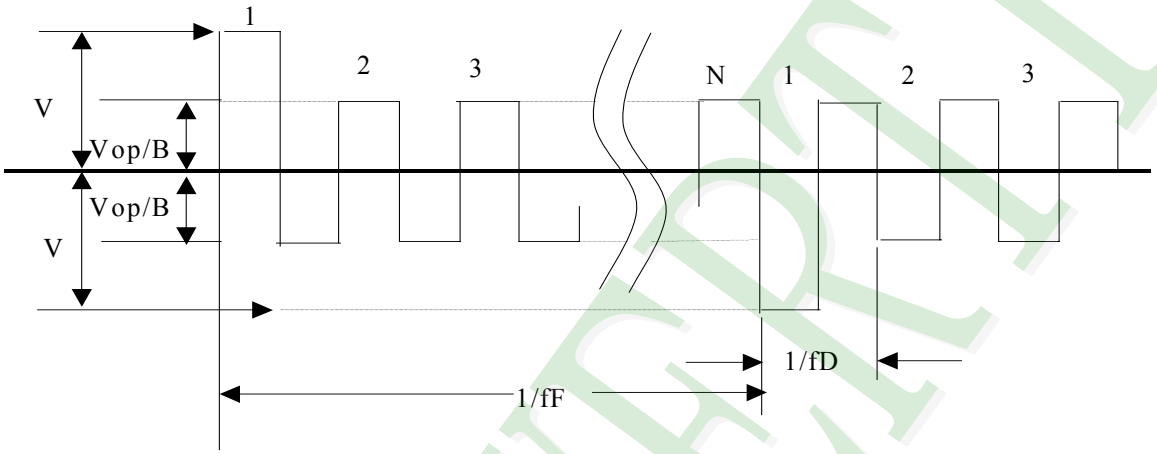
※2 Drive waveform

Vop: Drive voltage　　fF: Frame frequency

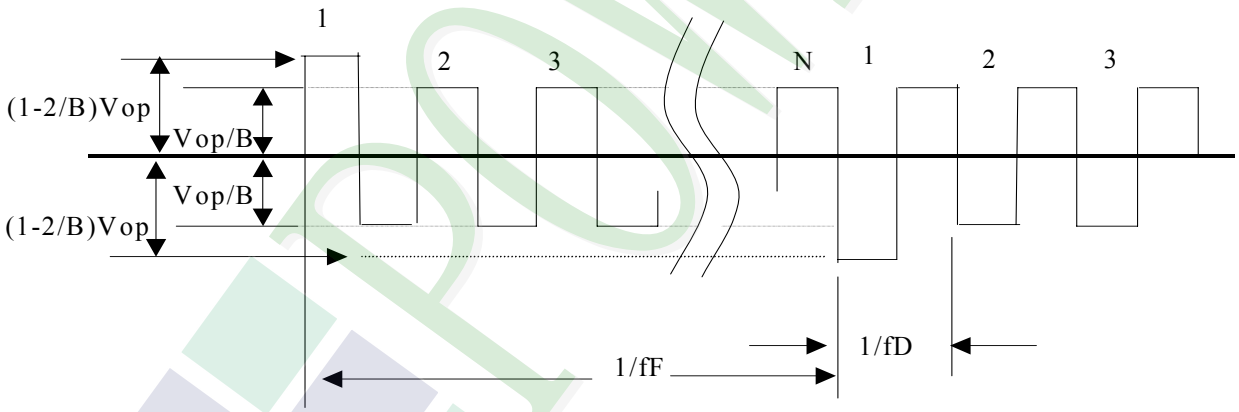
1/B: Bias　　fD: Drive frequency

N: Duty

(1) Selected waveform

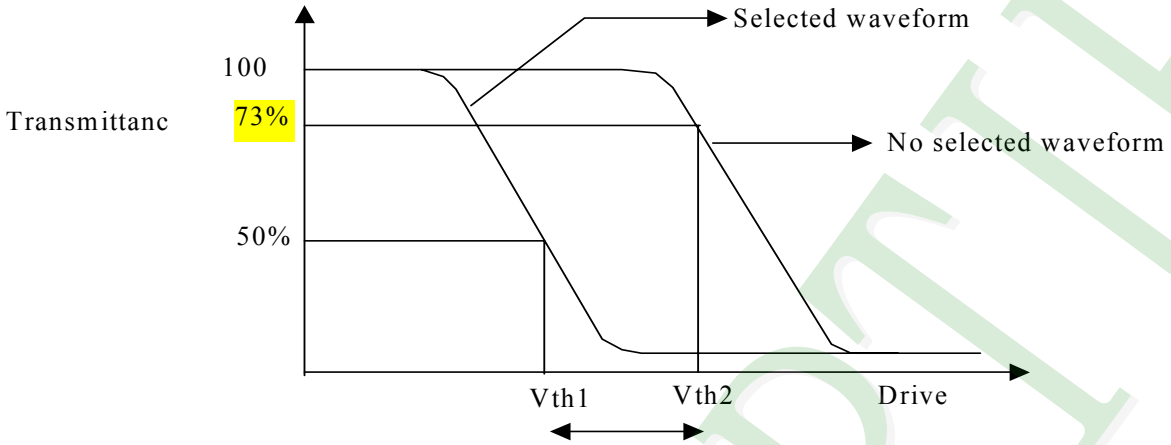


(2) Non- Selected waveform



Note:
Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period

Note 3. : Definition of Vth

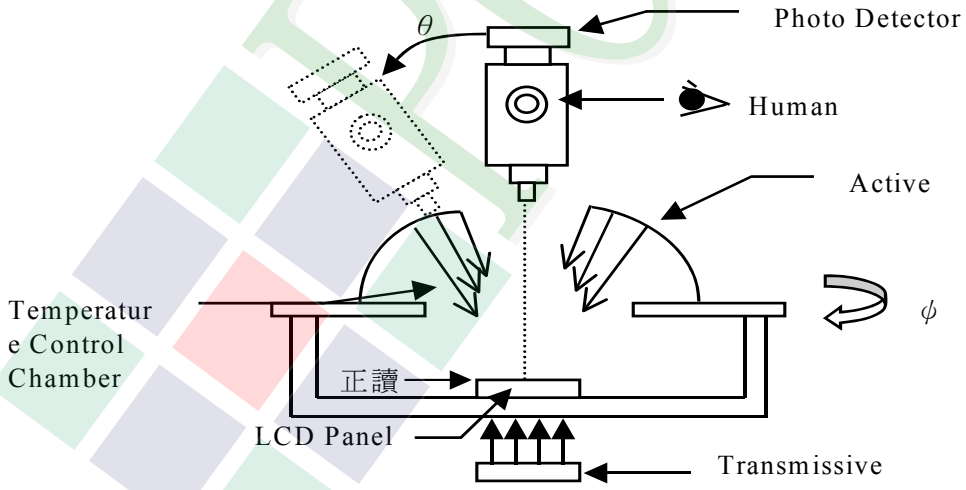


Active voltage range

	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio
= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System



Measuring System: Autronic DMS-803

1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25℃	-	275	mA
Reverse Voltage	VR	Ta =25℃	-	8	V
Power Dissipation	PO	Ta =25℃	-	1.21	W
Operating Temperature	T_{OP}	-	-20	70	℃
Storage Temperature	T_{ST}	-	-40	80	℃
Solder Temp.for 3 Second	-	-	-	260	℃

Electrical / Optical Characteristics

Ta =25℃

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 110A	-	4.0	4.4	V
Reverse Current	IR	VR= 8V	-	-	0.2	mA
Average Brightness (Without LCD)	IV	IF= 110mA	144	180	-	cd/m2
Wavelength	λ_p	IF= 110mA	571	-	576	nm
Color	Yellow-green					

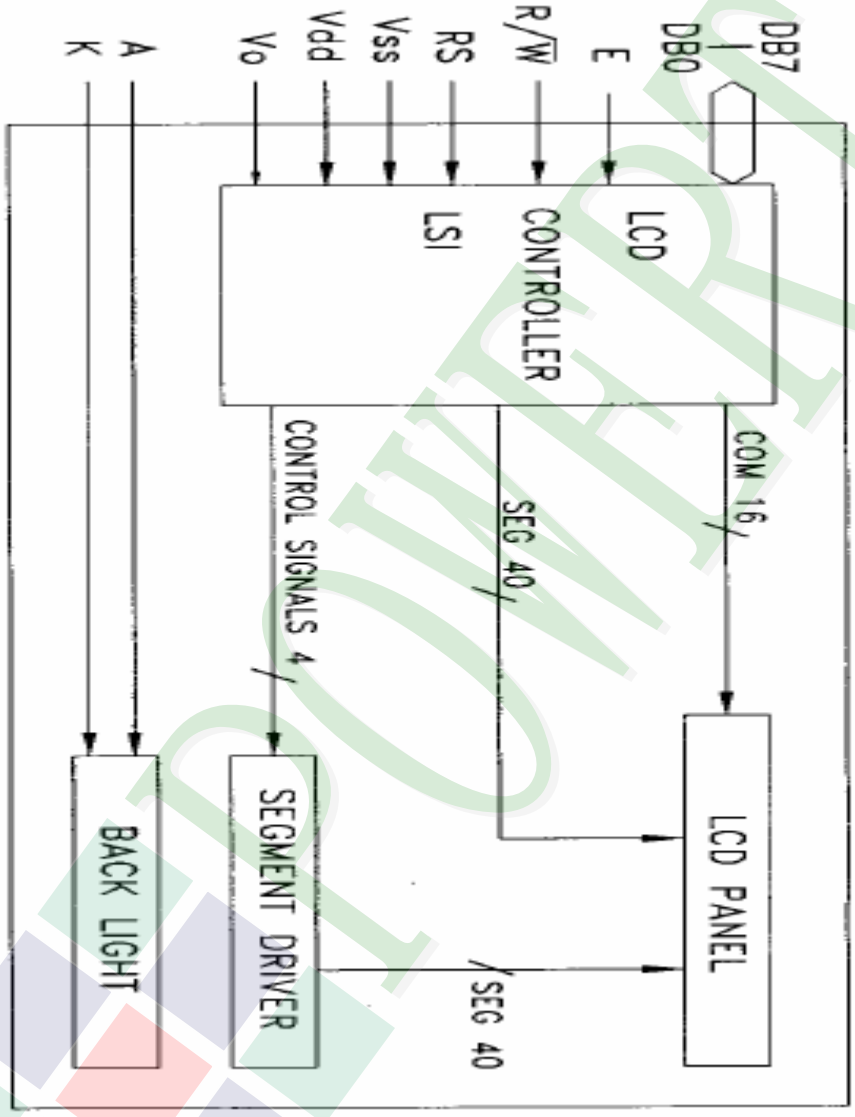
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

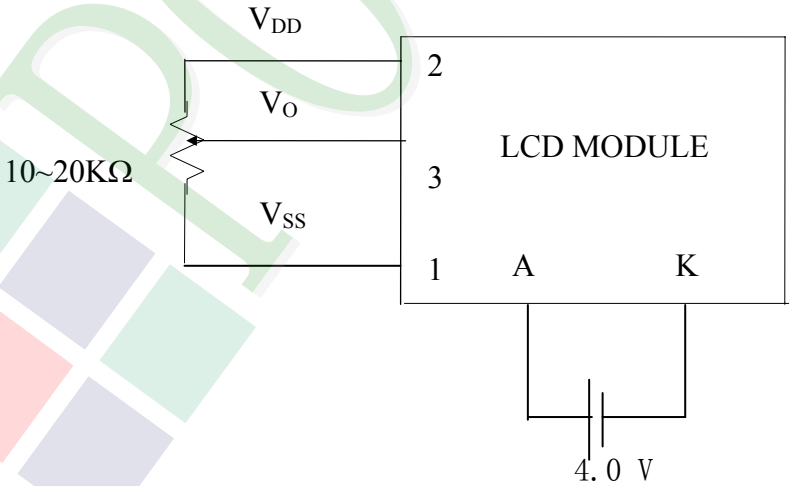
2.1.2 Block Diagram



2.2 Interface Pin Description

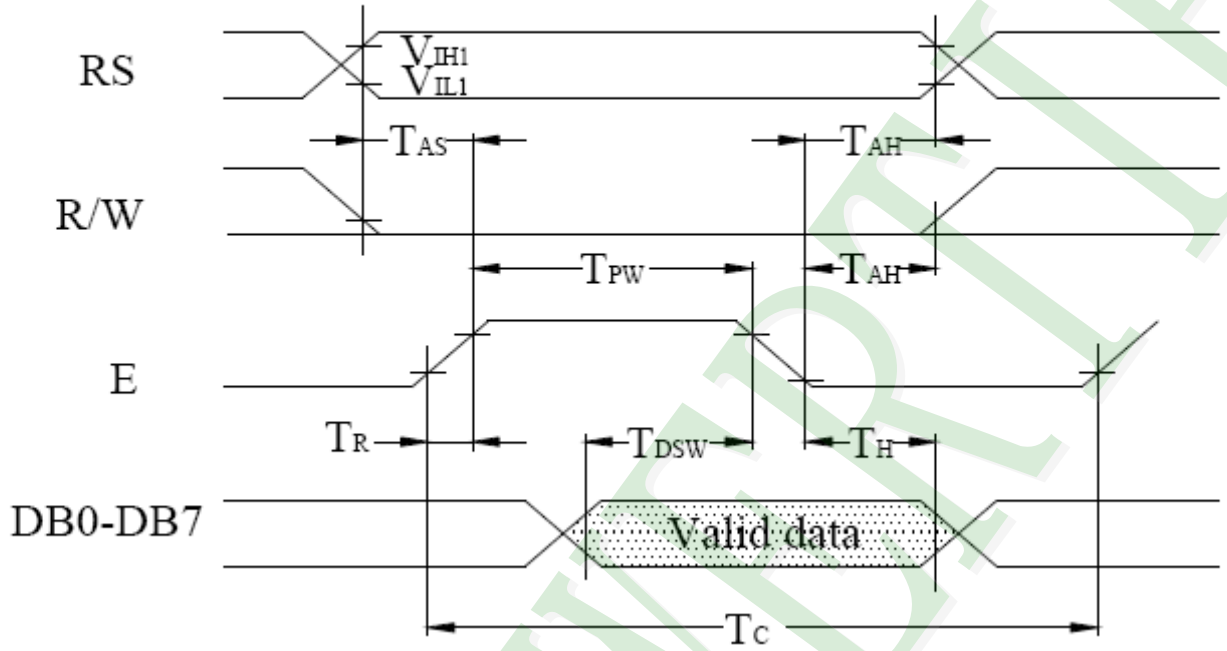
Pin No.	Symbol	Signal Description
1	VSS	Power Supply (V_{SS}=0)
2	VDD	Power Supply (V_{DD}>V_{SS})
3	VO	Operating voltage for LCD (variable)
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	R/$\overline{W}$	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7~10	DB0 ~ DB3	Four low order bi-directional three-state data bus lines. Use for data transfer between the MPU and the LCD module. These four are not used during 4-bit operation.
11~14	DB4 ~ DB7	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. DB7 can be used as a busy flag.

Contrast Adjust

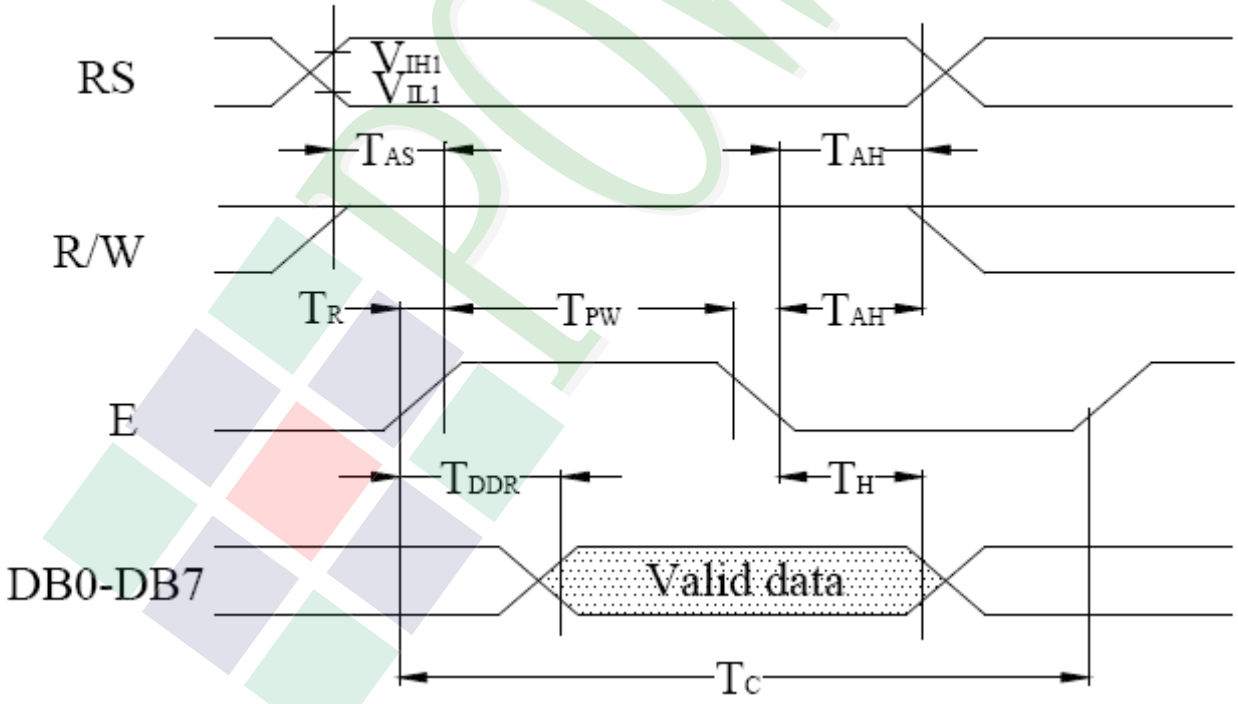


2.3 Timing Characteristics

Writing data from MPU to ST7066U



- Reading data from ST7066U to MPU



• Write Mode (Writing data from MPU to ST7066U)

(Vcc = +5V,Ta=25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R , T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DSW}	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
T_H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

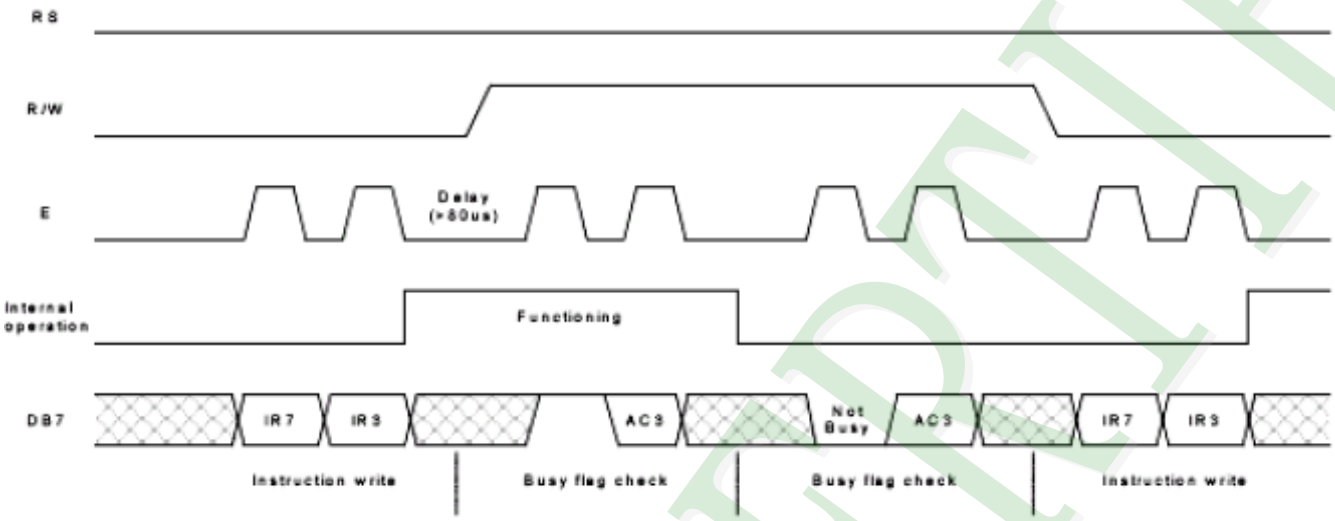
• Read Mode (Reading data from ST7066U to MPU)

(Vcc = +5V,Ta=25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R , T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T_H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

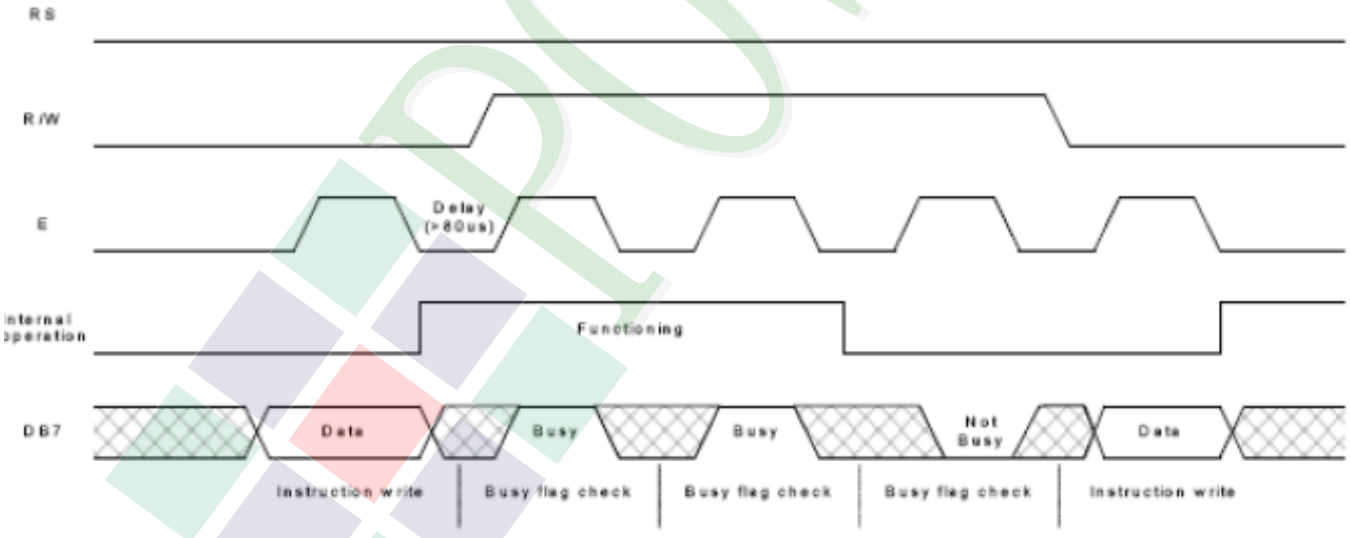
For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .

Example of busy flag check timing sequence



2.4 Display Command

Instructions	Instruction Code										Description	Description Time (270KHz)
	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read .	37μs
Display ON/OFF	0	0	0	0	0	0	1	D	C	B	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	37μs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37μs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37μs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37μs

Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0μs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37μs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37μs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag , the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF.

Refer to Instruction Table for the list of each instruction execution time.

2.5 Character Pattern

NO.7066-0A

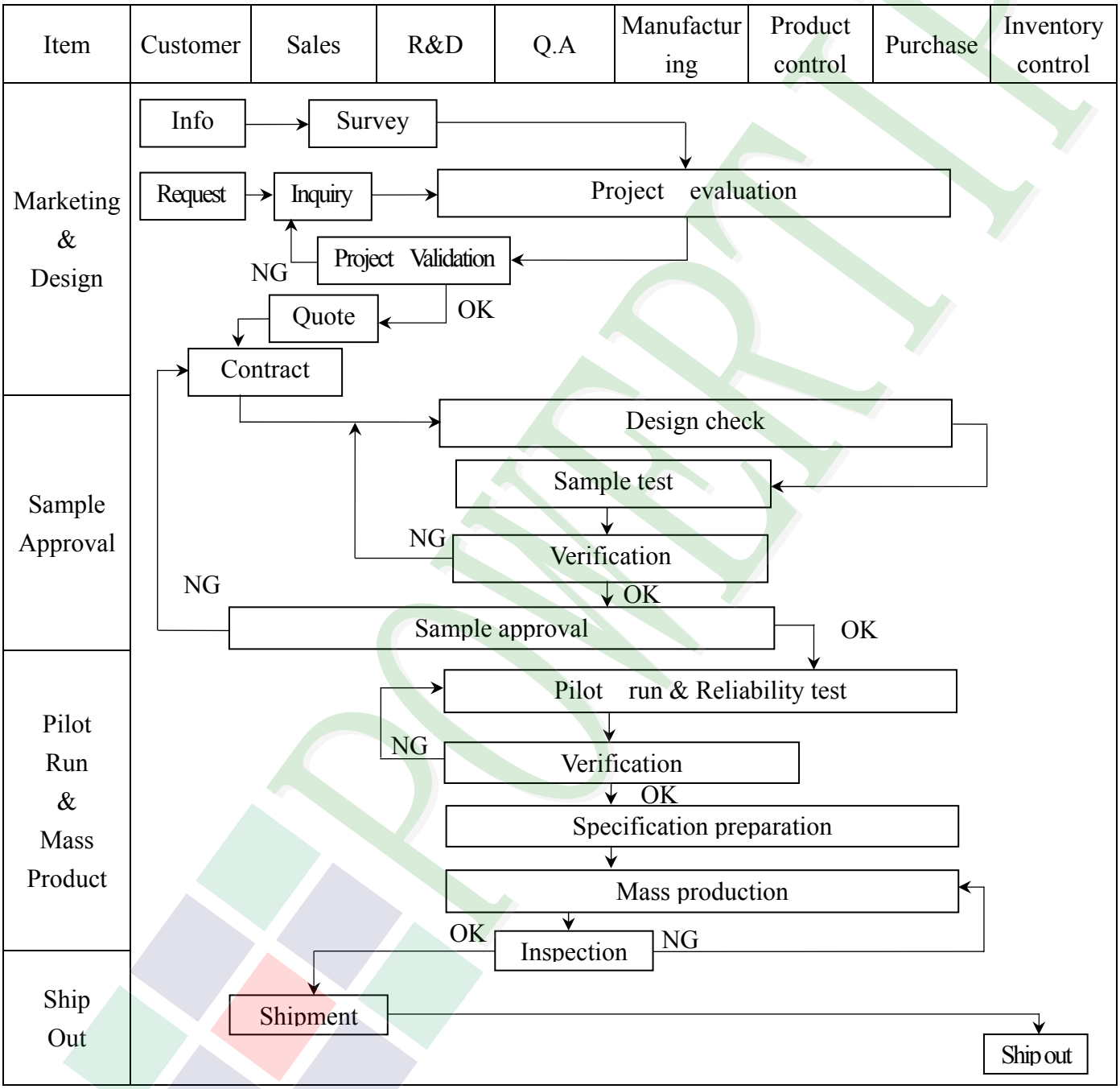
2.6 JUMPER（Setting different use）

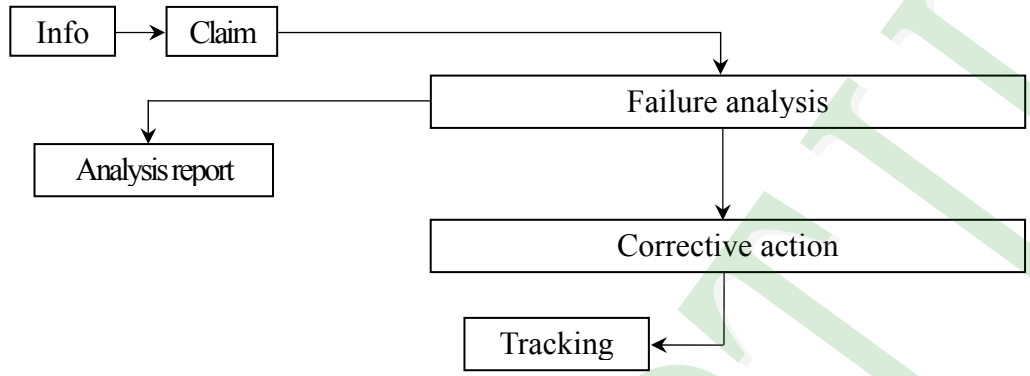
SHORT: J3/J5

OPEN: the others

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management					2. Process improvement proposal 4. Education And Training Activities		

3.2 Inspection Specification

◆Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ .

◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level : Major Defect AQL：0.4 ; Minor Defect：AQL： 1.5 .

◆OUT Going Defect Level : Sampling .

◆Manner of appearance test :

(1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.

(2). Standard of inspection： (Unit：mm)

(3). The test direction is base on about around 45°of vertical line. (Fig. 1)

(4). Definition of area . (Fig. 2)

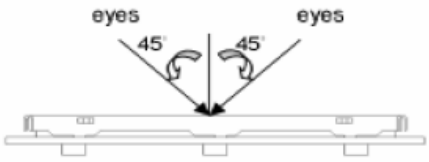


Fig.1

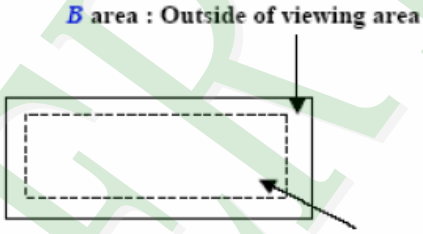


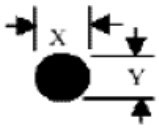
Fig. 2 A area : viewing area

◆ Specification:

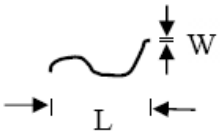
NO	Item	Criterion	Level
01	Product condition	1.1 The part number is inconsistent with work order of Production.	Major
		1.2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
04	Electrical Testing	4.1 Missing line character and icon.	Major
		4.2 No function or no display.	Major
		4.3 Output data is error.	Major
		4.4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major

◆Specification For Monotype and Color STN：　(Ver. B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>05</td><td>Black or white dot、scratch、contamination

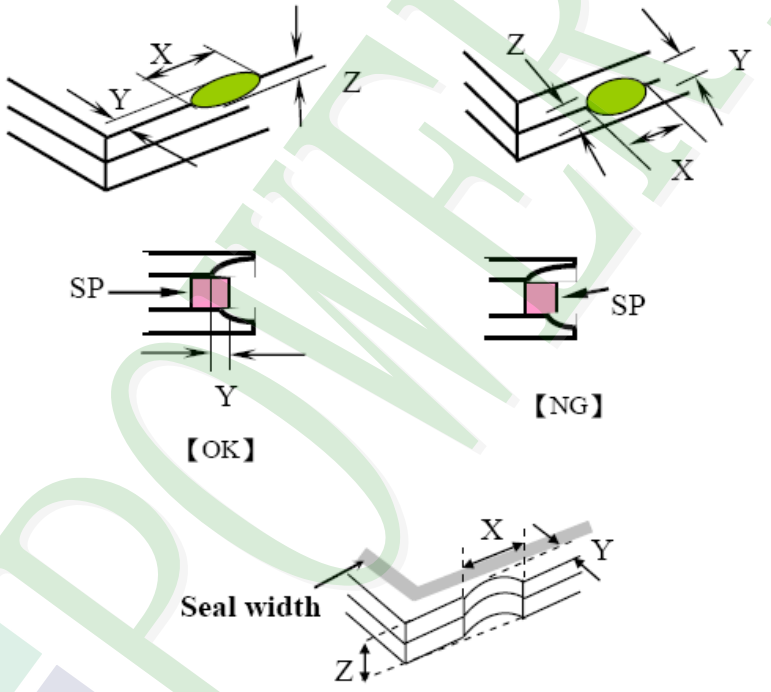
Round type
 

Φ=(x+y)/2

Line type
 
</td><td>5. 1 Round type:
5. 1. 1 display only :
• White and black spots on display ≦ 0. 30 mm , no more than 4 white or black spots present.
• Densely spaced : NO more than two spots or lines within 3 mm.

5. 1. 2 Non-display :
<table>
<tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr>
<tr><th>A area</th><th>B area</th></tr>
<tr><td>Φ ≦ 0. 10</td><td>Accept no dense</td><td rowspan="4">Ignore</td></tr>
<tr><td>0. 10 ＜ Φ ≦ 0. 20</td><td>3</td></tr>
<tr><td>0. 20 ＜ Φ ≦ 0. 30</td><td>2</td></tr>
<tr><td>Total quantity</td><td>4</td></tr>
</table>
5. 1. 3 Line type:
<table>
<tr><th colspan="2">Dimension</th><th colspan="2">Acceptance (Q'ty)</th></tr>
<tr><th>Length (L)</th><th>Width (W)</th><th>A area</th><th>B area</th></tr>
<tr><td>---</td><td>W ≦ 0. 03</td><td>Accept no dense</td><td rowspan="3">Ignore</td></tr>
<tr><td>L ≦ 3. 0</td><td>0. 03 ＜ W ≦ 0. 05</td><td rowspan="2">4</td></tr>
<tr><td>L ≦ 2. 5</td><td>0. 05 ＜W ≦ 0. 075</td></tr>
<tr><td>---</td><td>W ＞0. 075</td><td colspan="2">As round type</td></tr>
</table></td><td>Minor</td></tr>
<tr><td>06</td><td>Polarizer Bubble</td><td>
<table>
<tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr>
<tr><th>A area</th><th>B area</th></tr>
<tr><td>Φ ≦ 0. 20</td><td>Accept no dense</td><td rowspan="5">Ignore</td></tr>
<tr><td>0. 20 ＜Φ ≦ 0. 50</td><td>3</td></tr>
<tr><td>0. 50 ＜Φ ≦ 1. 00</td><td>2</td></tr>
<tr><td>Φ ＞ 1. 00</td><td>0</td></tr>
<tr><td>Total quantity</td><td>4</td></tr>
</table></td><td>Minor</td></tr>
</table>

◆Specification For Monotype and Color STN：　(Ver. B01)

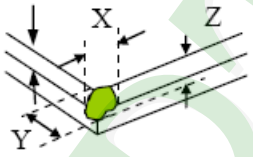
NO	Item	Criterion	Level
07	The crack of glass	Symbols： X : The length of crack　Y : The width of crack. Z : The thickness of crack　W : terminal length t : The thickness of glass　a : LCD side length 7.1 General glass chip： 7.1.1 Chip on panel surface and crack between panels: 	Minor

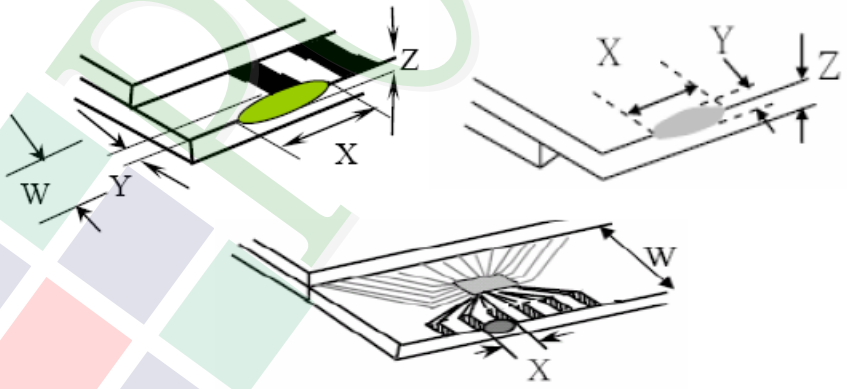
X	Y	Z
≦ a	Crack can't enter viewing area	≦1/2 t
≦ a	Crack can't exceed the half of SP width.	1/2 t < Z ≦2 t

◆Specification For Monotype and Color STN：　(Ver. B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>07</td><td>The crack of glass</td><td>
Symbols：

X : The length of crack　　Y : The width of crack.
Z : The thickness of crack　　W : terminal length
t : The thickness of glass　　a : LCD side length

7.1.2 Corner crack：


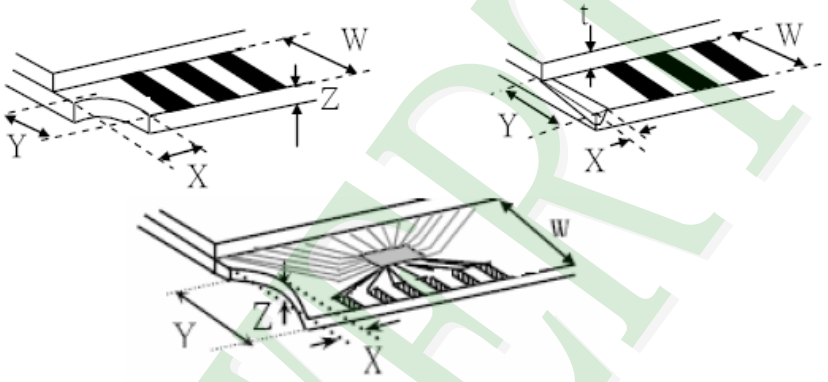
<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5 a</td><td>Crack can’t enter viewing area</td><td>Z ≦ 1/2 t</td></tr>
<tr><td>≦1/5 a</td><td>Crack can’t exceed the half of SP width.</td><td>1/2 t < Z ≦ 2 t</td></tr>
</table>
7.2 Protrusion over terminal：
7.2.1 Chip on electrode pad：


<table>
<tr><th></th><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>Front</td><td>≦ a</td><td>≦ 1/2 W</td><td>≦ t</td></tr>
<tr><td>Back</td><td colspan="3">Neglect</td></tr>
</table>
</td><td>Minor</td></tr>
</table>

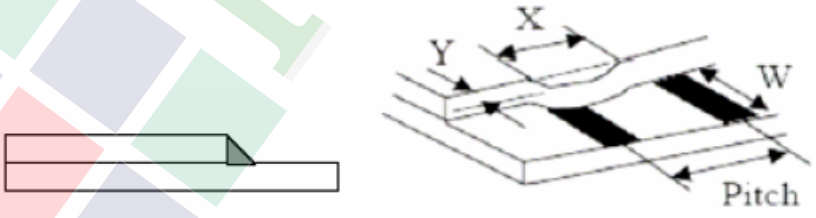
◆Specification For Monotype and Color STN：　(Ver. B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>07</td><td>The crack of glass</td><td>
Symbols：

X : The length of crack　　Y : The width of crack.
Z : The thickness of crack　　W : terminal length
t : The thickness of glass　　a : LCD side length

7.2.2 Non-conductive portion：


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/3 a</td><td>≦W</td><td>≦t</td></tr>
</table>
⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.

7.2.3 Glass remain：


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ a</td><td>≦ 1/3 W</td><td>≦ t</td></tr>
</table>
</td><td>Minor</td></tr>
</table>

◆Specification For Monotype and Color STN：　(Ver. B01)

NO	Item	Criterion	Level
08	Backlight elements	8. 1 Backlight can't work normally.	Major
		8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
09	General appearance	9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
		9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≦1. 5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition (Ver.B01)

NO.	TEST ITEM	TEST CONDITION
1	High Temperature Storage Test	Keep in 70℃ ±2℃ 96 hrs Surrounding temperature, then storage at normal condition 4hrs.
2	Low Temperature Storage Test	Keep in -20℃ ±2℃ 96 hrs Surrounding temperature, then storage at normal condition 4hrs.
3	High Temperature / High Humidity Storage Test	Keep in 40℃ / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)
4	Temperature Cycling Storage Test	-20℃ → +25℃ → 70℃ → +25℃ (30mins) (5mins) (30mins) (5mins) 10 Cycle Surrounding temperature, then storage at normal condition 4hrs.
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/- 1. Temperature ambiance : 15℃～35℃ 2. Humidity relative : 30%～60% 3. Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)
6	Vibration Test (Packaged)	1. Sine wave 10～55 Hz frequency (1 min/sweep) 2. The amplitude of vibration :1.5 mm 3. Each direction (X、Y、Z) duration for 2 Hrs
7	Drop Test (Packaged)	Packing Weight (Kg) / Drop Height (cm): 0 ~ 45.4 — 122 45.4 ~ 90.8 — 76 90.8 ~ 454 — 61 Over 454 — 46 Drop Direction :※1 corner / 3 edges / 6 sides each 1time

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands , this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320 ± 10°C and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.

5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush , shake , or jolt the module.

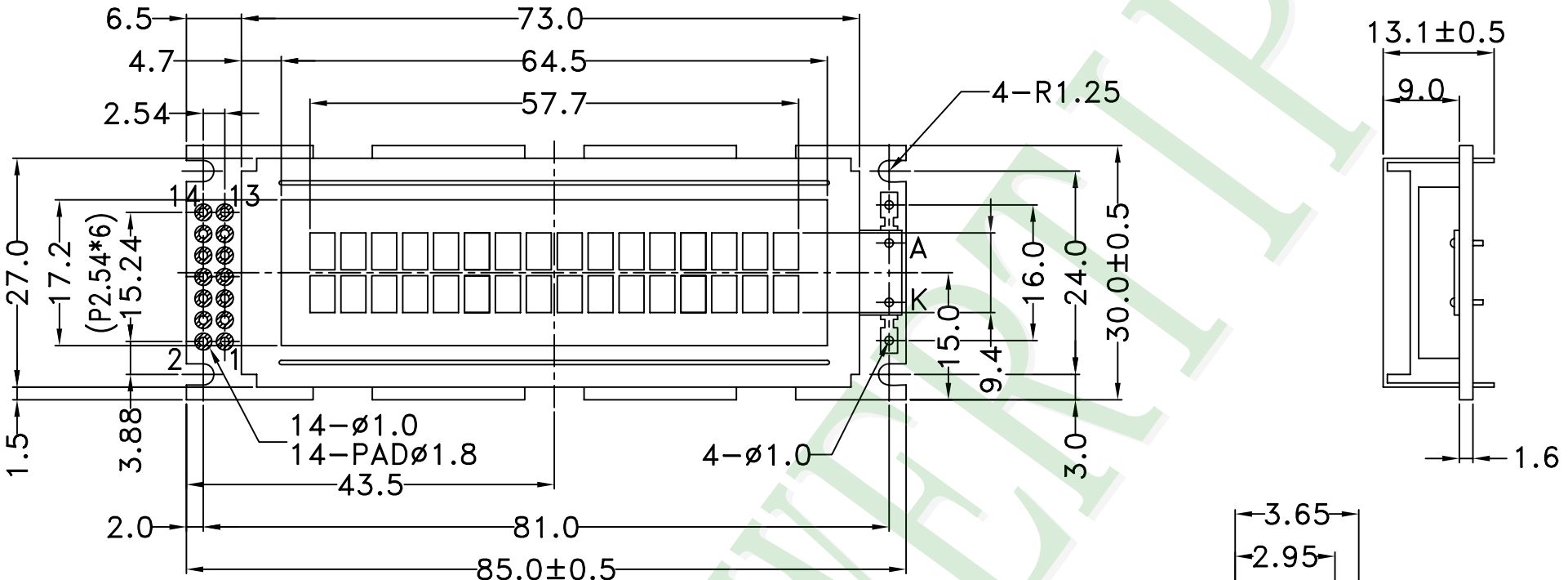
5.4 TERMS OF WARRANTY

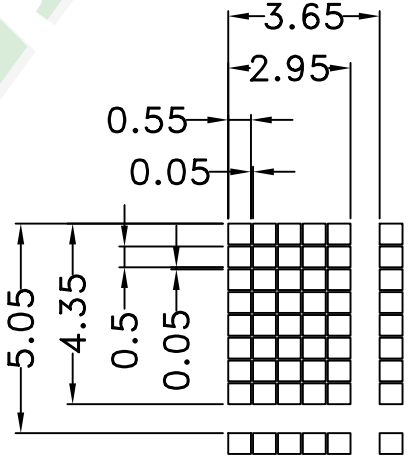
5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





SCALE:4/1

NOTE:

1. The tolerance unless classified ±0.3mm
2. LCD type :Positive,STN,Gray,Transflective
3. T_{op}:0°C~50°C , T_{st}:−10°C~60°C
4. This product confroms ROHS.

REV	REV BY	REVISER	DATE
007			
006			
005			
004			
003			
002			
001	NEW DRAWING	Sally	2011/07/15

PART NO: PC1602LRS-JWA-B-Q

DRAWING NAME : JLMD-PC1602LRS-JWA-B-Q

TITLE: LCD Module Drawing

久正光電股份有限公司
POWERTIP TECHNOLOGY CORPORATION

Design	Sally	(3)	Surface		
Check	Eddy	Unit	MM	Material	
Approve	Ryan	Scale	1:1	Thickness	
		Page	1/1	Quantity	

最小長度 (mm) \ 精度等級	精級
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

PT-A-054-01

Ver.001			
Documents NO.	JPKG-PC1602LRS-JWA-B-Q		

LCM包裝規格書
LCM Packaging Specifications

Approve	Check	Contact
Ryan	Eddy	Sally

1.包裝材料規格表 (Packaging Material) : (per carton)

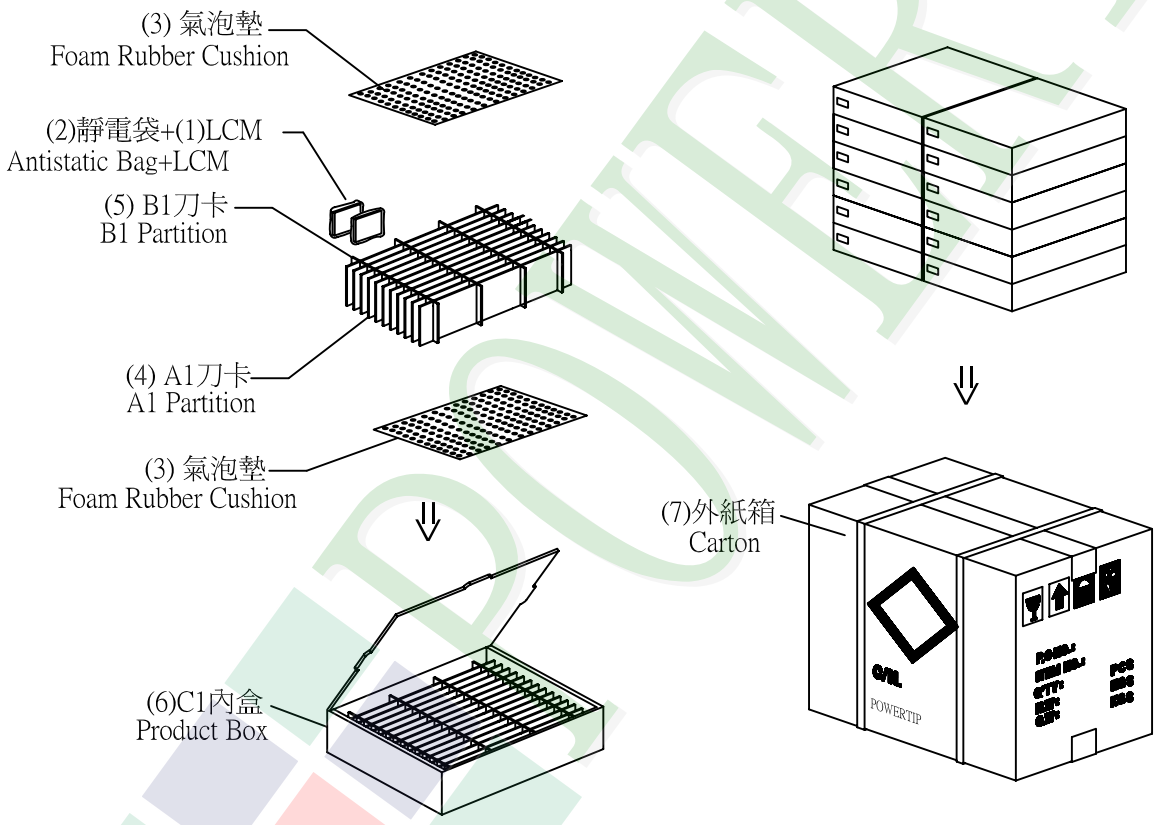
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品(1) (LCM)	PC1602LRS-JWA-B-Q	85.0*30.0*13.1	0.035	432	15.12
2	靜電袋 (2)BAG	BAG100100ARABA	100*100*0.05	0.003	432	1.296
3	氣泡墊(3)BAG	BAG290240BRBBA	240*290*5	0.0029	24	0.0696
4	刀卡A1(4)BX	BX29500047BZBA	295*47*3	0.0069	168	1.1592
5	刀卡B1(5)BX	BX24500047BZBA	245*47*4.5	0.009	48	0.432
6	C1內盒(6)Product Box	BX31025555AABA	310*255*55	0.1101	12	1.3212
7	外紙箱(7)Carton	BX52532536CCBA	525 * 325 *360	1.092	1	1.092
8						
9						

2.一 整箱總重量 (Total LCD Weight in carton) : 20.49 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

(1)Quantity Of Spacer : A1刀卡 X 14 , B1刀卡 X 4

(2)Total LCM quantity in carton : quantity per box 36 x no. of boxes 12 = 432



特 記 事 項 (REMARK)

1. Label Specifications :

MODEL:
LOT NO:
QUANTITY:
CHECK:

將模組LCD面朝外放入刀卡間隔內,
啤盒前、後各空一欄