

POWERTIP TECH. CORP.

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

Specification For Approval

Customer : _____

Model Type : LCD Module

Sample Code : PC2004LRS-ASO-B-SB

Mass Production Code : _____

Edition : B

Customer Sign	Sales Sign	Approved By	Prepared By

CONTENTS

1.SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
- 1.6 Backlight Characteristics

2.MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics
- 2.4 Display Command
- 2.5 Character Pattern

Appendix 2

Out Going Inspection Specification



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

1. SPECIFICATIONS

1.1 Features

- i 20-characters, four-lines liquid crystal display of 5*7 dot matrix + cursor
- i 1/32 Duty, 1/5 bias
- i STN LCD, positive, gray display
- i Transflective LCD
- i 6 o'clock viewing angle
- i 8 bits parallel data input
- i Built-in LED backlight

1.2 Mechanical Specifications

- i Outline dimension : 98.0mm(L)* 60.0mm(W)*13.8mm max.(H)
- i Viewing area : 76.0mm *25.2mm
- i Active area : 70.4mm *20.8mm
- i Dot size : 0.55mm *0.55mm
- i Dot pitch : 0.6mm *0.6mm
- i Character Size : 2.95mm *4.75mm

1.3 Absolute Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Power supply Voltage	VDD	-	-0.3	7.0	V
LCD drive Supply voltage	VDD-VO	-	V _{DD} -15	V _{DD} +0.3	V
Input voltage	VIN	-	-0.3	V _{DD} +0.3	V
Operating temperature	TOPR	-	0	50	°C
Storage temperature	TSTG	-	-10	60	°C
Humidity	HD	-	-	90	%RH

1.4 DC Electrical Characteristics

V_{DD}=+5V±10%, V_{SS}=0V, T_A=25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply voltage	VDD	-	4.5	5.0	5.5	V
“H” input voltage	V _{IH}	-	2.2	-	V _{DD}	V
“L” input voltage	V _{IL}	-	-0.3	-	0.6	V
“H” output voltage	V _{OH}	-	2.4	-	-	V
“L” output voltage	V _{OL}	-	-	-	0.4	V
Supply current	I _{DD}	V _{DD} =5V	-	2.34	2.80	mA
LCD driving voltage	V _{OP}	V _{DD} -V _O	-	4.24	4.7	V



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

1.5 Optical Characteristics

1/32 duty, 1/5bias, $V_{OPR}=4.3V$ $T_a=25^{\circ}C$

Item	Symbol	Conditions	Min.	Typ.	Max	Reference
Viewing angle	θ	$C \geq 2.0, \varnothing = 0^{\circ}$	30°	-	-	Notes 1 & 2
Contrast	C	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	-	4	-	Note 3
Response time(rise)	T_r	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	-	120ms	180ms	Note 4
Response time(fall)	T_f	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	-	130ms	200ms	Note 4

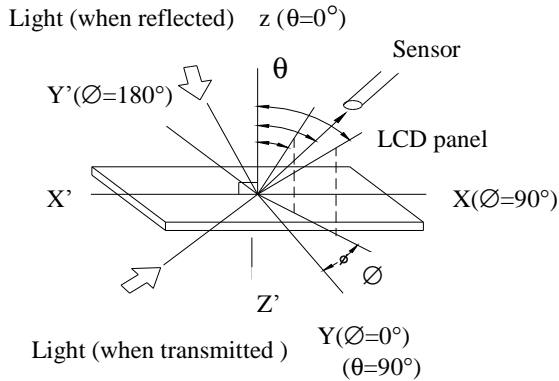
Parameter	Symbol	Temperature ($^{\circ}C$)	Standard			Unit
			Min	Typ	Max	
Driving voltage	V_{OP}	0	4.5	4.8	5.1	V
		25	4.1	4.4	4.7	
		50	3.8	4.1	4.4	



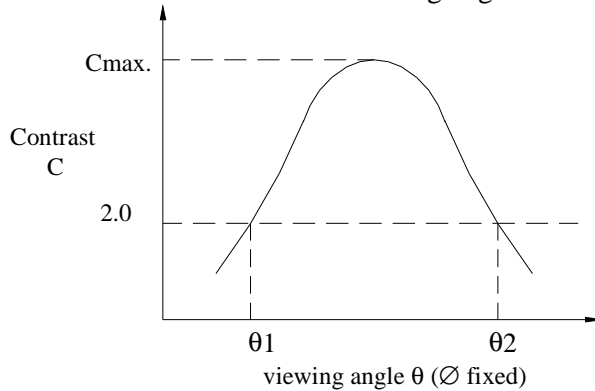
POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

Note 1: Definition of angles θ and \varnothing



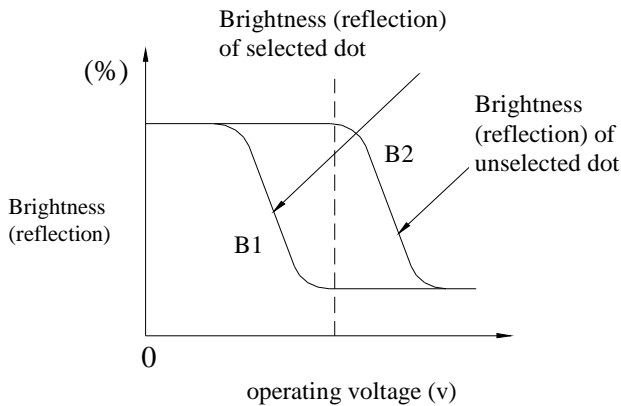
Note 2: Definition of viewing angles θ_1 and θ_2



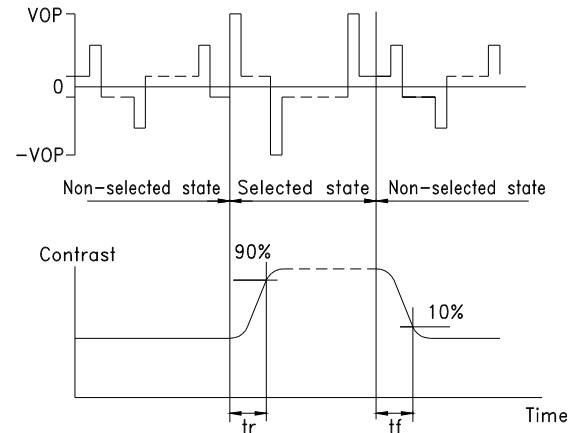
Note : Optimum viewing angle with the naked eye and viewing angle θ at C_{max} . Above are not always the same

Note 3: Definition of contrast C

$$C = \frac{\text{Brightness (reflection) of unselected dot (B2)}}{\text{Brightness (reflection) of selected dot (B1)}}$$



Note 4: Definition of response time



Note: Measured with a transmissive LCD panel which is displayed 1 cm^2

V_{OPR} : Operating voltage f_{FRM} : Frame frequency
 t_r : Response time (rise) t_f : Response time (fall)



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

1.6 Backlight Characteristic

The LCD Module is backlight using a LED panel

- Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward current	IF	TA=25°C	-	650	mA
Reverse voltage	VR	TA=25°C	-	8	V
Power dissipation	PO	TA=25°C	-	3.0	W
Operating Temperature	TOPR	-	-20	70	°C
Storage temperature	TSTG	-	-40	80	°C

- Electrical Ratings

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward voltage	VF	IF=260mA	-	4.2	4.6	V
Reverse current	IR	VR=8V	-	-	0.2	mA
Luminous intensity (without LCD)	IV	IF=260mA	200	250	-	cd/m ²
Luminous intensity (with LCD)	IV	IF=260mA	-	74.0	-	cd/m ²
Wavelength	λ_p	IF=260mA	571	-	576	nm
Color	Yellow Green					



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

2. MODULE STRUCTURE

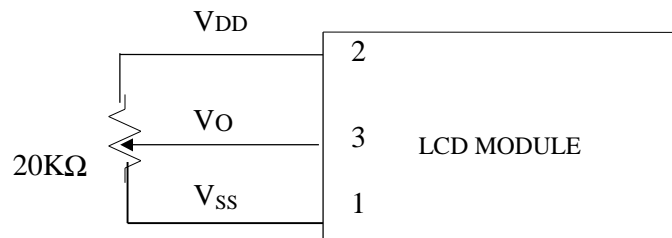
2.1 Counter Drawing

*See Appendix 1

2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	VSS	Power Supply ($V_{SS}=0$)
2	VDD	Power Supply ($V_{DD}>V_{SS}$)
3	VO	Operating voltage for LCD (variable)
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	$\overline{\text{R/W}}$	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7~10	DB0 ~ DB3	Four low order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. These four are not used during 4-bit operation.
11~14	DB4 ~ DB7	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. DB7 can be used as a busy flag.
15	A	Power supply for LED B / L (+)
16	K	Power supply for LED B / L (-)

Contrast Adjust

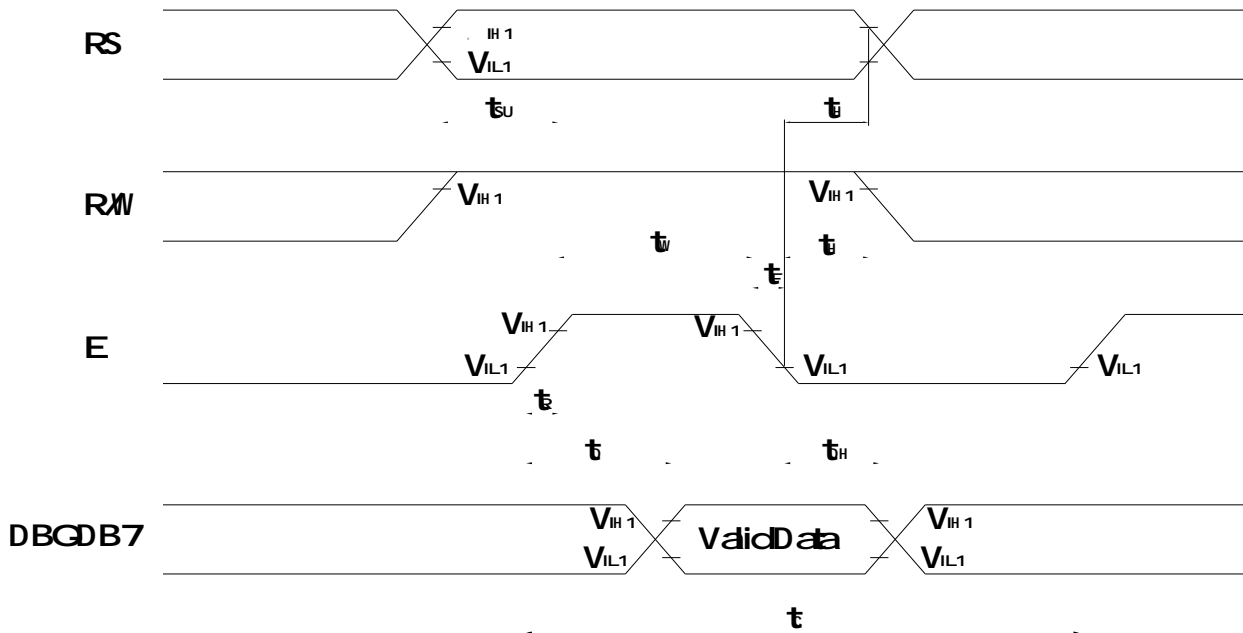


POWERTIP TECHNOLOGY CORPORATION

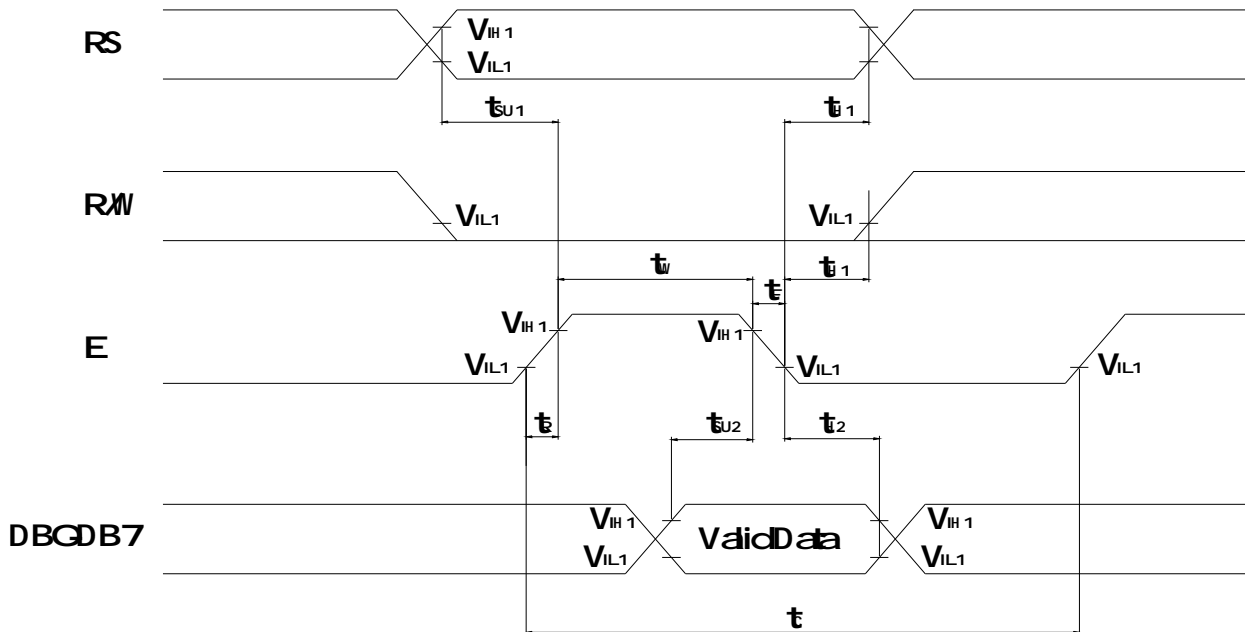
DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

2.3 Timing Characteristics

- Read cycle



- Write cycle



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

- Read cycle

 $V_{DD}=4.5V\sim 5.5V, T_a=-30\sim +85^{\circ}C$

Characteristics	Symbol	Min.	Typ.	Max.	Unit
E Cycle Time	t_C	500	-	-	ns
E Rise / Fall Time	t_R, t_F	-	-	20	ns
E Pulse Width (High, Low)	t_W	230	-	-	ns
R/W and RS Setup Time	t_{SU}	40	-	-	ns
R/W and RS Hold Time	t_H	10	-	-	ns
Data Output Delay Time	t_D	-	-	120	ns
Data Hold Time	t_{DH}	5	-	-	ns

- Write cycle

Characteristics	Symbol	Min.	Typ.	Max.	Unit
E Cycle Time	t_C	500	-	-	ns
E Rise / Fall Time	t_R, t_F	-	-	20	ns
E Pulse Width (High, Low)	t_W	230	-	-	ns
R/W and RS Setup Time	t_{SU1}	40	-	-	ns
R/W and RS Hold Time	t_{H1}	10	-	-	ns
Data Setup Time	t_{SU2}	80	-	-	ns
Data Hold Time	t_{H2}	10	-	-	ns



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

2.4 Display Command

Instructions	Instruction Code										Description	Execution Time (fosc = 270KHZ)	
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0			
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.53ms	
Return Home	0	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.53ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and make shift of entire display enable.	39μs	
Display ON/OFF Control	0	0	0	0	0	0	1	D	C	B	Sets display (D), cursor(C), and blinking of cursor(B) on/off control bit.	39μs	
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39μs	
Function Set	0	0	0	0	1	DL	N	F	×	×	Set interface data length (DL:4 - bit/8-bit), numbers of display line (N: 1-line/2-line), display font type(F:5*8 dots/5*11 dots)	39μs	
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39μs	
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39μs	
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0μs	
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43μs	
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43μs	

※ "x":don't care



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN

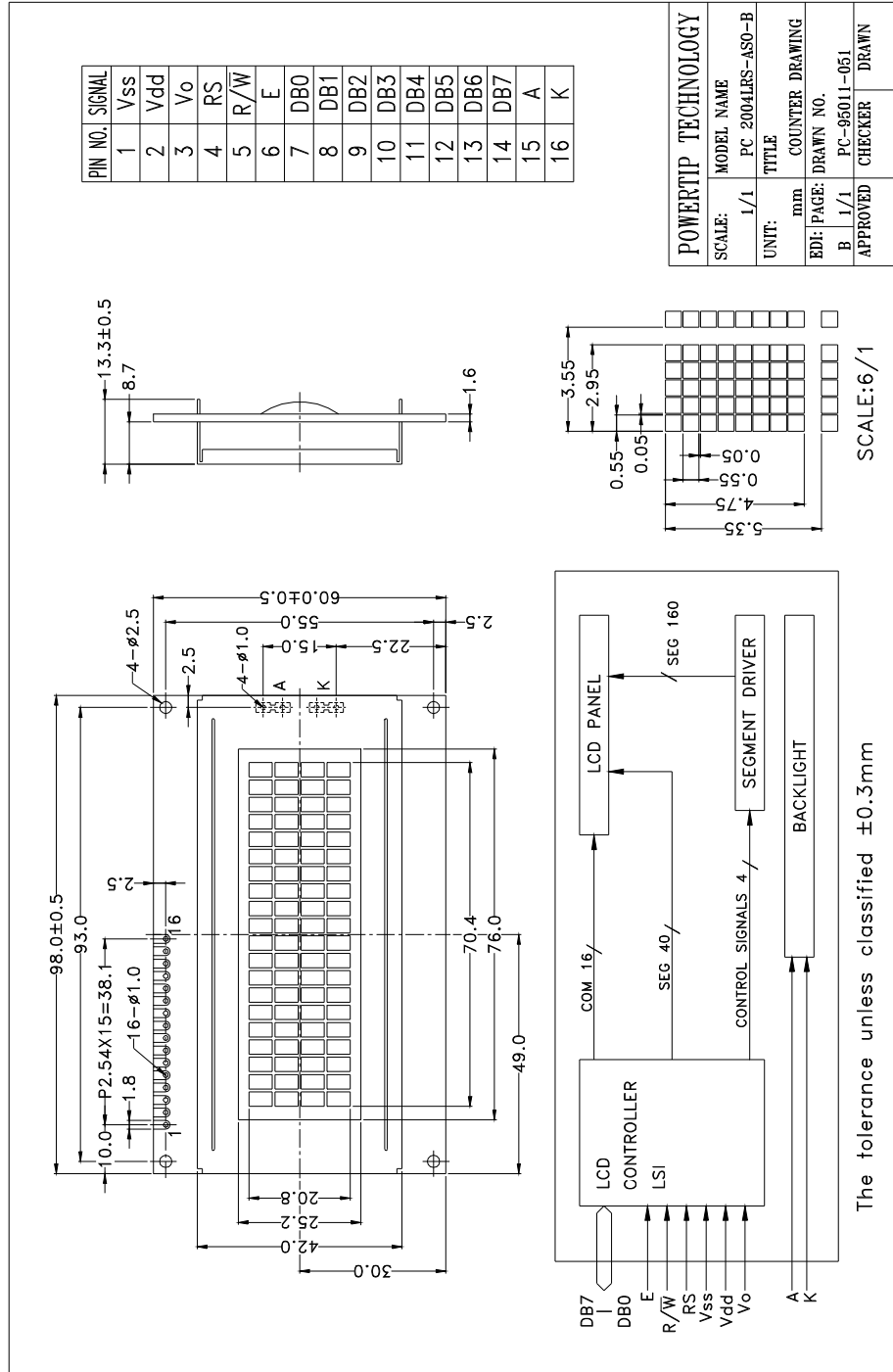
2.5 Character Pattern

Lower bits \ Upper bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			0	@	P	`	P				-	夕	ミ	α	⊕
xxxx0001	CG RAM (2)		!	1	A	Q	a	q			。	ア	チ	△	ä	⊙
xxxx0010	CG RAM (3)		"	2	B	R	b	r			「	イ	ツ	×	β	⊕
xxxx0011	CG RAM (4)		#	3	C	S	c	s			」	ウ	テ	ε	ε	⊕
xxxx0100	CG RAM (5)		\$	4	D	T	d	t			、	イ	ト	⊕	⊕	⊕
xxxx0101	CG RAM (6)		%	5	E	U	e	u			・	オ	ナ	1	⊕	⊕
xxxx0110	CG RAM (7)		&	6	F	V	f	v			ヲ	カ	ニ	ヨ	⊕	⊕
xxxx0111	CG RAM (8)		'	7	G	W	g	w			フ	キ	ヌ	ラ	⊕	⊕
xxxx1000	CG RAM (1)		(8	H	X	h	x			イ	ク	ネ	リ	⊕	⊕
xxxx1001	CG RAM (2))	9	I	Y	i	y			ウ	ケ	ル	⊕	⊕	⊕
xxxx1010	CG RAM (3)		*	:	J	Z	j	z			エ	コ	ハ	レ	⊕	⊕
xxxx1011	CG RAM (4)		+	;	K	[k	(オ	サ	ヒ	⊕	⊕	⊕
xxxx1100	CG RAM (5)		,	<	L	¥	l	l			カ	シ	フ	⊕	⊕	⊕
xxxx1101	CG RAM (6)		-	=	M]	m)			ユ	ズ	ハ	⊕	⊕	⊕
xxxx1110	CG RAM (7)		.	>	N	^	n	‡			ヨ	セ	ホ	⊕	⊕	⊕
xxxx1111	CG RAM (8)		/	?	O	_	o	⊕			ッ	ソ	マ	⊕	⊕	⊕



POWERTIP TECHNOLOGY CORPORATION

DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN



POWER TIP TECHNOLOGY	
SCALE:	MODEL NAME
1/1	PC 2004LRS-ASO-B
UNIT:	TITLE
mm	COUNTER DRAWING
EDI: PAGE:	DRAWN NO.
B 1/1	PC-95011-051
APPROVED	CHECKER
	DRAWN

The tolerance unless classified ±0.3mm

SCALE:6/1



POWERTIP TECHNOLOGY CORPORATION
 DISPLAY DEVICES FOR BETTER ELECTRONIC DESIGN