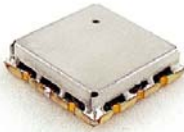


PHASE NOISE (1 Hz BW, typical)



£(f) (dBc/Hz)

OFFSET (Hz)

FEATURES
<ul style="list-style-type: none"> • Frequency Range: 159 - 189 MHz • Step Size: 100 KHz • CPLL - Style Package
APPLICATIONS
<ul style="list-style-type: none"> • Telecommunications • Satellite •

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	159 - 189	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-100	dBc/Hz
Harmonic Suppression (2nd, typ.)	-13	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	2±2	dBm
Load Impedance	50	Ω
Step Size	100	kHz
Charge Pump Output Current	2500	µA
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	3	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	CPLL	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	40	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES
<ul style="list-style-type: none"> • AN-107 : How to Solder Z-COMM VCOs / PLLs • AN-202 : PLL Functional Description

NOTES:

Reference Oscillator Signal: 5 MHz f_{osc} <math>< 100</math> MHz
 Frequency Synthesizer: Analog Devices - ADF4001

VCO TUNING CURVE, typ.

FREQUENCY (MHz)

- 85 °C
- 25 °C
- -40 °C

TUNING VOLTAGE (Vdc)

VCO POWER CURVE, typ.

OUTPUT POWER (dBm)

- 25 °C

FREQUENCY (MHz)

PHYSICAL DIMENSIONS

