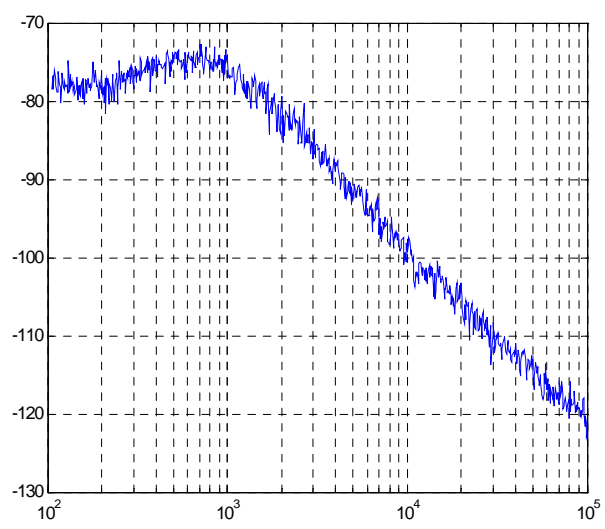




PHASE NOISE (1 Hz BW, typical)

£(f) (dBc/Hz)



FEATURES

- Frequency Range: 420 - 470 MHz
- Step Size: 100 KHz
- cPLL - Style Package

APPLICATIONS

- Satellite Modems
- Mobile Radios
- CATV

PERFORMANCE SPECIFICATIONS

	VALUE	UNITS
Frequency Range	420 - 470	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-99	dBc/Hz
Harmonic Suppression (2nd, typ.)	-10	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	4±3	dBm
Load Impedance	50	Ω
Step Size	100	KHz
Charge Pump Output Current	1250	µA
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	cPLL	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	24	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

NOTES:

Reference Oscillator Signal: 5 MHz f_{osc} <math><100\text{ MHz}</math>
 Frequency Synthesizer IC: Analog Devices - ADF4113

