ON CHIP SYSTEMS

T-75-07-15

# On Chip Systems

**PCT 14** 

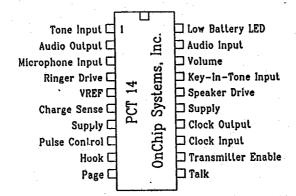
#### **PRODUCT** HIGHLIGHTS

- Complete audio processing and tone signalling on a single chip.
- Few external components
- Low pin count and small size: 20 pin plastic DIP or SOIC package.
- No trimming required.
- Three different pilot tone frequencies available
- Pilot delay timer accomodates hook signalling.
- Page tone generation.
- Ring tone detection
- Hook and pulse dialing control
- Low battery indication.
- Low current drain: >1mA

#### Information: Ordering

- 20 pin plastic DIP PCT 14P
- 20 pin plastic Small Outline (SOIC) PCT 14S

### Cordless Telephone Handset Subsystem



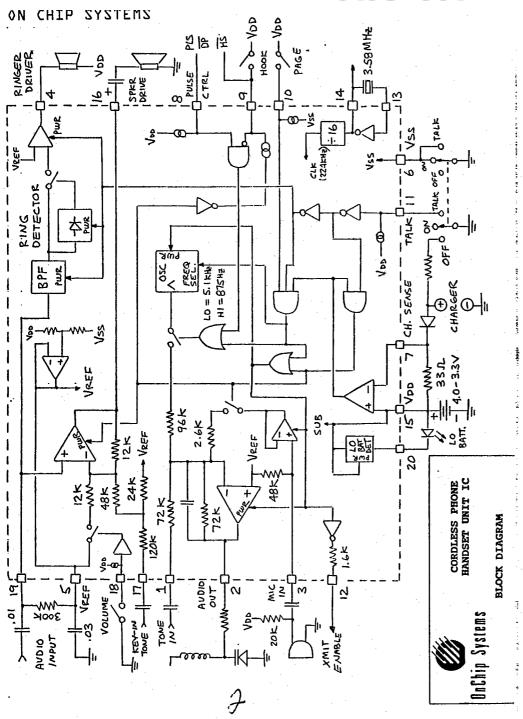
#### Description

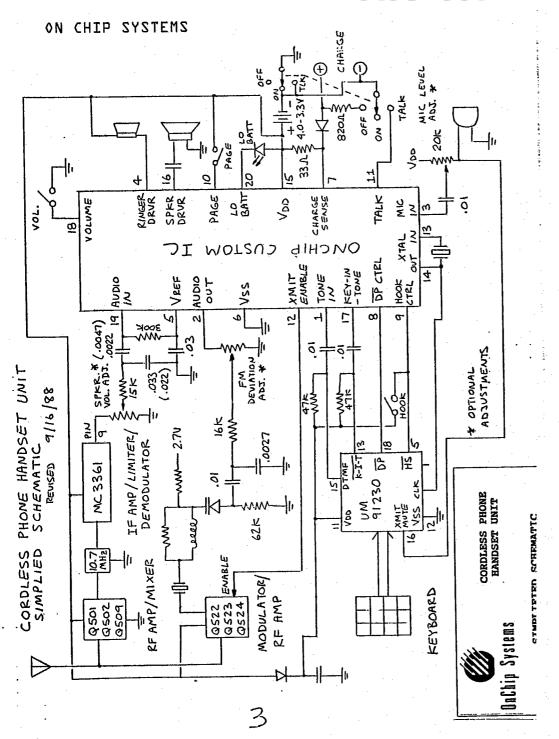
The PCT 14 Cordless Telephone Hand-set Subsystem IC performs all the audio processing and tone signalling functions required ofthe handset in a cordless telephone system. Included onchip are the earpiece driver with selectable gain, a microphone preamplifier and DTMF tone mixer, a ring tone detector circuit for driving an external transducer with the ring tone, and a lowbattery voltage indicator. Additionally, a pilot tone oscillator-generates one of three possible pilot

tones for signalling the base unit to connect to the telephone line, a page tone oscillator allows the handset user to signal the base for intercom operation, and a charge sense circuit detects when the handset is in or out of the base cradle. Finally, on-chip logic allows enabling of the external transmission circuitry, hook control and pulse dialing modulation of the pilot tone, and selection of a standby mode for low power consumption. Use of switched capacitor techniques and crystal controlled divider chains for the detection

filters and tone generators not only eliminate many external components but provide extremely high frequency accuracy without the need for any external circuit trimming. Able to operate down to 3.3V (three 1.2V battery cells) and requiring low power (less than 1mA in standby mode) the PCT 14 allows low cost cordless telephone handsets to be implemented with just the addition of a standard IF/FM demodulator IC, standard DTMF gener-ator and simple RF receiver and transmitter circuitry.

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#### Functional Description - Handset IC (20 pin)

#### Audio Input Pin: (pin 19)

Accepts band limited audio output from FM demodulator (50mV.P.P. nominal) and applies it to the speaker amplifier and ring tone detector. The input must be biased through a resistor from the Vref pin, which sets the input impedance. The speaker amplifier provides a gain of 2.2 nominal with Vol pin 'high' or 'open', supplying 100mV.R.M.S into 8 ohms at the Speaker Drive pin for driving the external speaker.

The ring tone detector consists of a 2nd order bandpass filter with center frequency at 2.0KHz and bandwidth of +/- 100 Hz, followed by a rectifier and threshold comparator. When the Talk pin is "open" or "high", the detector is activated and, if the ring tone is present, the detector connects the output of the filter to a high gain squaring circuit which can provide 10MA of pull down drive to the Ringer Drive pin for energizing the ringer. The detector is disabled whenever the Talk pin is "low".

#### Volume pin: (pin 18)

When brought low, the gain of the speaker amplifier is reduced from 2.2 to 1.2 (6dB).

#### Speaker Drive pin: (pin 16)

Provides more than 15mA peak drive to the external speaker (2mW peak power into 8 ohm). The speaker amplifier is activated only when the Talk pin is "low" and the handset is not in-cradle. A trim pot may be used in series with the speaker to adjust the speaker volume if necessary.

#### Ringer Drive pin: (pin 4)

Provides more than 10mA of pull down drive at the ring tone frequency for energizing the ringer,

#### Key-in-Tone input pin: (pin 17)

Accepts "Key-In-Tone output" from tone dialer chip and applies it to speaker amplifier. Gain is .04 nominal, and is not affected by the Vol. pin.

#### Hook pin: (pin 9)

This pin is both an input and an output with low drive (15uA) which normally connects to the Hook Switch input on the Touch Tone dialer IC. This pin provides a "low" output for enabling the keyboard and dialer IC whenever the Talk pin is brought "low" and the handset is not in-cradle. In such state, a "high" (i.e. VDD) applied to this pin through the hook switch will override the weak pulldown, both disabling the dialer IC and the pilot tone if being generated. A pilot tone is generated at either 4.0, 5.1, or 5.9KHZ (selectable by metal mask option) and will appear at the Audio Output pin whenever the Talk pin is "low", the handset is not in-cradle, the Pulse Control pin is being held "high" or left "open", and the Hook pin is left "open" so that it is being pulled "low".

#### Pulse Control pin: (pin 8)

With an internal pull-up to VDD, an "open" condition or "high" applied to this pin will activate the pilot tone if also the Hook pin is "low", the Talk pin is "low", and the handset is not in-cradle. Normally connected to the "Dial Pulse Output" of the tone dialer chip, this pin is used for "make and break" control of the phone

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line in the pulse dial mode, where a momentary "low" applied to this pin will disable (inhibit) the pilot tone if being generated, which will cause the Base IC to momentarily disconnect the phone line.

#### Page pin: (pin 10)

Whenever this pin is brought "high" and the Talk pin is "high" and the handset is not in-cradle, then the tone oscillator is activated to generate the page frequency of 875HZ and the resulting page tone appears at the Audio Output pin. In addition, the Transmitter Enable pin is forced to its active low state.

#### Tone input pin: (pin 1)

Accepts the DTMF dual tone output of the tone dialer chip, and passes it through the Tone mixer and low pass filter to the Audio Output pin at a gain of 1.0. Input impedance is 72K internally biased at VREF.

#### Microphone Input pin: (pin 3)

Accepts the microphone output signal and, whenever the Talk pin is "low" and the handset not in-cradle, connects it to the microphone amplifier where it is amplified with a gain of 30 nominal before appearing at the Audio Output pin. The amplifier is also activated whenever the page tone is activated, but the microphone is not connected. Input impedance is 48K nominal, and since this input is internally biased at VREF, an input coupling capacitor will normally be required. A series trim pot may also be necessary to adjust the microphone output to the desired levels.

#### Audio Output pin: (pin 2)

This is the output of the microphone amplifier/tone mixer, and provides either the page tone or pilot tone, as well as the amplified microphone output and DTMF generator output to the FM modulator. An internal low pass Filter with 4.8KHz cutoff frequency and 6dB/octave roll off is provided to band limit the output signals. The output impedance is less than 500 ohm, and the quiescent DC output level of VREF may be used to directly bias the varactor diode. If additional band limiting is required, the output may be passed through an external RC low pass filter. Finally, a trim pot may be necessary to adjust the final level applied to the varactor diode, so that the resulting FM deviation complies to FCC specifications.

#### Transmitter Enable pin: (pin 12)

Provides a 1.6K ohm pull down to VSS whenever the page tone is activated, or Talk pin is brought low and handset not in-cradle. This pin may be used to enable power-up of the external FM modulator and RF amplifier transmission circuitry (by driving the base of a PNP transistor, for example).

#### Talk pin: (pin 11)

Whenever this pin is brought low and the handset is not in-cradle, internal power is applied to the speaker amplifier, Microphone amplifier, and oscillator, and the microphone is connected to the microphone amplifier. Power is removed from the ring tone detector /driver circuit (i.e. it is disabled). In addition, the Transmitter Enable pin is activated "low". Note that power is also applied but only to the microphone amplifier/tone mixer and oscillator whenever the page tone is activated. When in this talk mode, power drain is increased, but is less than 3mA.

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#### Clock input and Output pins: (pins 13 & 14)

A standard 3.58MHz clock crystal connected between these pins generates the necessary internal clock signals which determine the filter and tone oscillator frequencies. The signal from the XTAL Output pin may be capacitively coupled to the tone dialer chip to provide it with the necessary clock frequency when activated.

#### Low Battery LED pin: (pin 20)

This pin provides an active pull down of more than 5mA at 2.4V for turning on the Low Battery LED whenever VDD is applied to the chip and is less than 3.3V.

#### VREF pln: (pln 5)

An internally generated and buffered reference voltage, approximately 1/2(VDD-6), is brought out to this pin and should be bypassed with a .005 to .05uF capacitor.

#### Charge Sense and Supply pins: (pins 6, 7, & 15)

The battery power is supplied to the chip between the VDD and VSS pins. The charging terminal supplies the Charge Sense pin through a diode and then through a current limiting resistor to the VDD pin. Whenever the Charge Sense pin is higher than VDD pin by more than 300mV, as is the case when the handset is in the cradle, then the sense comparator output prevents the page tone from being activated, and prevents a "low" on the Talk pin from enabling all functions explained above.

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### Specifications - Handset

VDD = 4.0V				
	Min.	Typical	Max.	Unit
Audio Input				
Impedance	108	***		ohm
Signal Level		***	400.	mV.P.P.
	•			
Ring Tone Filter				
Center Frequency		2.0		KHz
Tolerance	-5	•••	+5	<b>%</b> .
Bandwidth	*** .	+100	• • • • • • • • • • • • • • • • • • • •	Hz
Detection Threshold				
At 2150 Hz	•••	60	••	mV.P.P.
At 1810 Hz		65		mV.P.P.
<b>DetectionHysterisis</b>	***	35	•	mV.P.P.
Response Time		60		mS
Speaker Driver				
Output Level	***	***	280	mV.P.P.
Output Power		***	2.3	mW Peak
Impedance		1.6		ohm
Gain from Audio Input	2		•	
VOL = HI		2.2	·	
VOL = LO	•••	1.2	***	
Gain from Key-In-Tone		.04	***	
		•		

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	· Onc	ub Syste	ms, Inc
Mi-	Trainel	May	Unit
MIII.	rypicai	IVIDA,	Out
	-	•	
	136K		ohm
	•••	VDD .	V.P.P.
	•		
•••	72K	•••	ohm
•••		VDD	V.P.P.
***	48K		
		100	nV.P.I
***	500	***	ohm
***		3.2	V.P.P.
***	1.0	***	
***	30	***	
3.6	4.8	6.0	KHz
12K	***		ohm
•••	875		Hz
4.0	5,1	5.9	KHz
=XTAL			%
	Triangle		
***	2,1	•••	V.P.P.
· ·	1.0	***	V.P.P.
	150		ohm
	200		Juni
95-3350	· .	110	Highland Av
	3.6 12K 	136K 72K 48K 500 1,0 30 3.6 4.8 12K 875 4.0 5.1 = XTAL Triangle 2.1 1.0	136K VDD 72K VDD 48K 100 500 3.2 1.0 3.2 1.0 3.6 4.8 6.0 12K 875 4.0 5.1 5.9 = XTAL 1.0 1.

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OnChip Systems, Inc.	· .			*	PCT 14
		Min.	Typical	Max.	Unit
Transmitter Enable					
Pull-down Resistance		<del></del>	1.6	***	Kohm
Low Battery LED					•
Pull-down Drive @ 2.4V			. 5		mA
VDD for Activation		3.3	3.45	3.6	<b>v</b>
VOL Input					
Talk Enable Input					
Pulse Control Input					
Internal Pull-up		•••	15	•••	u <b>A</b> `
Low Threshold			1.8	_	v
Page Enable Input					
Hook Control Input/Output					
Internal Pull-down		•4=	15		· uA
High Threshold		***	1.8	. •••	V
Clock Input Impedance		•••	1 MEG		ohm
Clock Output Level		1.0	<del>*</del>	VDD	<b>V.P.P.</b>
Charge Sense Input					
Threshold re. VDD			+300		шV
Input Bias Current			15		uА

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PCT 14	OnChip Systems, Inc.			
	Min.	Typical	Max.	Unit
Supply				
Voltage Range	3.3	. ***	10	V
Current Drain				
"Talk" pin open			500	uA.
"Talk" pin low	***	<b></b> .	3	mA

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#### Handset Truth Table

	**********************************										====	=======================================
-1	INPUTS					OUTPUTS			;	- 1	OPERATION	
	Ring Tone @ Audio In	Talk Pin	Charge Current	Page Pin	Hook Pin	Pulse Control	Audio Out	Speaker Out	Ringer	Hook Pin	Transmit Enable	
	- S	H H	X X	X X	X X	X	-	- -	R	H	*	Standby Ring
	х	н	-	Н	Х	х	P	_	*	Н	L	Page Base
	X	L L	-	X	X	X	M/* M/Q	A A	-	L L	L	Enable Mic & Speaker Amp Line Connect (Generate Pilot)

Key:

- Signal Present
  Signal Not Present/Function Not Enabled
  Low Logic Level
  High Logic Level
  Don't Care
  Depends on Don't Care Inputs
  Signal Applied to Audio Input
  Signal Applied to Microphone Input
  Pilot Tone
  Page Tone
  Ring Tone