

Version: 1.0

## TECHNICAL SPECIFICATION

MODEL NO: PD104SLD

The content of this information is subject to be changed without notice. Please contact PVI or its agent for further information.

Customer's Confirmation
Customer
Date
Ву
☐PVI's Confirmation

Confirmed By

Prepared By



### **Revision History**

Rev.	Issued Date	Eng.	Revised Content
1.0	Aug.08,2007	李穎銘	New



# TECHNICAL SPECIFICATION <u>CONTENTS</u>

NO.	ITEM	PAGE
_	Cover	1
-	Revision History	2
-	Contents	3
1	Application	4
2	Features	4
3	Mechanical Specifications	4
4	Mechanical Drawing of TFT-LCD module	5
5	Input / Output Terminals	7
6	Absolute Maximum Ratings	9
7	Electrical Characteristics	9
8	Power On Sequence	15
9	Optical Characteristics	16
10	Handling Cautions	19
11	Reliability Test	20
12	Block Diagram	21
13	Packing	22



#### 1. Application

This data sheet applies to a color TFT LCD module, PD104SLD.

PD104SLD module applies to OA product, car TV(must use Analog to Digital drive board), which require high quality flat panel display. If you must use in high reliability environment can't over reliability test condition

Prime View assume no responsibility for any damage resulting from the use of the device which dose not comply with the instructions and the precautions in these specification sheet.

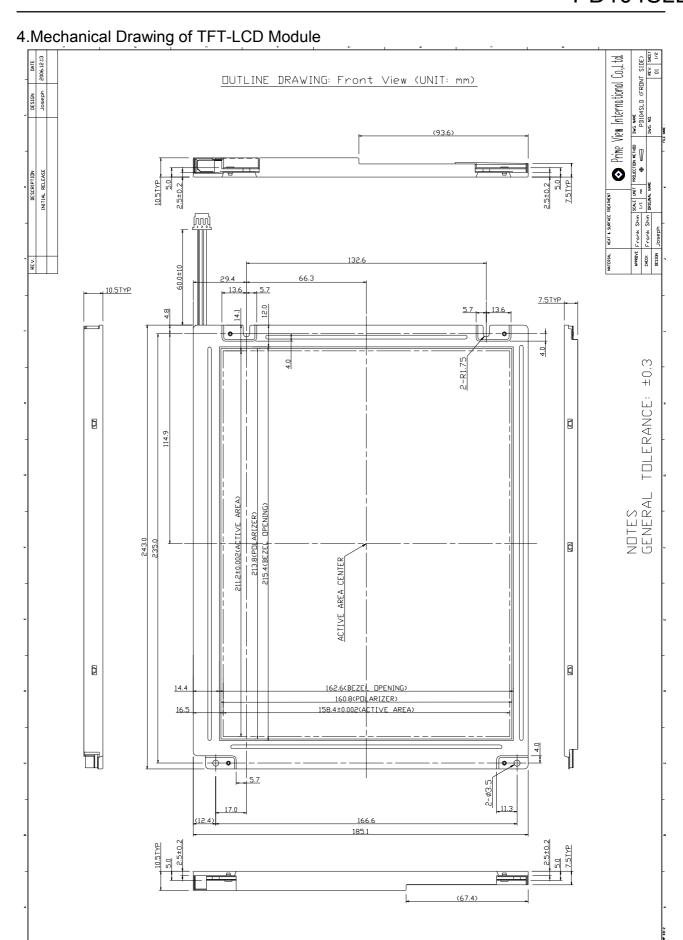
#### 2. Features

- . Amorphous silicon TFT LCD panel with back-light unit
- . Pixel in stripe configuration
- . Slim and compact, designed for O/A application
- . Display Colors: 262,144 colors
- . +3.3V DC supply voltage for TFT LCD panel driving
- . Backlight driving DC/AC inverter not included in this module
- . Wide Viewing Angle

#### 3. Mechanical Specifications

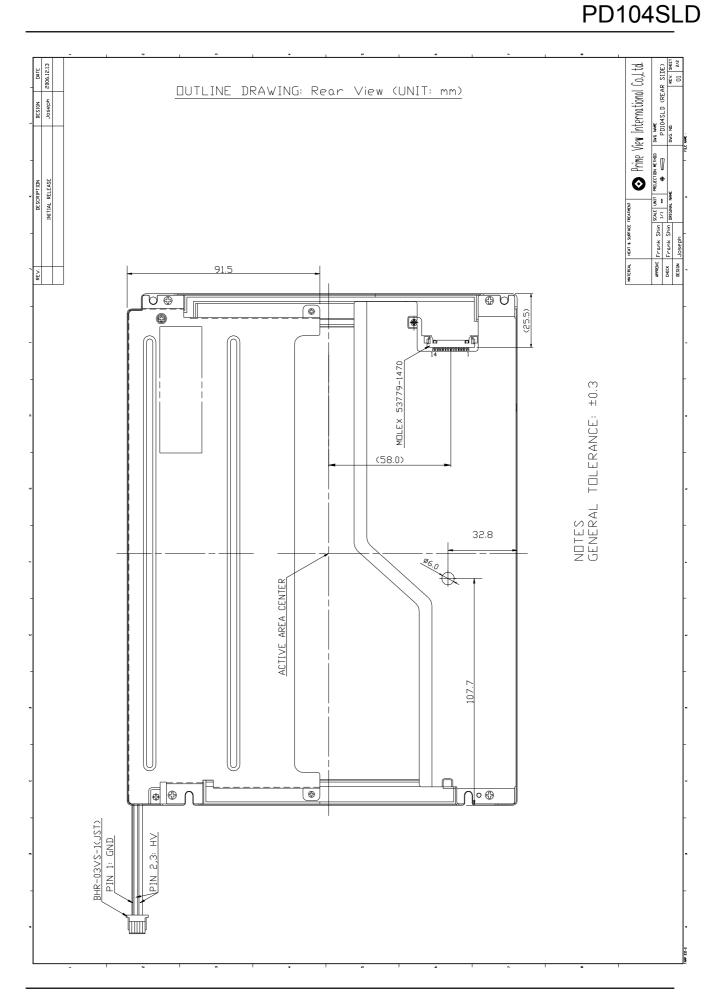
Parameter	Specifications	Unit
Screen Size	26.4(diagonal)	cm
Screen Size	10.4 (diagonal)	inch
Display Format	800×(R, G, B)×600	dot
Display Colors	262,144	
Active Area	211.2(H)×158.4 (V)	mm
Pixel Pitch	0.264 (H)×0.264 (V)	mm
Pixel Configuration	Stripe	
Outline Dimension	243.0 (w)×185.1(H)×10.5 (D) (typ.)	mm
Weight	475±20	g
Back-light	CCFL, 2-CCFL	
Surface treatment	Anti-glare and WV film	
Display mode	Normally white	
C	12 o'clock	
Gray scale inversion direction	[ ref to Page 16 viewing angle ]	













#### 5.Input Terminals

5-1) TFT-LCD Panel Driving

Connector type: Molex 53779-1470

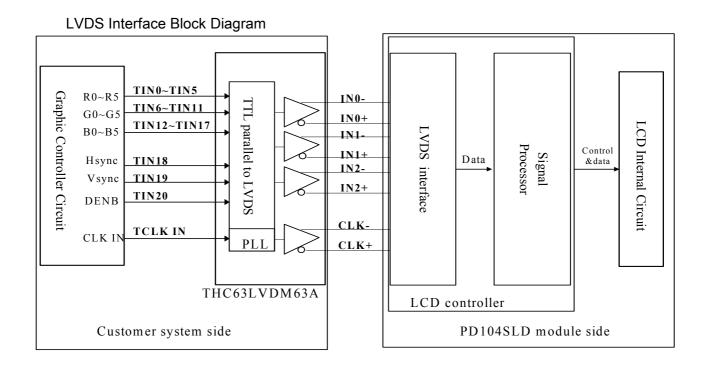
Pin No.	Symbol	Function	Remark
1	VDD	Power supply: +3.3V	
2	VDD	Power supply: +3.3V	
3	GND	Ground	
4	GND	Ground	
5	INO-	Pixel data Transmission pair 0 (negative -)	
6	IN0+	Pixel data Transmission pair 0 (positive +)	
7	IN1-	Pixel data Transmission pair 1 (negative -)	
8	IN1+	Pixel data Transmission pair 1 (positive +)	
9	IN2-	Pixel data Transmission pair 2 (negative -)	
10	IN2+	Pixel data Transmission pair 2 (positive +)	
11	CLK-	Sampling Clock (negative -)	
12	CLK+	Sampling Clock (positive +)	
13	GND	Ground	
14	GND	Ground	

Recommended Transmitter (THC63LVDM63A Thine) to PD104SLD interface Assignment:

. 1000	minonace	10+0LD Interiac	o 7 toolgiiiiionti		
			aphic controller output signal	Output signal	
of				symbol	interface
THC63LVDM63A					terminal(Symbol)
Thine					
Symbol	No.	Symbol	Function		
TIN0	44	R0	Red pixel data (LSB)	)	
TIN1	45	R1	Red pixel data		
TIN2	47	R2	Red pixel data	Tout0-	─ No.5 : IN0-
TIN3	48	R3	Red pixel data	>	
TIN4	1	R4	Red pixel data	Tout0+ —	No.6 : IN0+
TIN5	3	R5	Red pixel data(MSB)		
TIN6	4	G0	Green pixel data (LSB)	<b> </b>	
TIN7	6	G1	Green pixel data	`	
TIN8	7	G2	Green pixel data		
TIN9	9	G3	Green pixel data	Tout1- —	—No.7 : IN1-
TIN10	10	G4	Green pixel data		
TIN11	12	G5	Green pixel data(MSB)	Tout1+ —	─No.8 : IN1+
TIN12	13	В0	Blue pixel data(LSB)		
TIN13	15	B1	Blue pixel data	<b> </b>	
TIN14	16	B2	Blue pixel data	`	
TIN15	18	В3	Blue pixel data		
TIN16	19	B4	Blue pixel data	Tout2- —	— No.9 : IN2-
TIN17	20	B5	Blue pixel data(MSB)	<b>\</b>	
TIN18	22	NC	No connection	Tout2+ —	─N0.10 : IN2+
TIN19	23	NC	No connection		
TIN20	25	ENAB	Compound Synchronization signal	ノ	
CLK in	26	CLK	Data sampling clock	TCLK out-	No.11 : CLK -
				TCLK out+	No.12 : CLK+
	_	_			

Data stream of INO-/+, IN1-/+ and IN2-/+ for PD104SLD





#### 5-2) Backlight driving

Connector type: "BHR-03VS-1" of Japan Solderless Terminal MFG Co. LTD

PIN NO.	Symbol	Description	Remark
1	LV	Ground	White
2	HV	Lamp power input	Pink (or Gray)
3	HV	Lamp power input	Pink (or Gray)



#### 6. Absolute Maximum Ratings:

GND=0V, Ta=25°C

Parameters	Symbol	MIN.	MAX.	Unit	Remark
Supply Voltage	$V_{DD}$	-0.3	+4.0	V	
Backlight Driving Voltage	$V_L$	-	2000	V	
Backlight Driving Frequency	F∟	20	80	KHz	

#### 7. Electrical Characteristics

7-1) Recommended Operating Conditions:

GND = 0V, Ta =  $25^{\circ}C$ 

7 1/ Noccinimended operating conditions.									
Item	Symbol	Min.	Тур.	Max.	Unit	Remark			
Supply Voltage	VDD	3.0	3.3	3.6	V				
Current Dissipation	$I_{DD}$	-	190	380	mA	Note 7-1			
LVDS Differential input high threshold	VTH	-	-	100	mV	Note 7-2			
LVDS Differential input low threshold	VTL	-100	-	-					
Lamp Current	I <sub>FL</sub>	6.0	14.0	16.0	mA	Note 7-3 Note 7-5			
Lamp Voltage	V <sub>L</sub>	420	475	530	Vrms	Note 7-3 I <sub>L</sub> =14mA			
Starting Voltage (25˚ℂ) (Reference Value)	Vs	-	-	810	Vrms				
Starting Voltage (0°ℂ) (Reference Value)	Vs	-	-	1080	Vrms	Note 7-4			
Starting Voltage (-20˚ℂ) (Reference Value)	Vs	-	-	1125	Vrms				
Lamp Driving Frequency	F∟	20	50	80	KHz				
Lamp power consumption	-	5.88	6.65	7.42	W	Note 7-5 I <sub>L</sub> =14mA			

Note 7-1: To test the current dissipation of VDD, using the "color bars" testing pattern shown as below

		0.0					
1	2	3	4	5	6	7	8

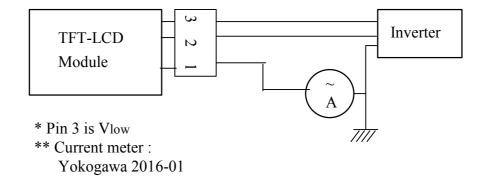
- 1. White
- 2. Yellow
- 3. Cyan
- 4. Green
- 5. Magenta
- 6. Red
- 7 Blue
- 8. Black

Idd current dissipation testing pattern

- Note 7-2 :Please refers to THC63LVDF63A specification by THINE Corporation. This LCD module conforms to LVDS standard.
- Note 7-3: The back-light driving waveform should be as closed to sine-wave as possible. In order to satisfy the quality of B/L, no matter use what kind of inverter, the output lamp current must between Min. and Max. to avoid the abnormal display image caused by B/L.

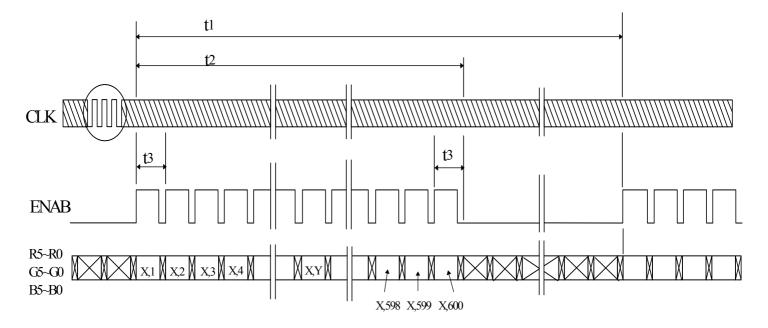


- Note 7-4: The" Max of starting voltage " means the minimum voltage of inverter to turn on the CCFL. and it should be applied to the lamp for more than 1 second to start up. Otherwise the lamp may not be turned on.
- Note 7-5: Lamp current is measured with current meter for high frequency as shown below

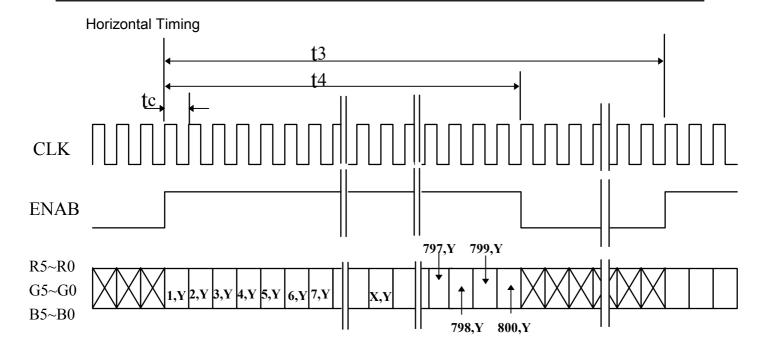


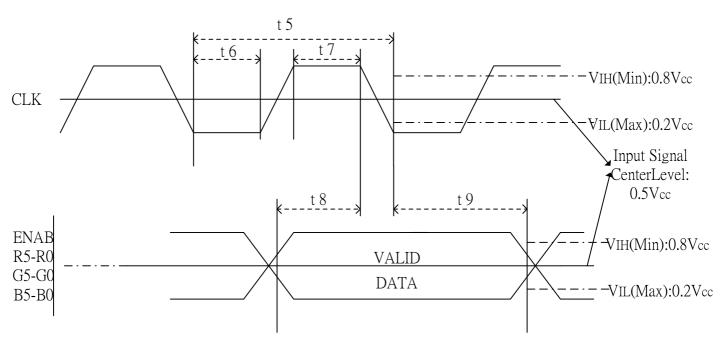
Lamp current dissipation testing configuration

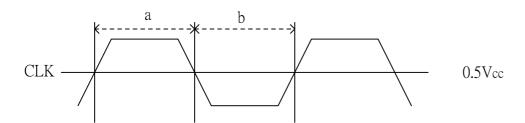
- Note 7-6: The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 7-2) Input / Output signal timing chart
- (A) Vertical Timing











Duty (a,b):  $50 \pm 10\%$ 





D) Timing Specifications

Item	symbol	Min.	Тур.	Max.	Unit	Remark
Frame Cycling	t1	604 X t3	628 X t3	800 X t3	-	
		-	16.58	-	ms	
Vertical Display Period	t2	600 X t3	600 X t3	600 X t3		
Horizontal Scanning	t3	920X t5	1056 X t5	1064 X t5		
Time						
		-	26.4	-	us	
Horizontal Display	t4	800 X t5	800 X t5	800 X t5		
Period						
Clock Cycle	t5	-	25.0	-	ns	
Clock High Level Time	t6	9.0	-	-	ns	
Clock Low Level Time	t7	9.0	-	-	ns	
Hold time	t8	4.0	-	-	ns	
Setup time	t9	5.0	-	-	ns	



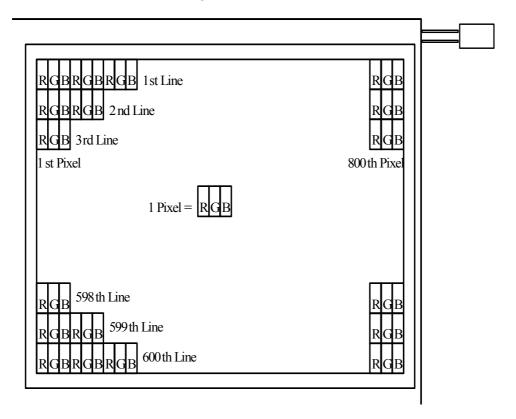
#### 7-3) Display Color and Gray Scale Reference

								In	put	Cc	lor	Da	ta						
Co	olor			Re	ed			Green						Blue					
15		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	<b>B</b> 5	<b>B4</b>	В3	B2	<b>B</b> 1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker																		
Red	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$
	Brighter																		
	Red (61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker																		
Green	$\rightarrow$	$\downarrow$	$\leftarrow$	$\downarrow$	$\rightarrow$	$\rightarrow$	$\leftarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$						
	Brighter																		
	Green (61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Darker																		
Blue	<del></del>	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	<b>↓</b>	<b>↓</b>									
	Brighter																		
	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



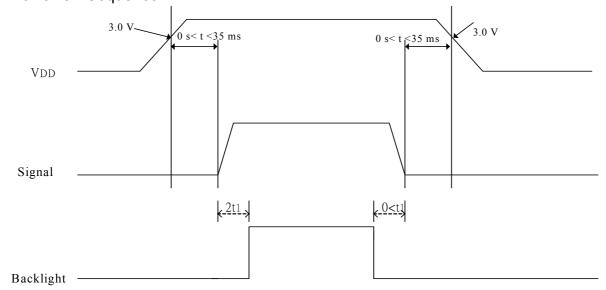
#### 7-4) Pixel Arrangement

The LCD module pixel arrangement is the stripe.





#### 8. Power On Sequence



- 1. The supply voltage for input signals should be same as  $V_{DD}$ .
- When the power is off , please keep whole signals (ENAB,CLK, Data) low level or high impedance



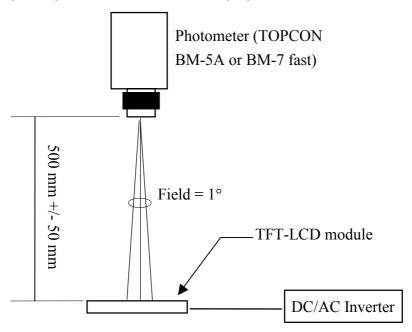
#### 9.) Optical Characteristics

#### 9-1) Specification:

Ta = 25°C

Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks
	Horizontal	$\theta$		±55	±60	-	deg	
Viewing Angle	Vertical	$\theta$ (to 12 o'clock)	CR≥10	50	55	ı	deg	Note 9-1
		$\theta$ (to 6 o'clock)		35	40	ı	deg	
Contrast Ratio		CR	Optimum direction	200	400	ı	ı	Note 9-2
Response time	Rise	Tr	<i>⊕</i> =0°	-	15	50	ms	Note 9-4
	Fall	Tf	$\varphi$ =0°	-	25	50	ms	
Luminance		L	$\theta$ =0°/ $\varphi$ =0°	300	350	-	cd/m²	Note 9-3
Uniformity		U		55	80	-	%	Note 9-5
White Chromaticity		Х		0.30	0.33	0.36	-	
Write Chiomatic	ity	У		0.34	0.37	0.40	-	
Cross Talk Ratio		CTK		-	-	3.5	%	Note 9-6
Lamp Life Time		-	<b>+25</b> ℃	50000	-	-	hrs	

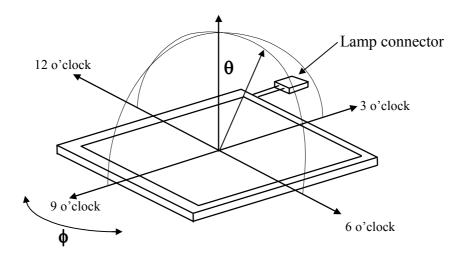
All the optical measurement shall be executed 30 minutes after backlight being turn-on. The optical characteristics shall be measured in dark room (ambient illumination on panel surface less than 1 Lux). The measuring configuration shows as following figure.



Optical characteristics measuring configuration



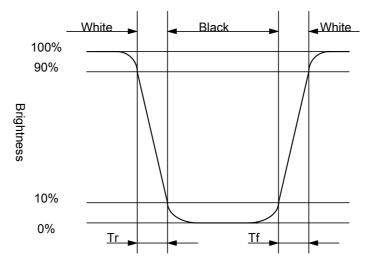
Note 9-1: The definitions of viewing angles are as follows.



Note 9-2 : The definition of contrast ratio  $CR = \frac{Luminance at gray level 63}{Luminance at gray level 0}$ 

Note 9-3: Topcon BM-5A or BM-7 fast luminance meter 1°field of view is used in the testing (after 30 minutes' operation). The typical luminance value is measured at lamp current 14.0 mA.

Note 9-4: Definition of Response Time T<sub>r</sub> and T<sub>f</sub>:



Note 9-5: The uniformity of LCD is defined as

U = The Minimum Brightness of the 9 testing Points

The Maximum Brightness of the 9 testing Points

Luminance meter: BM-5A or BM-7 fast(TOPCON)

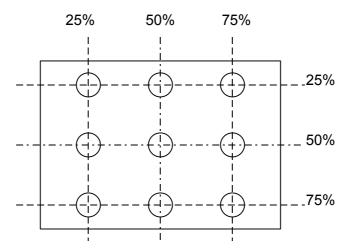
Measurement distance: 500 mm +/- 50 mm

Ambient illumination : < 1 Lux

Measuring direction: Perpendicular to the surface of module



The test pattern is white (Gray Level 63).



Note 9-6: Cross Talk (CTK) = 
$$\frac{|YA-YB|}{YA} \times 100\%$$

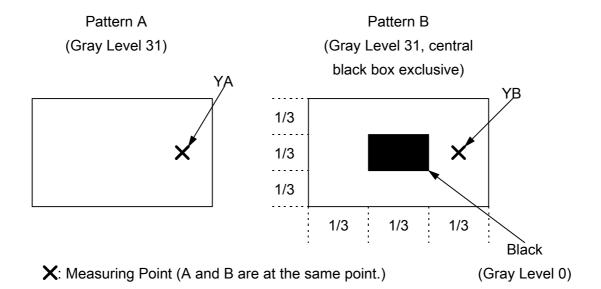
YA: Brightness of Pattern A YB: Brightness of Pattern B

Luminance meter: BM 5A or BM-7 fast (TOPCON)

Measurement distance: 500 mm +/- 50 mm

Ambient illumination : < 1 Lux

Measuring direction: Perpendicular to the surface of module





#### 10. Handling Cautions

- 10-1) Mounting of module
  - a)Please power off the module when you connect the input/output connector.
  - b) Please connect the ground pattern of the inverter circuit surely. If the connection is not perfect, some following problems may happen possibly.
    - 1. The noise from the backlight unit will increase.
    - 2. The output from inverter circuit will be unstable.
    - 3.In some cases a part of module will heat.
  - c) Polarizer which is made of soft material and susceptible to flaw must be handled carefully.
  - d) Protective film (Laminator) is applied on surface to protect it against scratches and dirts. It is recommended to peel off the laminator before use and taking care of static electricity.
- 10-2) Precautions in mounting
  - a) When metal part of the TFT-LCD module (shielding lid and rear case) is soiled, wipe it with soft dry cloth.
  - b) Wipe off water drops or finger grease immediately. Long contact with water may cause discoloration or spots.
  - c) TFT-LCD module uses glass which breaks or cracks easily if dropped or bumped on hard surface. Please handle with care.
  - d) Since CMOS LSI is used in the module. So take care of static electricity and earth yourself when handling.
- 10-3) Adjusting module
  - a) Adjusting volumes on the rear face of the module have been set optimally before shipment.
  - b) Therefore, do not change any adjusted values. If adjusted values are changed, the specifications described may not be satisfied.

#### 10-4) Others

- a) Do not expose the module to direct sunlight or intensive ultraviolet rays for many hours.
- b) Store the module at a room temperature place.
- c) The voltage of beginning electric discharge may over the normal voltage because of leakage current from approach conductor by to draw lump read lead line around.
- d) If LCD panel breaks, it is possibly that the liquid crystal escapes from the panel. Avoid putting it into eyes or mouth. When liquid crystal sticks on hands, clothes or feet. Wash it out immediately with soap.
- e) Observe all other precautionary requirements in handling general electronic components.
- f) Please adjust the voltage of common electrode as material of attachment by 1 module.
- 10-5) Polarizer mark

The polarizer mark is to describe the direction of wide view angle film how to mach up with the rubbing direction.





#### 11. Reliability Test

No	Test Item	Test Condition	Remark
1	High Temperature Storage Test	Ta = +70℃, 240 hrs	
2	Low Temperature Storage Test	Ta = -30°ℂ, 240 hrs	
3	High Temperature Operation Test	Ta =+70°ℂ, 240 hrs	
4	Low Temperature Operation Test	Ta = -20°ℂ, 240 hrs	
_	High Temperature & High Humidity	Ta = +60℃, 90%RH, 240 hrs	
5	Operation Test	(No Condensation)	
6	Thermal Cycling Test	0°C <b>←→</b> +60°C, 100 Cycles	
Ь	(non-operating)	1Hr 1Hr	
7	Vibration Test (non-operating)	Frequency: 10 ~ 57 H <sub>Z</sub> , Amplitude: 0.15 mm,58~500Hz, 1G Sweep time: 11 min Test Period: 3 hrs	
		(1 hr for each direction of X, Y, Z)	
8	Shock Test	80G, 6ms, X,Y, Z	
L	(non-operating)	1 times for each direction	
9	Electron Static Discharge	C=150pF,R=330 Ω Contact=±8KV Air=±15KV 10 times/terminal	

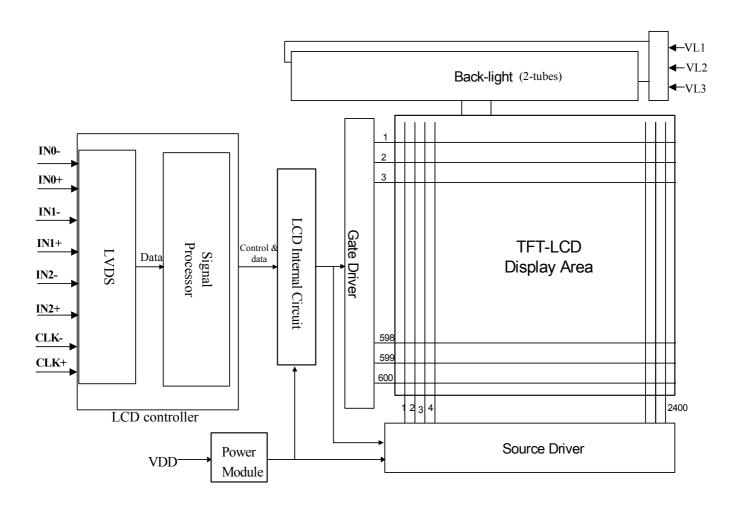
Ta: ambient temperature

#### [Criteria]

In the standard conditions, there is not display function NG issue occurred. (including: line defect, no image). All the cosmetic specification is judged before the reliability stress.



#### 12.)Block Diagram





#### 13.)Packing

