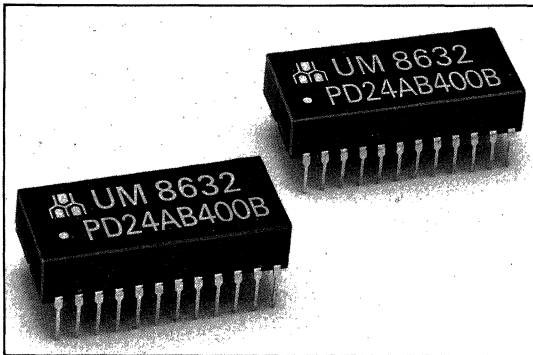




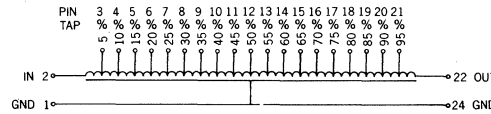
PD24 SERIES: 24-PIN DIP 20-TAP EQUALLY-SPACED



FEATURES:

- TTL and DTL Compatible
- 20 Equally-Spaced Delay Taps
- Standard 24-pin DIP Package
- Custom Designs Available upon Request

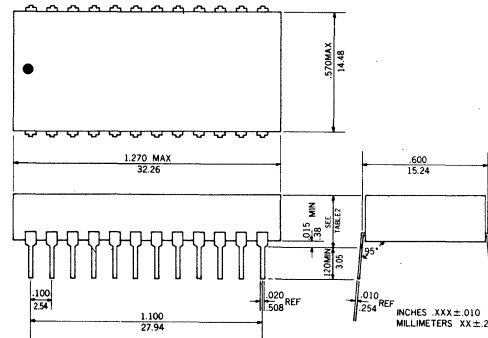
CIRCUIT CONFIGURATION AND PIN CONNECTIONS:



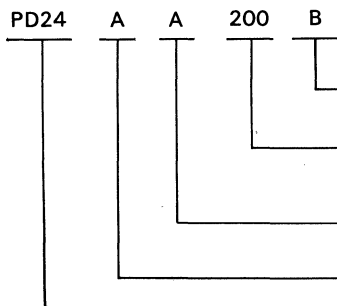
GENERAL SPECIFICATIONS:

- Total Delay: 20 to 1000 ns
- Tap Delays: 5% increments of total delay
- Characteristic Impedance: 50 to 200 ohms ($\pm 10\%$)
- Distortion: $\pm 10\%$ max.
- Temperature Coefficient of Delay: 100 ppm/ $^{\circ}\text{C}$ max.
- Operating Temperature Range: 0°C to $+70^{\circ}\text{C}$
- Storage Temperature Range: -55°C to $+125^{\circ}\text{C}$
- Insulation Resistance: 1000 megohms min.
- 50VDC, $+25^{\circ}\text{C}$
- Dielectric Strength: 50VDC

PACKAGE DIMENSIONS:



PART NUMBER SYSTEM:



- Characteristic Impedance in ohm
A=50 ohms B=100 ohms C=200 ohms
- Total Delay in ns
For examples: 200=200ns
A00=1000ns
- Circuit Configuration and Pin Connections (see Table 1)
- Mounting Height of Package (see Table 2)
- Basic Series Number
Passive delay line, DIP 24 Pins

TABLE 1-PIN CONNECTIONS

Circuit	IN	Tap 1	Tap 2	Tap 3	Tap 4	Tap 5	Tap 6	Tap 7	Tap 8	Tap 9	Tap 10	Tap 11	Tap 12	Tap 13	Tap 14	Tap 15	Tap 16	Tap 17	Tap 18	Tap 19	OUT	GND
A	2	3	4	5	6	7	8	9	10	11	12	14	15	16	17	18	19	20	21	22	23	1,24
B	2	3	4	5	6	7	8	9	10	11	13	14	15	16	17	18	19	20	21	22	23	12
C	1	2	3	4	5	6	7	8	9	10	11	14	15	16	17	18	19	20	21	22	23	12,24
E	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	1,24

TABLE 2- MOUNTING HEIGHT

Code	in inches	in mm
A	.325	8.26
B	.300	7.62
C	.275	6.99

ELECTRICAL SPECIFICATIONS:

PART NO.	TOTAL DELAY Td(ns) $\pm 5\%$	TAP DELAY (ns)	RISE TIME Tr(ns) max.	ATTENUATION(%) PER IMPEDANCE		TYPICAL (ohm)
				50	100	
PD24XX020X	20 ± 2	1 ± 0.5	3	5	5	6
PD24XX040X	40	2 ± 0.5	4	5	5	8
PD24XX060X	60	3 ± 1	6	6	6	8
PD24XX080X	80	4 ± 2	8	6	6	8
PD24XX100X	100	5 ± 2	10	6	8	8
PD24XX200X	200	10 ± 3	20	8	8	8
PD24XX300X	300	15 ± 3	30	8	8	8
PD24XX400X	400	20 ± 3	40	8	8	10
PD24XX500X	500	25 ± 3	50	8	8	10
PD24XA00X	1000	50 ± 5	100	10	10	10