100V Dual N-Channel MOSFETs

PDC0810T

General Description

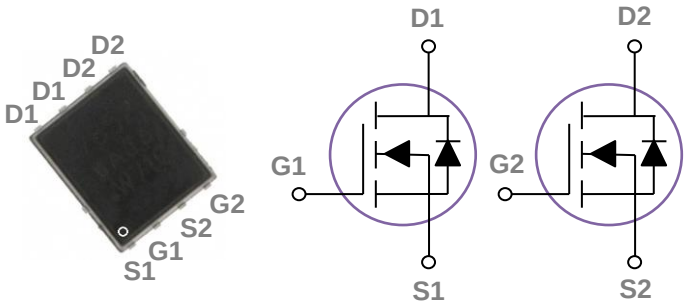
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
100V	185mΩ	10A

Features

- *100V,10A, RDS(ON) =185mΩ@VGS = 10V*
- *Improved dv/dt capability*
- *Fast switching*
- *100% EAS Guaranteed*
- *Green Device Available*

PPAK5x6 Dual Pin Configuration



Applications

- *Networking*
- *Load Switch*
- *LED applications*

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25˚C)	10	A
	Drain Current – Continuous (T_C=100˚C)	6.3	A
I_{DM}	Drain Current – Pulsed[1]	40	A
P_D	Power Dissipation (T_C=25˚C)	43	W
	Power Dissipation – Derate above 25˚C	0.34	W/˚C
T_{STG}	Storage Temperature Range	-55 to 150	˚C
T_J	Operating Junction Temperature Range	-55 to 150	˚C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	˚C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.9	˚C/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	100	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.10	---	V/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=100V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=80V , V_{GS}=0V , T_J=125℃	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=4A	---	145	185	mΩ
		V_{GS}=4.5V , I_D=2A	---	150	195	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.2	1.8	2.5	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4	---	mV/℃
gfs	Forward Transconductance	V_{DS}=10V , I_D=1A	---	5	---	S

Dynamic Characteristics

Q_g	Total Gate Charge[2,3]	V_{DS}=50V , V_{GS}=10V , I_D=2A	---	13.4	21	nC
Q_{gs}	Gate-Source Charge[2,3]		---	2.9	6	
Q_{gd}	Gate-Drain Charge[2,3]		---	1.7	4	
$T_{d(on)}$	Turn-On Delay Time[2,3]	V_{DD}=30V, V_{GS}=10V , R_G=3.3Ω I_D=1A	---	1.6	3	ns
T_r	Rise Time[2,3]		---	6.6	13	
$T_{d(off)}$	Turn-Off Delay Time[2,3]		---	11.5	22	
T_f	Fall Time[2,3]		---	3.6	7	
C_{iss}	Input Capacitance	V_{DS}=25V , V_{GS}=0V , F=1MHz	---	820	1190	pF
C_{oss}	Output Capacitance		---	35	55	
C_{rss}	Reverse Transfer Capacitance		---	20	30	
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, F=1MHz	---	1.3	2.6	Ω

Drain-Source Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	10	A
I_{SM}	Pulsed Source Current[2]		---	---	20	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1	V

Note :
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
3. Essentially independent of operating temperature.

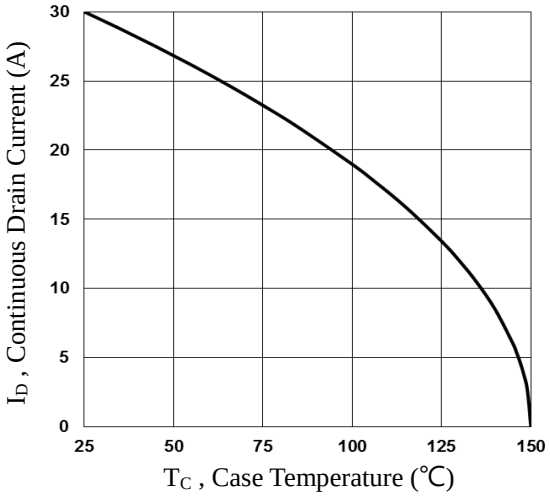


Fig.1 Continuous Drain Current vs. T_C

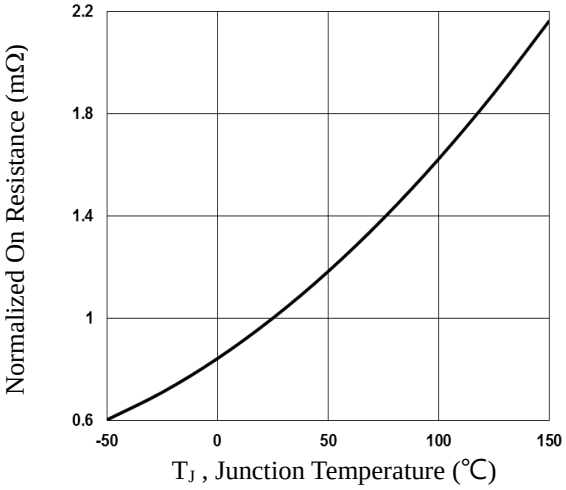


Fig.2 Normalized RDSON vs. T_J

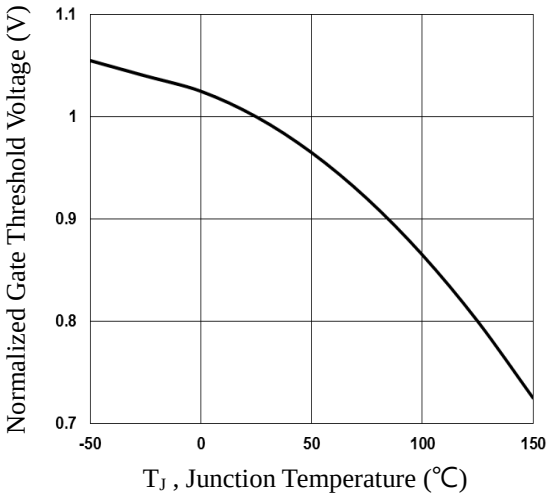


Fig.3 Normalized V_{th} vs. T_J

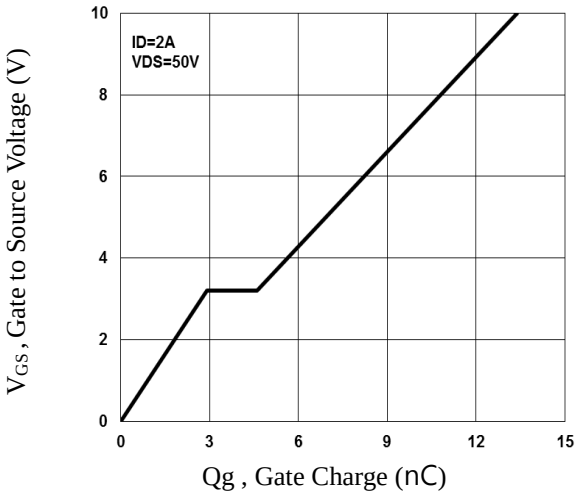


Fig.4 Gate Charge Waveform

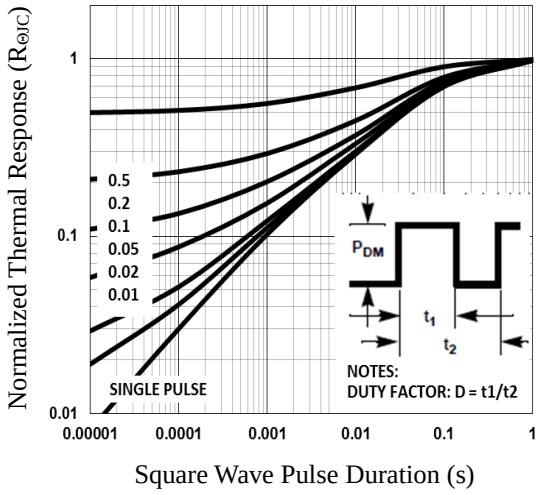


Fig.5 Normalized Transient Impedance

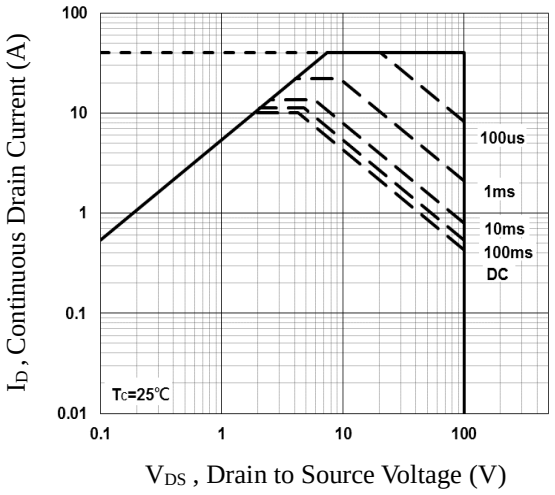


Fig.6 Maximum Safe Operation Area

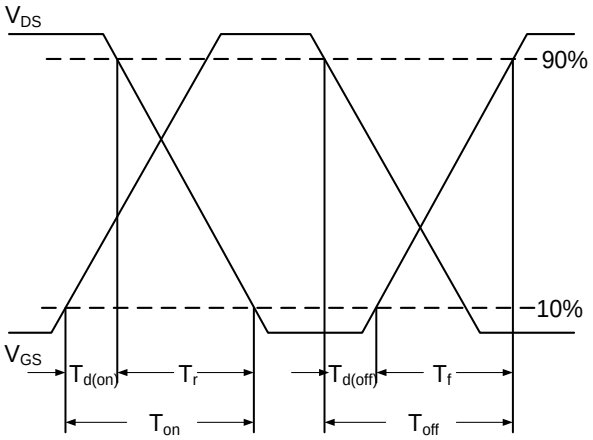


Fig.7 Switching Time Waveform

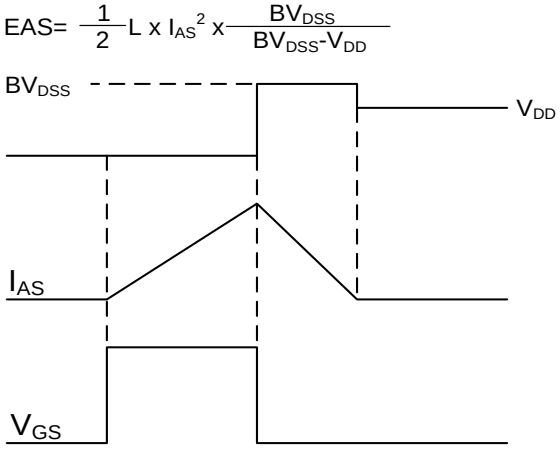
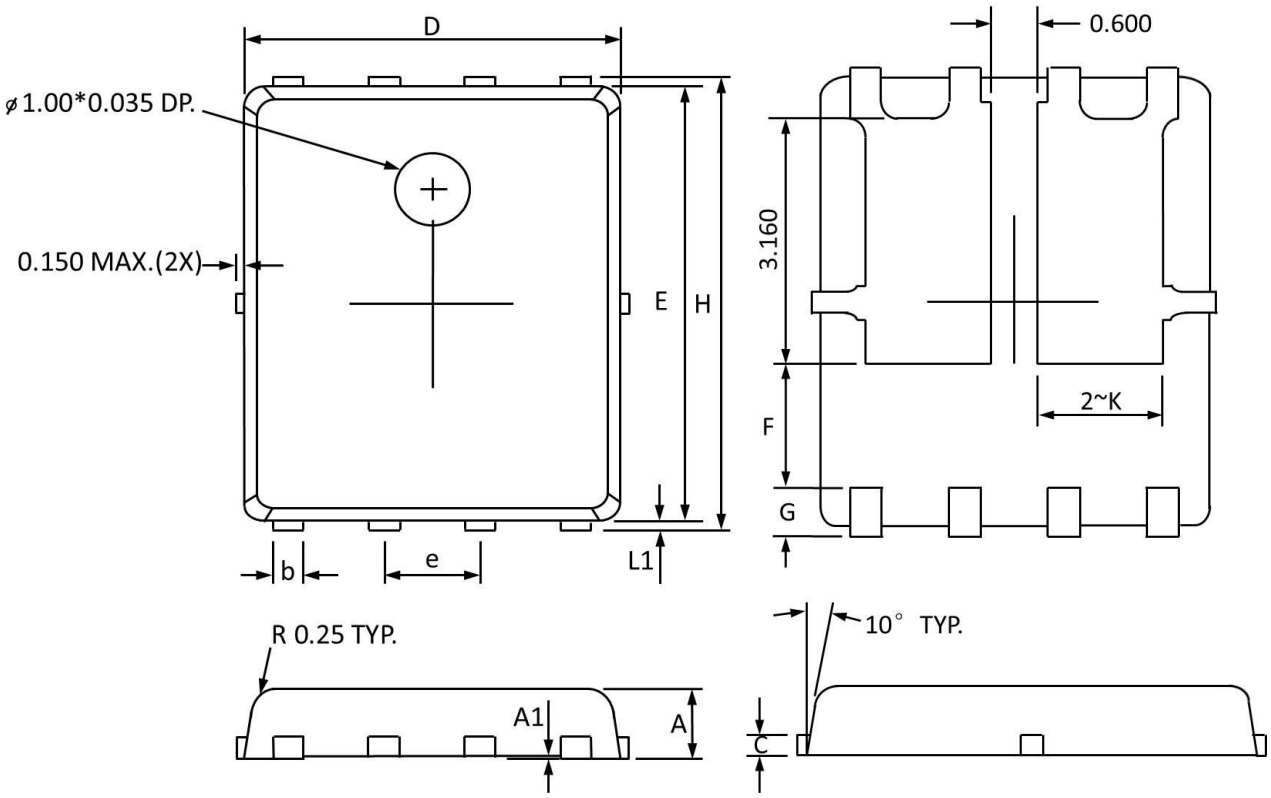


Fig.8 EAS Waveform

PPAK5x6 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.000	0.032	0.039
A1	0.000	0.005	0.000	0.000
b	0.350	0.490	0.014	0.019
C	0.254 Ref		0.254 Ref	
D	4.900	5.100	0.193	0.200
E	5.700	5.900	0.225	0.232
e	1.27 BSC		1.27 BSC	
F	1.600 Ref		1.600 Ref	
G	0.600 Ref		0.600 Ref	
H	5.950	6.200	0.235	0.244
L1	0.100	0.180	0.004	0.007
K	1.600 Ref		1.600 Ref	