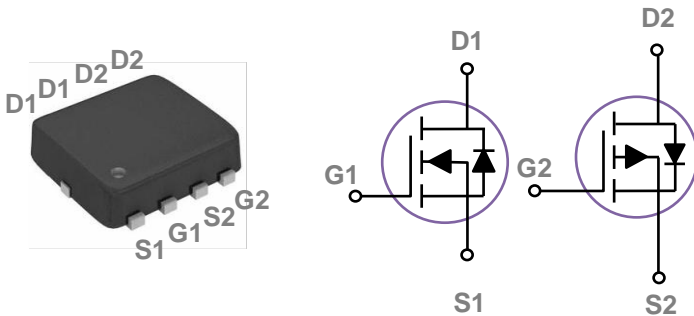


### General Description

These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	20mΩ	12A
-30V	50mΩ	-8A

### PPAK3X3 Dual NEP Pin Configuration



### Features

- Fast switching
- Green Device Available
- Suit for 4.5V Gate Drive Applications

### Applications

- DC Fan
- Motor Drive Applications
- Networking
- Half / Full Bridge Topology

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating		Units
$V_{DS}$	Drain-Source Voltage	30	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	12	-8	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	7.2	-4.8	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	48	-32	A
EAS	Single Pulse Avalanche Energy <sup>2,6</sup>	14	5	mJ
IAS	Single Pulse Avalanched Current <sup>2</sup>	17	10	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	20		W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.16		W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150		$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150		$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62.5	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	6.4	$^\circ\text{C/W}$

**N-CH Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	---	15	20	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A	---	21	30	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.5	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-4	---	mV/°C
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =6A	---	13	---	S

**Dynamic and switching Characteristics**

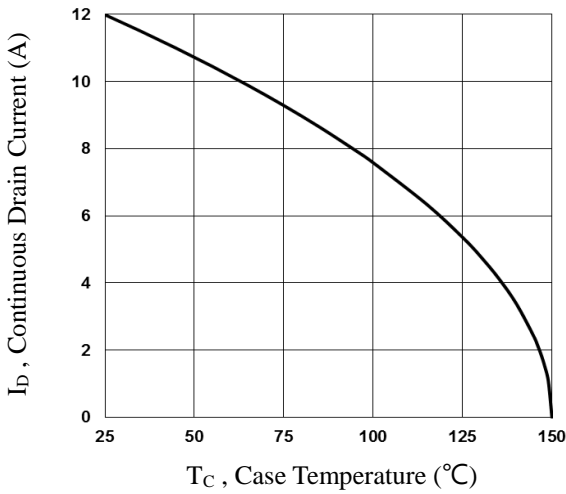
Q <sub>g</sub>	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A	---	4.1	6	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3, 4</sup>		---	1	1.4	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3, 4</sup>		---	2.1	4	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3, 4</sup>	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω I <sub>D</sub> =1A	---	2.8	5	ns
T <sub>r</sub>	Rise Time <sup>3, 4</sup>		---	7.2	14	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>		---	15.8	30	
T <sub>f</sub>	Fall Time <sup>3, 4</sup>		---	4.6	9	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1MHz	---	345	500	pF
C <sub>oss</sub>	Output Capacitance		---	55	80	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	32	55	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	3.2	6.4	Ω

**Drain-Source Diode Characteristics and Maximum Ratings**

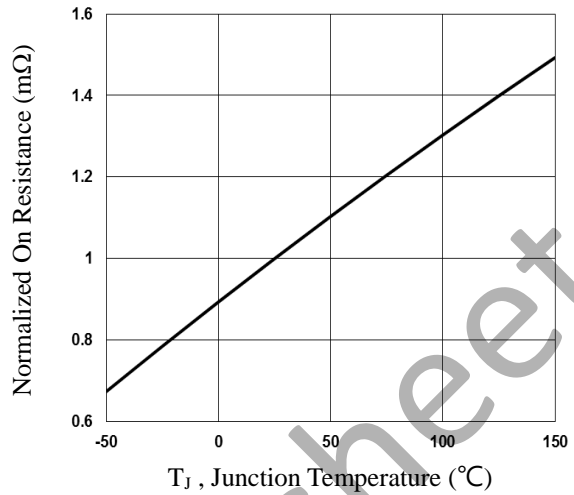
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	12	A
I <sub>SM</sub>	Pulsed Source Current		---	---	24	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

Note :

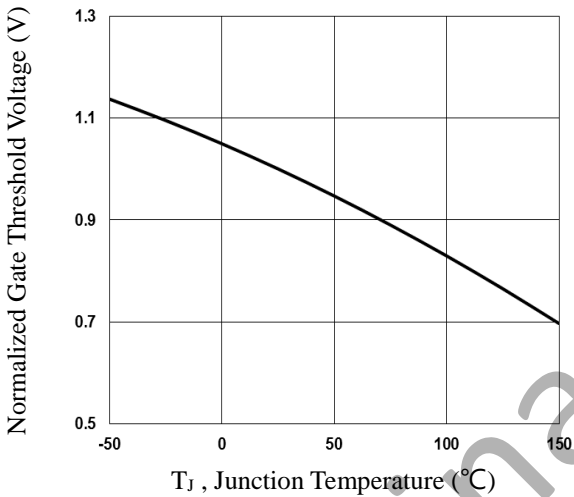
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=17A, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.



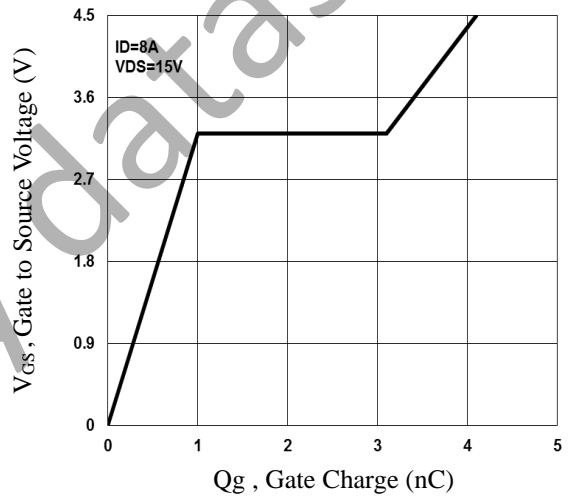
**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**



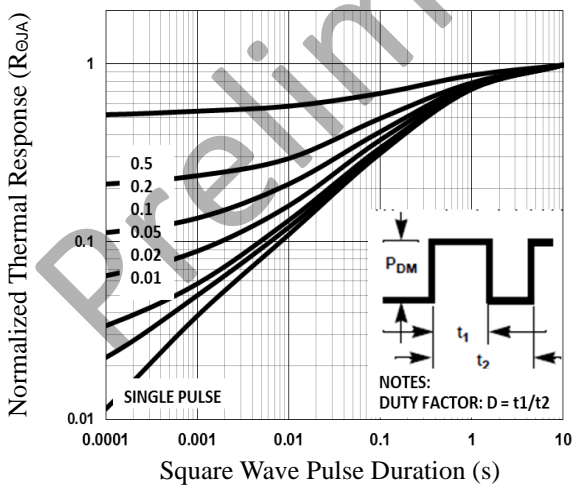
**Fig.2 Normalized R<sub>DSon</sub> vs. T<sub>j</sub>**



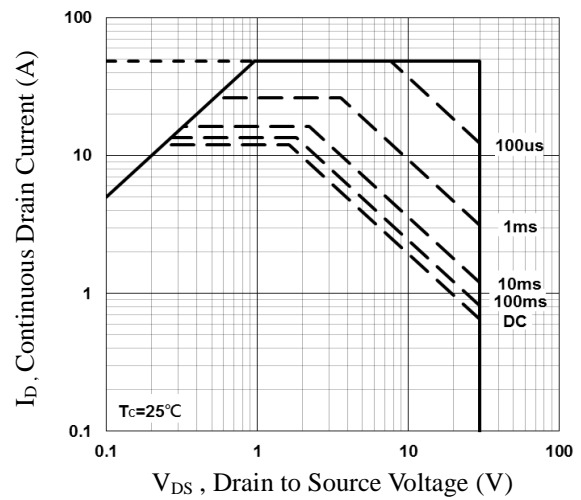
**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**



**Fig.4 Gate Charge Waveform**



**Fig.5 Normalized Transient Response**



**Fig.6 Maximum Safe Operation Area**

**P-CH Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	---	-0.03	---	V/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	---	40	50	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3A	---	60	75	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage		-1.2	-1.6	-2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	---	4	---	mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3A	---	3.5	---	S

**Dynamic and switching Characteristics**

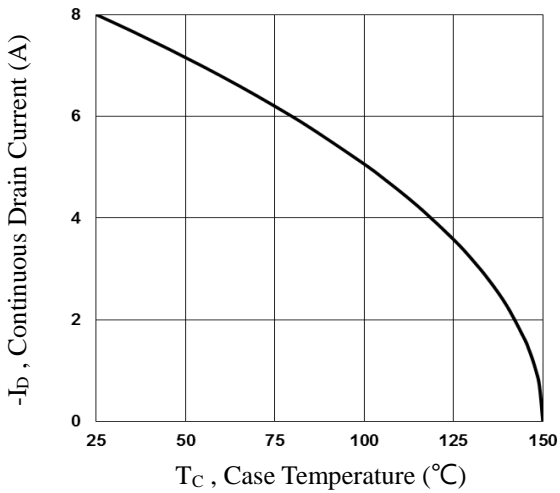
Q <sub>g</sub>	Total Gate Charge <sup>7,8</sup>		---	5.1	7	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>7,8</sup>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3A	---	2	3	
Q <sub>gd</sub>	Gate-Drain Charge <sup>7,8</sup>		---	2.2	4	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>7,8</sup>		---	3.4	6	ns
T <sub>r</sub>	Rise Time <sup>7,8</sup>	V <sub>DD</sub> =-15V, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω	---	10.8	21	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>7,8</sup>	I <sub>D</sub> =-1A	---	26.9	51	
T <sub>f</sub>	Fall Time <sup>7,8</sup>		---	6.9	13	
C <sub>iss</sub>	Input Capacitance		---	560	810	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, F=1MHz	---	55	80	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	40	60	

**Drain-Source Diode Characteristics and Maximum Ratings**

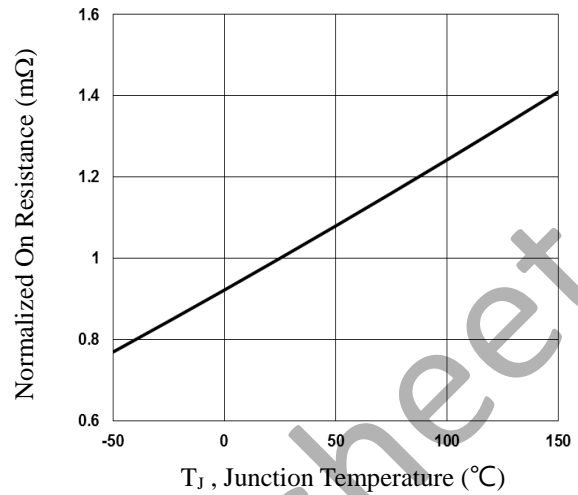
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-8	A
I <sub>SM</sub>	Pulsed Source Current		---	---	-16	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1	V

Note :

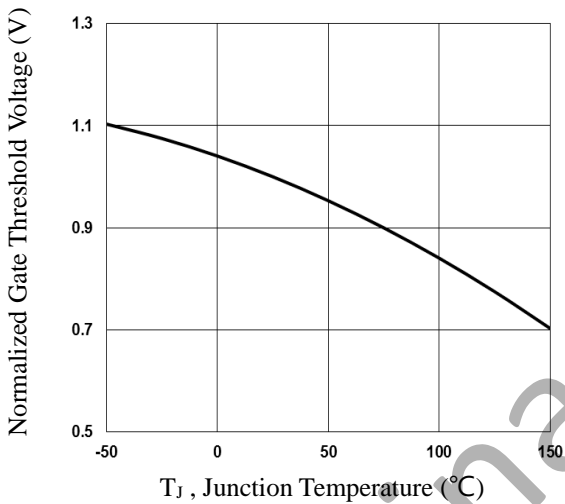
5. Repetitive Rating : Pulsed width limited by maximum junction temperature.
6. V<sub>DD</sub>=-25V, V<sub>GS</sub>=-10V, L=0.1mH, I<sub>AS</sub>=-10A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C
7. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
8. Essentially independent of operating temperature.



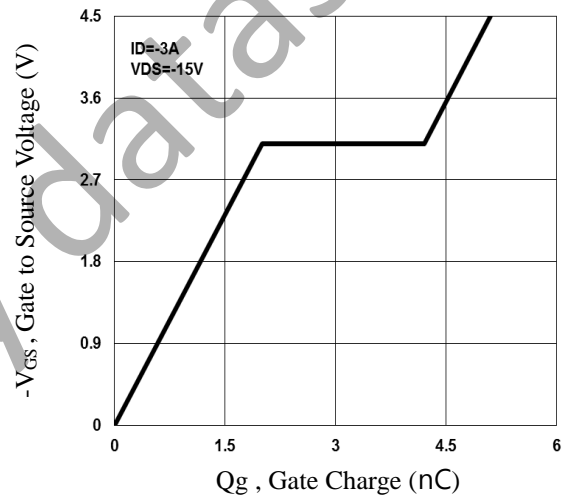
**Fig.1 Continuous Drain Current vs.  $T_c$**



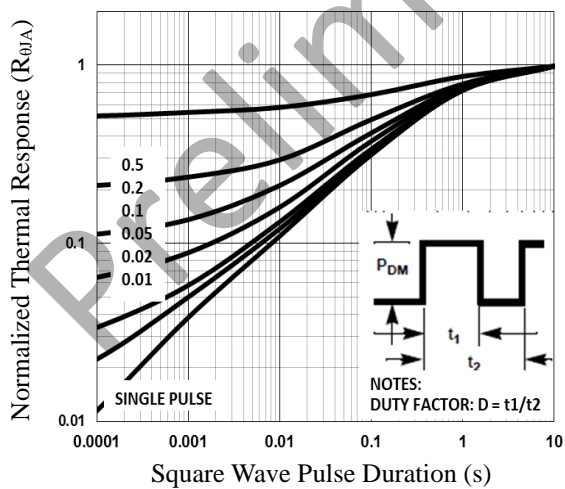
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$**



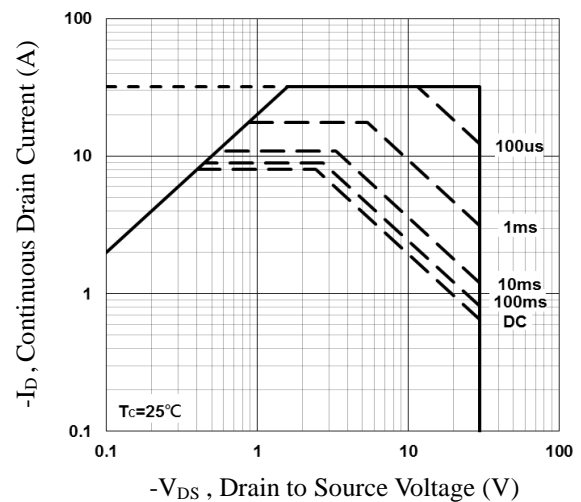
**Fig.3 Normalized  $V_{th}$  vs.  $T_j$**



**Fig.4 Gate Charge Waveform**



**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**

PPAK3x3 Dual NEP PACKAGE INFORMATION

