

## General Description

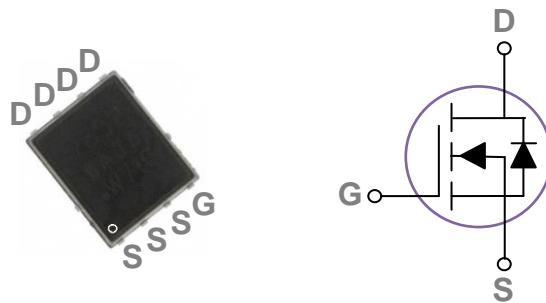
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
40V	6.5mΩ	70A

## Features

- 40V, 70A, RDS(ON) = 6.5mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

## PPAK5x6 Pin Configuration



## Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

## Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	70	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	44	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	280	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	76	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	39	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	83	W
	Power Dissipation – Derate above 25°C	0.66	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.5	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	40	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.03	---	$\text{V}/^{\circ}\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=40\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=32\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10\text{V}$ , $I_D=10\text{A}$	---	5.5	6.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=5\text{A}$	---	6.9	8.5	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = 250\mu\text{A}$	1.2	1.6	2.5	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-5	---	$\text{mV}/^{\circ}\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=3\text{A}$	---	16	---	S

**Dynamic Characteristics**

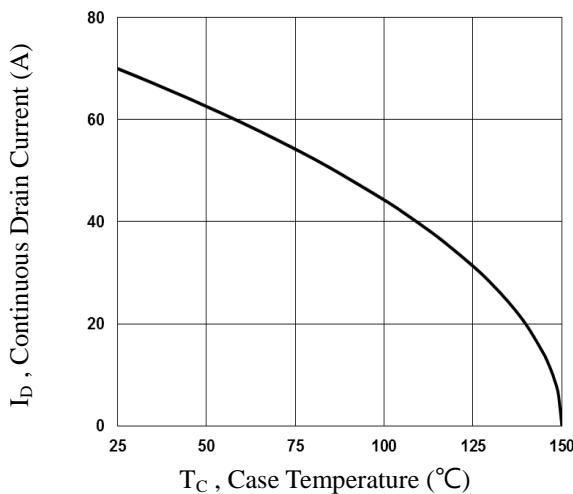
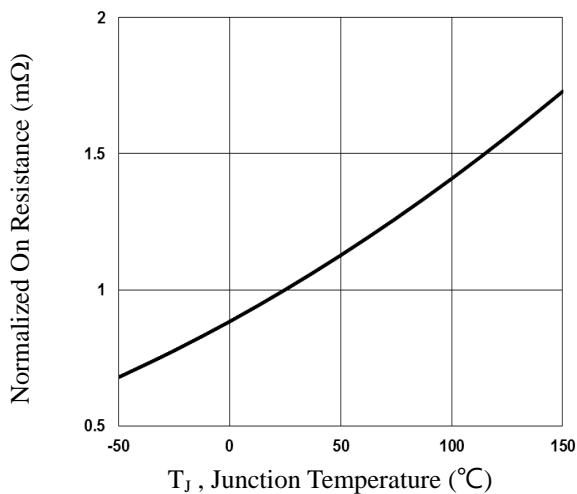
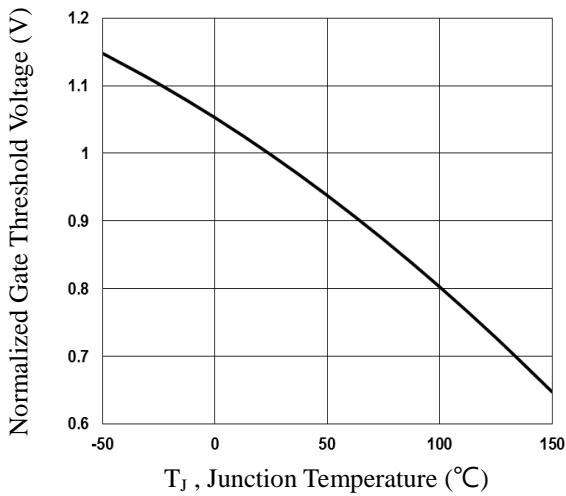
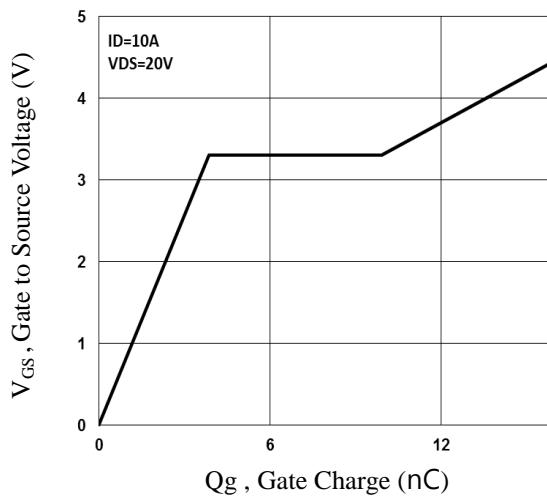
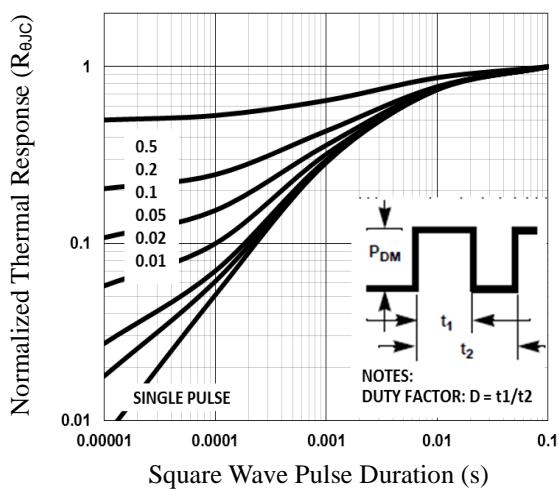
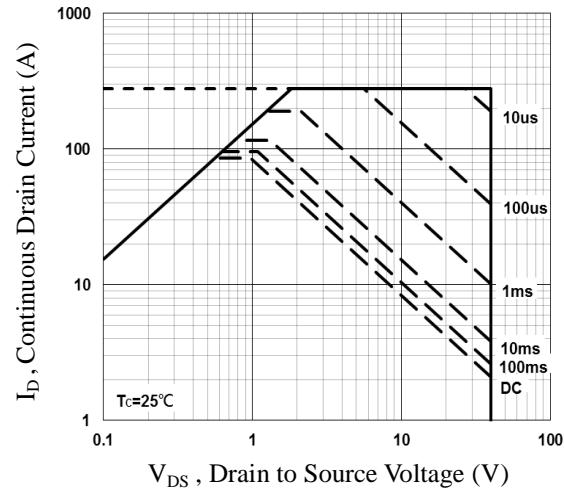
$Q_g$	Total Gate Charge <sup>3,4</sup>	$V_{DS}=20\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D=10\text{A}$	---	16.2	32	nC
$Q_{gs}$	Gate-Source Charge <sup>3,4</sup>		---	3.85	7	
$Q_{gd}$	Gate-Drain Charge <sup>3,4</sup>		---	6.05	12	
$T_{d(on)}$	Turn-On Delay Time <sup>3,4</sup>	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$	---	13.6	25	ns
$T_r$	Rise Time <sup>3,4</sup>		---	2.5	5	
$T_{d(off)}$	Turn-Off Delay Time <sup>3,4</sup>		---	68	120	
$T_f$	Fall Time <sup>3,4</sup>		---	5	10	
$C_{iss}$	Input Capacitance		---	1540	2500	pF
$C_{oss}$	Output Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	171	330	
$C_{rss}$	Reverse Transfer Capacitance		---	115	220	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	1.2	2.2	$\Omega$

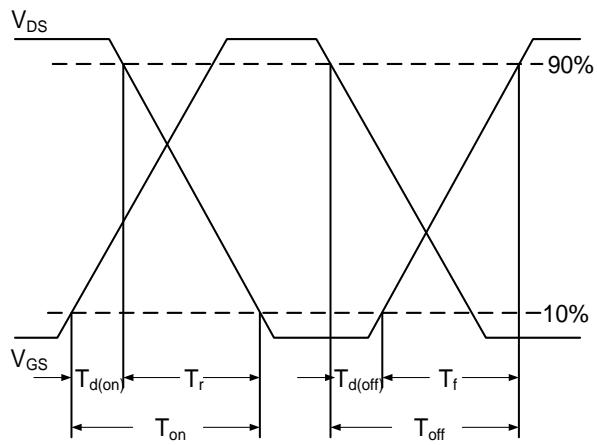
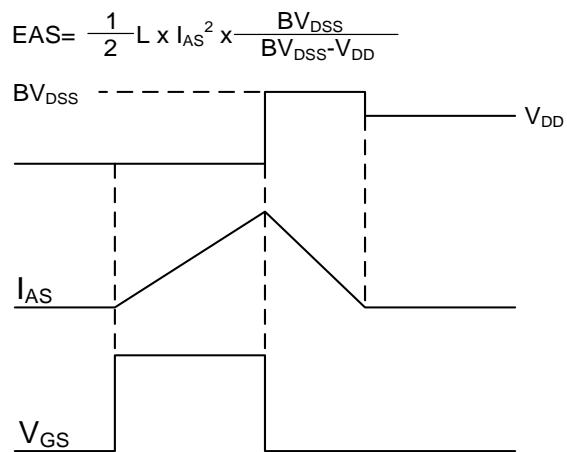
**Drain-Source Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	70	A
$I_{SM}$	Pulsed Source Current <sup>3</sup>		---	---	140	A
$V_{SD}$	Diode Forward Voltage <sup>3</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

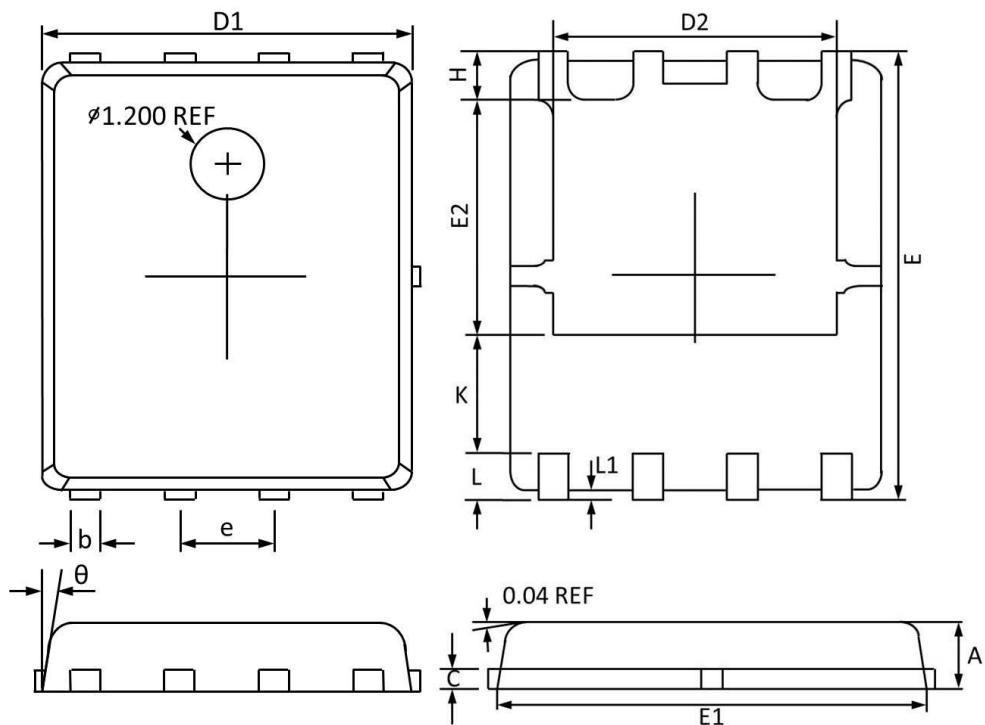
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=39\text{A}$ , Starting  $T_J=25\text{ }^{\circ}\text{C}$
3. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_c$** 

**Fig.2 Normalized RDSON vs.  $T_j$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## PPAK5x6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.100	0.800	0.043	0.031
b	0.510	0.330	0.020	0.013
C	0.300	0.200	0.012	0.008
D1	5.100	4.800	0.201	0.189
D2	4.100	3.610	0.161	0.142
E	6.200	5.900	0.244	0.232
E1	5.900	5.700	0.232	0.224
E2	3.780	3.350	0.149	0.132
e	1.27BSC		0.05BSC	
H	0.700	0.410	0.028	0.016
K	1.500	1.100	0.059	0.043
L	0.710	0.510	0.028	0.020
L1	0.200	0.060	0.008	0.002