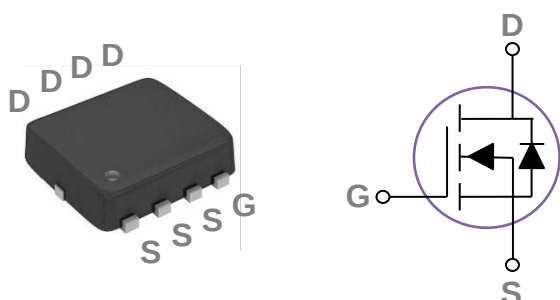


## General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

## PPAK3X3 Pin Configuration



| BVDSS | RDS(ON) | ID  |
|-------|---------|-----|
| 60V   | 15mΩ    | 35A |

## Features

- 60V,35A,  $R_{DS(ON)} = 15m\Omega$  @  $V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

## Applications

- Motor Drive
- Power Tools
- LED Lighting
- Quick Charger

## Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

| Symbol    | Parameter                                              | Rating     | Units               |
|-----------|--------------------------------------------------------|------------|---------------------|
| $V_{DS}$  | Drain-Source Voltage                                   | 60         | V                   |
| $V_{GS}$  | Gate-Source Voltage                                    | $\pm 20$   | V                   |
| $I_D$     | Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )  | 35         | A                   |
|           | Drain Current – Continuous ( $T_c=100^\circ\text{C}$ ) | 22         | A                   |
| $I_{DM}$  | Drain Current – Pulsed <sup>1</sup>                    | 140        | A                   |
| EAS       | Single Pulse Avalanche Energy <sup>2</sup>             | 45         | mJ                  |
| IAS       | Single Pulse Avalanched Current <sup>2</sup>           | 30         | A                   |
| $P_D$     | Power Dissipation ( $T_c=25^\circ\text{C}$ )           | 46         | W                   |
|           | Power Dissipation – Derate above $25^\circ\text{C}$    | 0.37       | W/ $^\circ\text{C}$ |
| $T_{STG}$ | Storage Temperature Range                              | -55 to 150 | $^\circ\text{C}$    |
| $T_J$     | Operating Junction Temperature Range                   | -55 to 150 | $^\circ\text{C}$    |

## Thermal Characteristics

| Symbol          | Parameter                              | Typ. | Max. | Unit               |
|-----------------|----------------------------------------|------|------|--------------------|
| $R_{\theta JA}$ | Thermal Resistance Junction to ambient | ---  | 62   | $^\circ\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance Junction to Case    | ---  | 2.7  | $^\circ\text{C/W}$ |

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

| Symbol     | Parameter                      | Conditions                                           | Min. | Typ. | Max.      | Unit    |
|------------|--------------------------------|------------------------------------------------------|------|------|-----------|---------|
| $BV_{DSS}$ | Drain-Source Breakdown Voltage | $V_{GS}=0V$ , $I_D=250\mu A$                         | 60   | ---  | ---       | V       |
| $I_{DSS}$  | Drain-Source Leakage Current   | $V_{DS}=60V$ , $V_{GS}=0V$ , $T_J=25^\circ\text{C}$  | ---  | ---  | 1         | $\mu A$ |
|            |                                | $V_{DS}=48V$ , $V_{GS}=0V$ , $T_J=125^\circ\text{C}$ | ---  | ---  | 10        | $\mu A$ |
| $I_{GSS}$  | Gate-Source Leakage Current    | $V_{GS}=\pm 20V$ , $V_{DS}=0V$                       | ---  | ---  | $\pm 100$ | nA      |

**On Characteristics**

|              |                                                |                                  |     |     |     |            |
|--------------|------------------------------------------------|----------------------------------|-----|-----|-----|------------|
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance <sup>3</sup> | $V_{GS}=10V$ , $I_D=10A$         | --- | 13  | 15  | m $\Omega$ |
|              |                                                | $V_{GS}=4.5V$ , $I_D=5A$         | --- | 16  | 19  | m $\Omega$ |
| $V_{GS(th)}$ | Gate Threshold Voltage                         | $V_{GS}=V_{DS}$ , $I_D=250\mu A$ | 1.2 | 1.7 | 2.5 | V          |
| gfs          | Forward Transconductance                       | $V_{DS}=10V$ , $I_D=3A$          | --- | 10  | --- | S          |

**Dynamic and switching Characteristics**

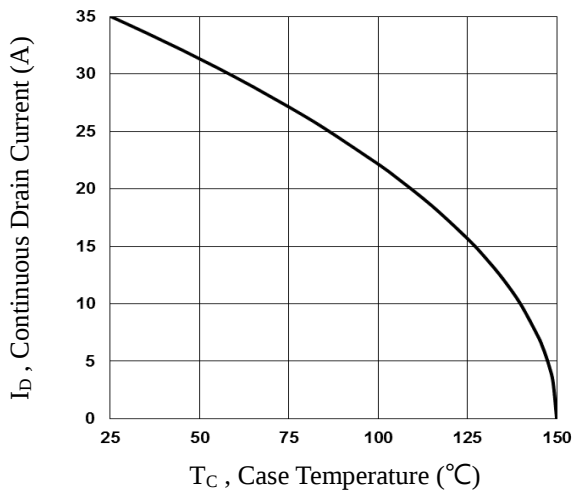
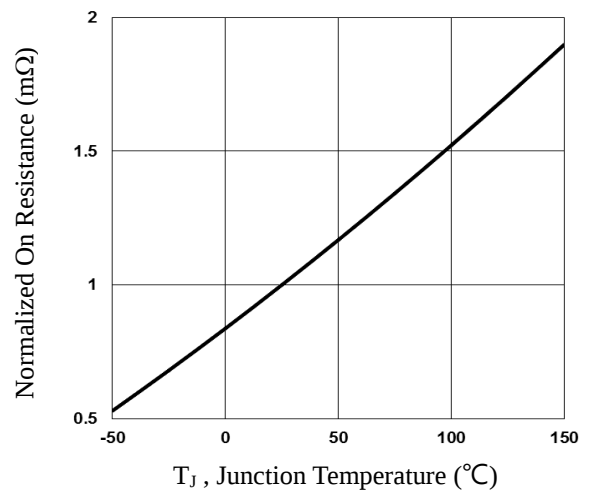
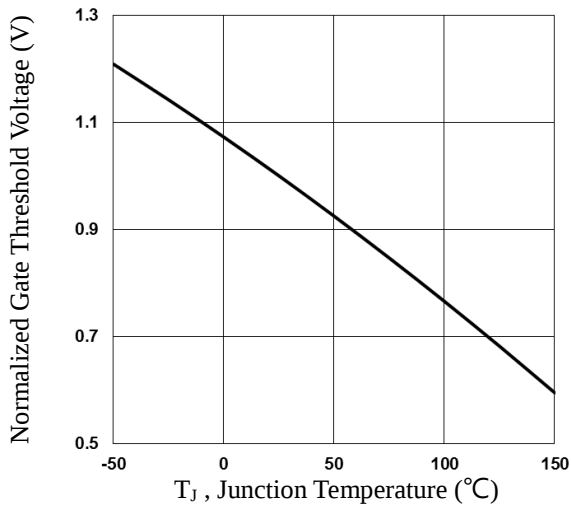
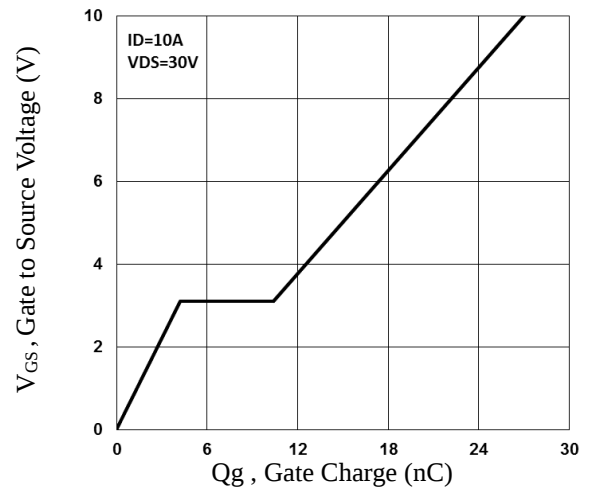
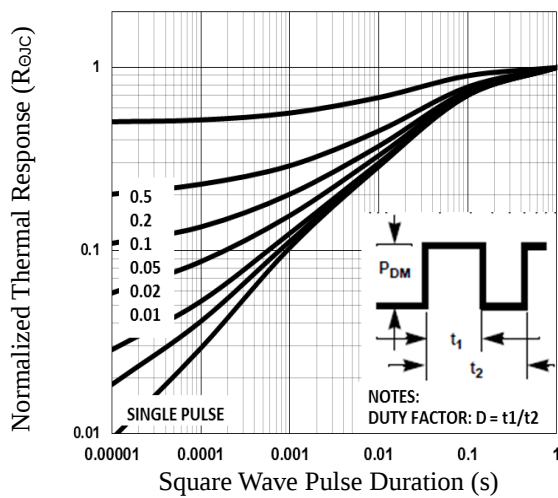
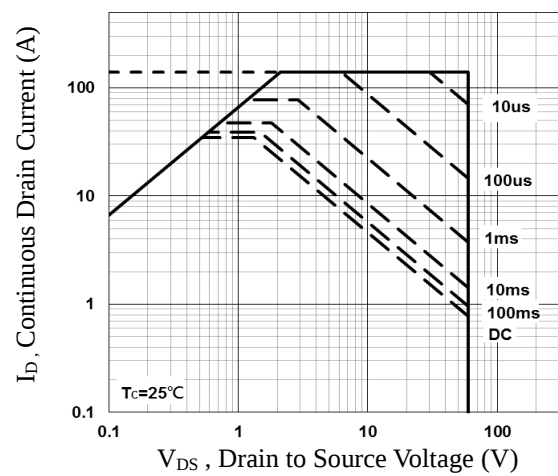
|              |                                     |                                                         |     |      |      |          |
|--------------|-------------------------------------|---------------------------------------------------------|-----|------|------|----------|
| $Q_g$        | Total Gate Charge <sup>3, 4</sup>   | $V_{DS}=30V$ , $V_{GS}=10V$ , $I_D=10A$                 | --- | 27   | 54   | nC       |
| $Q_{gs}$     | Gate-Source Charge <sup>3, 4</sup>  |                                                         | --- | 4.2  | 9    |          |
| $Q_{gd}$     | Gate-Drain Charge <sup>3, 4</sup>   |                                                         | --- | 6.2  | 12   |          |
| $T_{d(on)}$  | Turn-On Delay Time <sup>3, 4</sup>  | $V_{DD}=15V$ , $V_{GS}=10V$ , $R_G=6\Omega$<br>$I_D=1A$ | --- | 8.6  | 16   | ns       |
| $T_r$        | Rise Time <sup>3, 4</sup>           |                                                         | --- | 24.2 | 48   |          |
| $T_{d(off)}$ | Turn-Off Delay Time <sup>3, 4</sup> |                                                         | --- | 32.3 | 64   |          |
| $T_f$        | Fall Time <sup>3, 4</sup>           |                                                         | --- | 7.9  | 16   |          |
| $C_{iss}$    | Input Capacitance                   | $V_{DS}=25V$ , $V_{GS}=0V$ , $F=1\text{MHz}$            | --- | 1515 | 3000 | pF       |
| $C_{oss}$    | Output Capacitance                  |                                                         | --- | 120  | 200  |          |
| $C_{rss}$    | Reverse Transfer Capacitance        |                                                         | --- | 76   | 120  |          |
| $R_g$        | Gate resistance                     | $V_{GS}=0V$ , $V_{DS}=0V$ , $F=1\text{MHz}$             | --- | 1.8  | 3.6  | $\Omega$ |

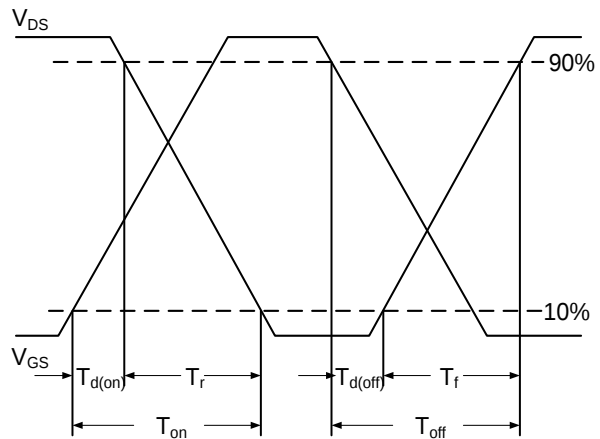
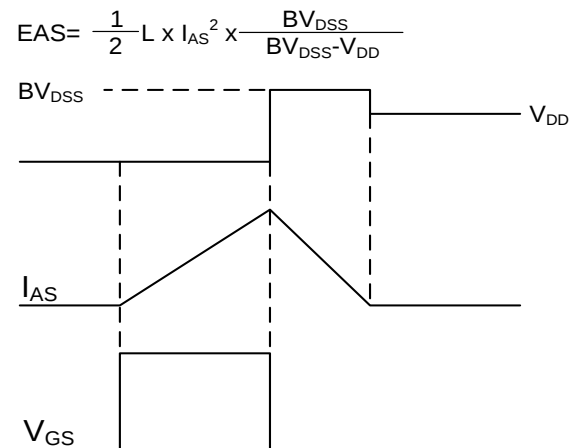
**Drain-Source Diode Characteristics and Maximum Ratings**

| Symbol   | Parameter                          | Conditions                                      | Min. | Typ. | Max. | Unit |
|----------|------------------------------------|-------------------------------------------------|------|------|------|------|
| $I_S$    | Continuous Source Current          | $V_G=V_D=0V$ , Force Current                    | ---  | ---  | 35   | A    |
| $I_{SM}$ | Pulsed Source Current <sup>3</sup> |                                                 | ---  | ---  | 70   | A    |
| $V_{SD}$ | Diode Forward Voltage <sup>3</sup> | $V_{GS}=0V$ , $I_S=1A$ , $T_J=25^\circ\text{C}$ | ---  | ---  | 1    | V    |
| $t_{rr}$ | Reverse Recovery Time              | $V_{GS}=0V$ , $I_S=1A$ , $di/dt=100A/\mu s$     | ---  | 19   | ---  | ns   |
| $Q_{rr}$ | Reverse Recovery Charge            | $T_J=25^\circ\text{C}$                          | ---  | 5    | ---  | nC   |

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25V$ ,  $V_{GS}=10V$ ,  $L=0.1mH$ ,  $I_{AS}=30A$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_c$** 

**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Response**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

