

### General Description

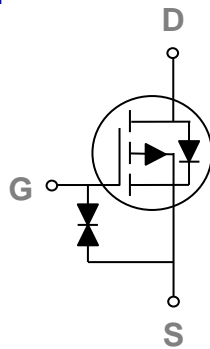
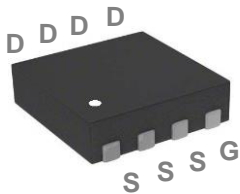
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-30V	20mΩ	-30A

### Features

- -30V,-30A,  $R_{DS(ON)} = 20m\Omega @ V_{GS} = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

### DFN3x3 Pin Configuration



### Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	-30	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	-19	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-120	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	27	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.22	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	4.6	$^\circ\text{C/W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	---	-0.03	---	V/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-27V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±20	uA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-8A	---	16	20	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-6A	---	26	35	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage		-1.2	-1.6	-2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	---	4	---	mV/°C
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-8A	---	6.8	---	S

**Dynamic and switching Characteristics**

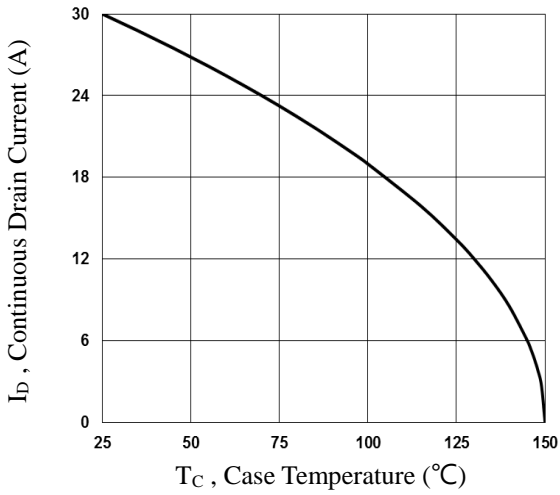
Q <sub>g</sub>	Total Gate Charge <sup>2, 3</sup>		---	11	17	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2, 3</sup>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A	---	3.4	6	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2, 3</sup>		---	4.2	8	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2, 3</sup>	V <sub>DD</sub> =-15V, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω I <sub>D</sub> =-1A	---	5.8	11	ns
T <sub>r</sub>	Rise Time <sup>2, 3</sup>		---	18.8	36	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2, 3</sup>		---	46.9	90	
T <sub>f</sub>	Fall Time <sup>2, 3</sup>		---	12.3	23	
C <sub>iss</sub>	Input Capacitance		---	1250	2500	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, F=1MHz	---	160	320	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	90	180	

**Drain-Source Diode Characteristics and Maximum Ratings**

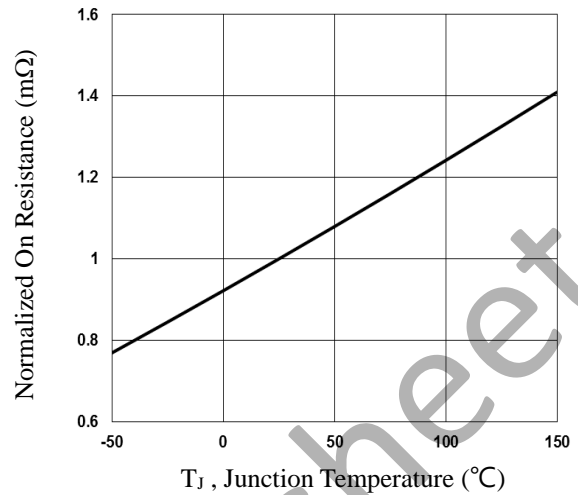
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-30	A
I <sub>SM</sub>	Pulsed Source Current		---	---	-60	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1	V

Note :

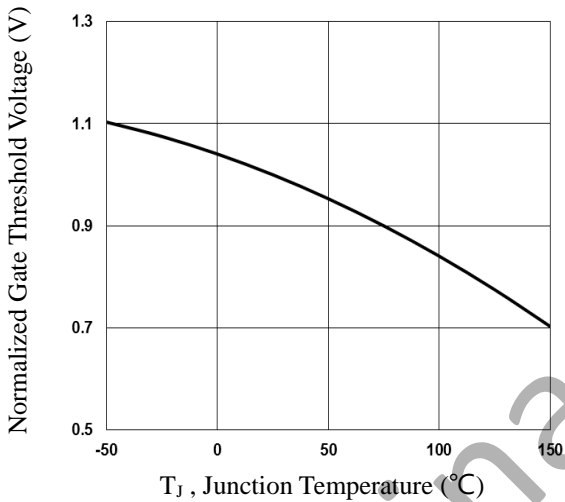
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



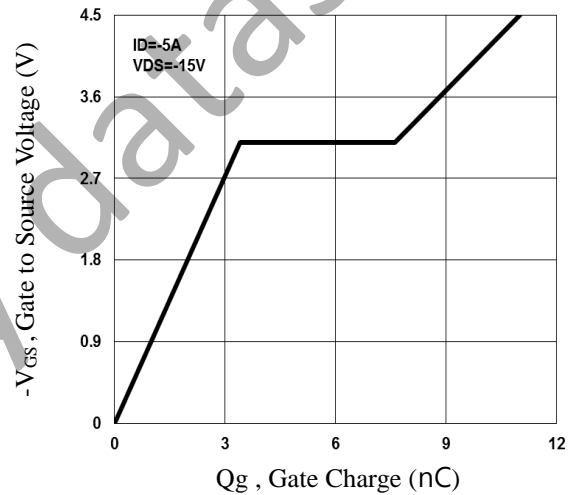
**Fig.1 Continuous Drain Current vs.  $T_c$**



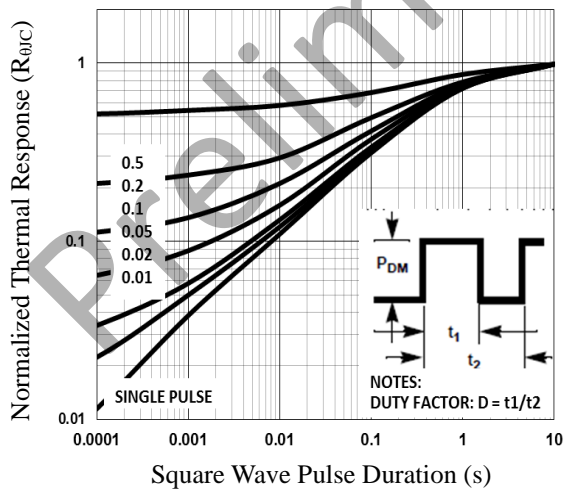
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$**



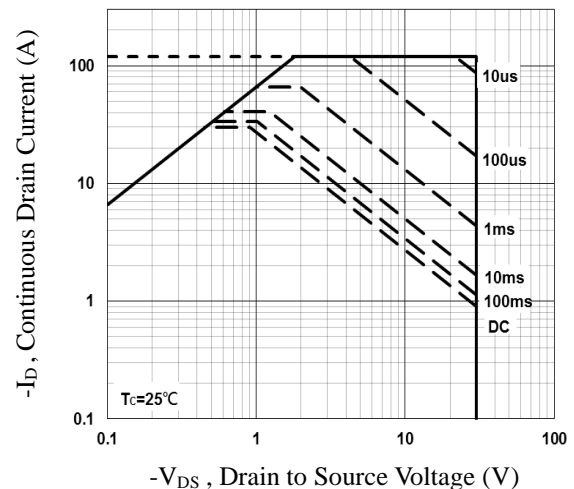
**Fig.3 Normalized  $V_{th}$  vs.  $T_j$**



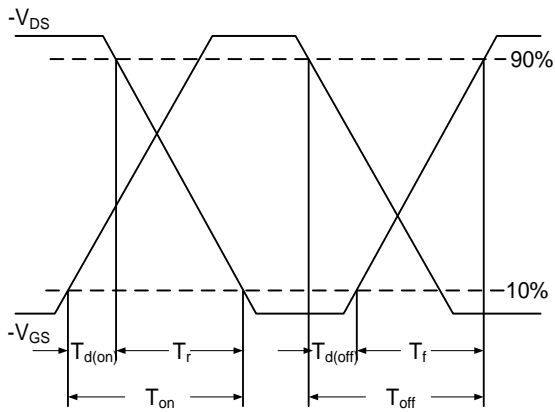
**Fig.4 Gate Charge Waveform**



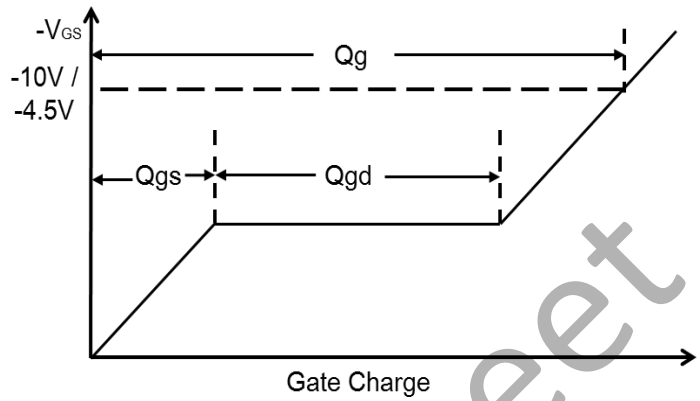
**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**



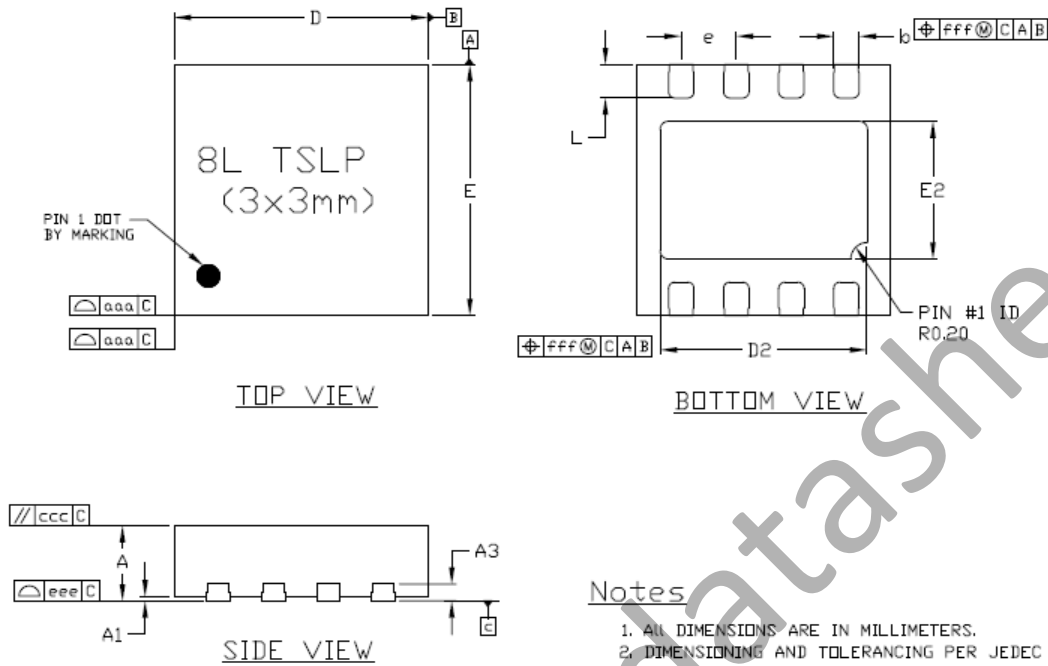
**Fig.7 Switching Time Waveform**



**Fig.8 Gate Charge Waveform**

Preliminary datasheet

## DFN3x3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		
	Min	Nom	Max
A	0.700	0.750	0.800
A1	-	-	0.050
A3	0.203Ref.		
D	2.950	3.000	3.050
E	2.950	3.000	3.050
D2	2.400	2.450	2.500
E2	1.600	1.650	1.700
b	0.250	0.300	0.350
e	0.650BSC		
L	0.350	0.400	0.450
aaa	0.010		
bbb	0.010		
ccc	0.010		
ddd	0.050		
eee	0.080		
fff	0.100		