

General Description

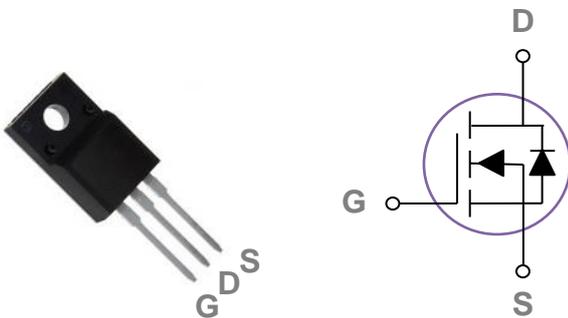
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

| | | |
|-------|-------|-----|
| BVDSS | RDSON | ID |
| 60V | 8.2mΩ | 51A |

Features

- 60V,51A, $R_{DS(ON)} = 8.2m\Omega @ V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO220F Pin Configuration



Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

| Symbol | Parameter | Rating | Units |
|-----------|--|------------|---------------|
| V_{DS} | Drain-Source Voltage | 60 | V |
| V_{GS} | Gate-Source Voltage | ± 25 | V |
| I_D | Drain Current – Continuous ($T_c=25^\circ C$) | 51 | A |
| | Drain Current – Continuous ($T_c=100^\circ C$) | 32.3 | A |
| I_{DM} | Drain Current – Pulsed ¹ | 204 | A |
| EAS | Single Pulse Avalanche Energy ² | 238 | mJ |
| IAS | Single Pulse Avalanche Current ² | 69 | A |
| P_D | Power Dissipation ($T_c=25^\circ C$) | 41 | W |
| | Power Dissipation – Derate above $25^\circ C$ | 0.33 | W/ $^\circ C$ |
| T_{STG} | Storage Temperature Range | -50 to 150 | $^\circ C$ |
| T_J | Operating Junction Temperature Range | -50 to 150 | $^\circ C$ |

Thermal Characteristics

| Symbol | Parameter | Typ. | Max. | Unit |
|-----------------|--|------|------|--------------|
| $R_{\theta JA}$ | Thermal Resistance Junction to ambient | --- | 62 | $^\circ C/W$ |
| $R_{\theta JC}$ | Thermal Resistance Junction to Case | --- | 3.05 | $^\circ C/W$ |

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------------|---|--|------|------|------|------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250μA | 60 | --- | --- | V |
| ΔBV _{DSS} /ΔT _J | BV _{DSS} Temperature Coefficient | Reference to 25°C, I _D =1mA | --- | 0.05 | --- | V/°C |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =60V, V _{GS} =0V, T _J =25°C | --- | --- | 1 | μA |
| | | V _{DS} =48V, V _{GS} =0V, T _J =125°C | --- | --- | 10 | μA |
| I _{GSS} | Gate-Source Leakage Current | V _{GS} =±25V, V _{DS} =0V | --- | --- | ±100 | nA |

On Characteristics

| | | | | | | |
|----------------------|---|--|-----|-----|-----|-------|
| R _{DS(ON)} | Static Drain-Source On-Resistance | V _{GS} =10V, I _D =20A | --- | 6.8 | 8.2 | mΩ |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _D =250μA | 2.5 | 3 | 4 | V |
| ΔV _{GS(th)} | V _{GS(th)} Temperature Coefficient | | --- | -5 | --- | mV/°C |
| g _{fs} | Forward Transconductance | V _{DS} =10V, I _D =3A | --- | 10 | --- | S |

Dynamic and switching Characteristics

| | | | | | | |
|---------------------|------------------------------------|--|-----|------|------|----|
| Q _g | Total Gate Charge ^{3,4} | V _{DS} =30V, V _{GS} =10V, I _D =10A | --- | 26.9 | 48 | nC |
| Q _{gs} | Gate-Source Charge ^{3,4} | | --- | 10.7 | 20 | |
| Q _{gd} | Gate-Drain Charge ^{3,4} | | --- | 6.55 | 13 | |
| T _{d(on)} | Turn-On Delay Time ^{3,4} | V _{DD} =30V, V _{GS} =10V, R _G =6Ω I _D =1A | --- | 16 | 30 | ns |
| T _r | Rise Time ^{3,4} | | --- | 12 | 24 | |
| T _{d(off)} | Turn-Off Delay Time ^{3,4} | | --- | 32 | 55 | |
| T _f | Fall Time ^{3,4} | | --- | 23 | 40 | |
| C _{iss} | Input Capacitance | V _{DS} =30V, V _{GS} =0V, F=1MHz | --- | 1690 | 2600 | pF |
| C _{oss} | Output Capacitance | | --- | 294 | 450 | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 90 | 180 | |
| R _g | Gate resistance | V _{GS} =0V, V _{DS} =0V, F=1MHz | --- | 1.3 | 2.5 | Ω |

Drain-Source Diode Characteristics and Maximum Ratings

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|---------------------------|---|------|------|------|------|
| I _S | Continuous Source Current | V _G =V _D =0V, Force Current | --- | --- | 51 | A |
| I _{SM} | Pulsed Source Current | | --- | --- | 102 | A |
| V _{SD} | Diode Forward Voltage | V _{GS} =0V, I _S =1A, T _J =25°C | --- | --- | 1 | V |

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=69A., Starting T_J=25°C
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

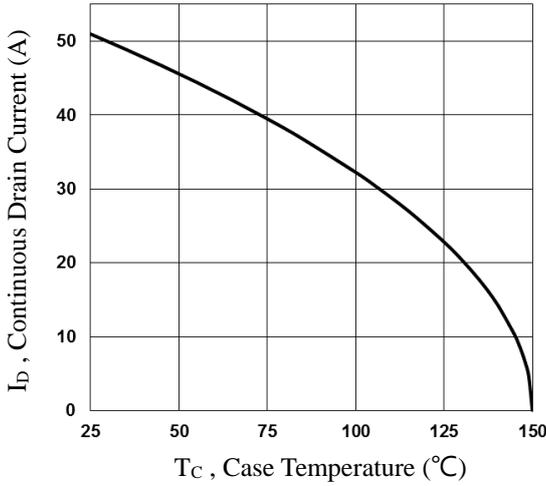


Fig.1 Continuous Drain Current vs. T_c

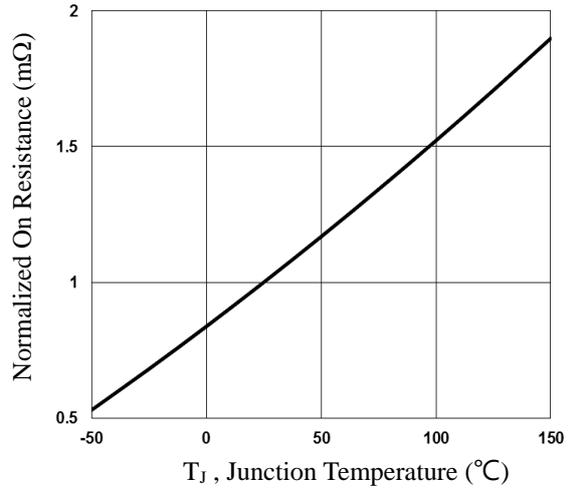


Fig.2 Normalized R_{DSon} vs. T_j

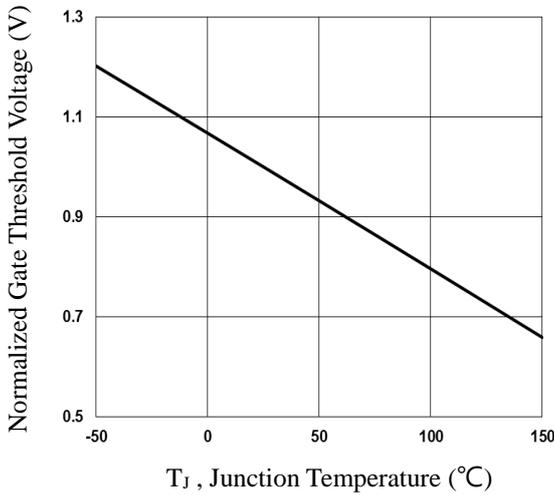


Fig.3 Normalized V_{th} vs. T_j

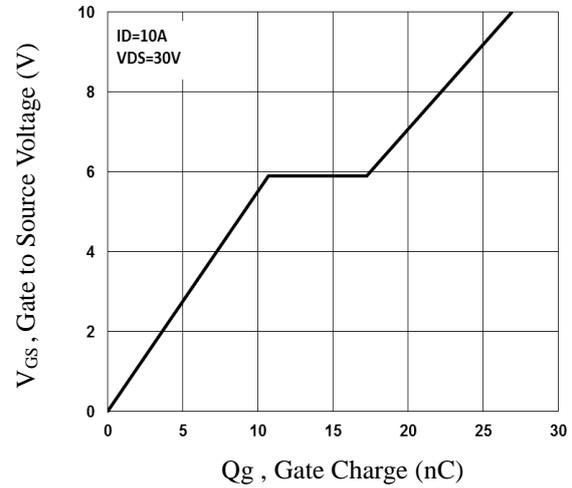


Fig.4 Gate Charge Characteristics

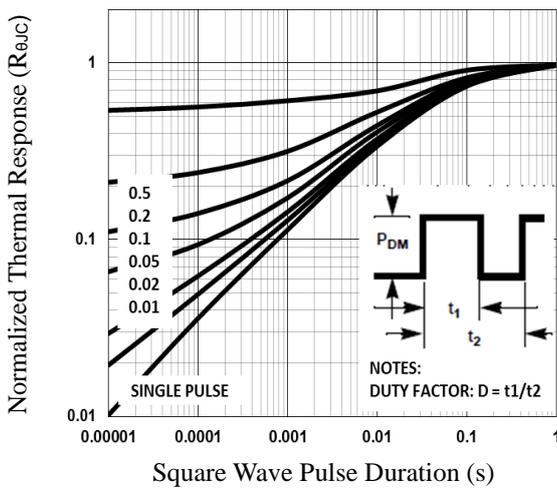


Fig.5 Normalized Transient Impedance

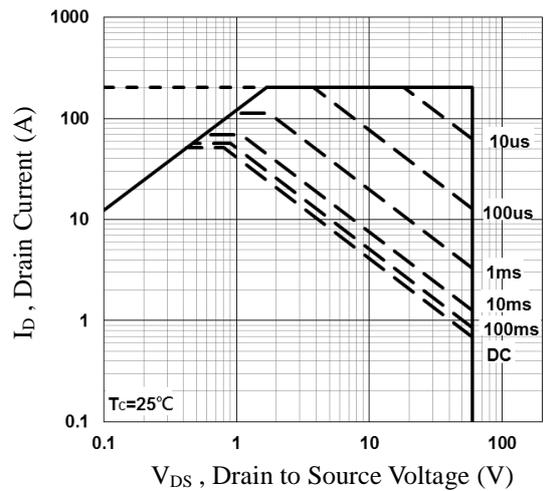


Fig.6 Maximum Safe Operation Area

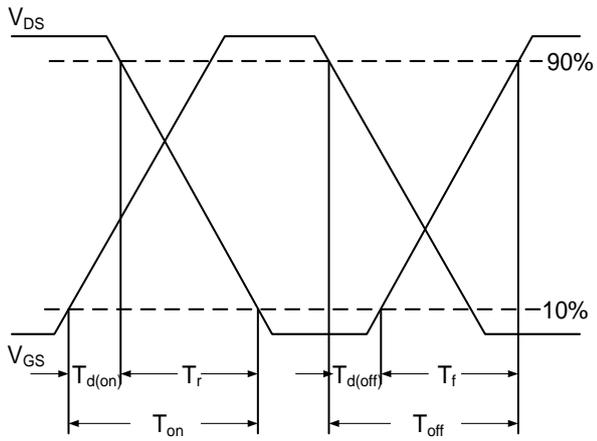


Fig.7 Switching Time Waveform

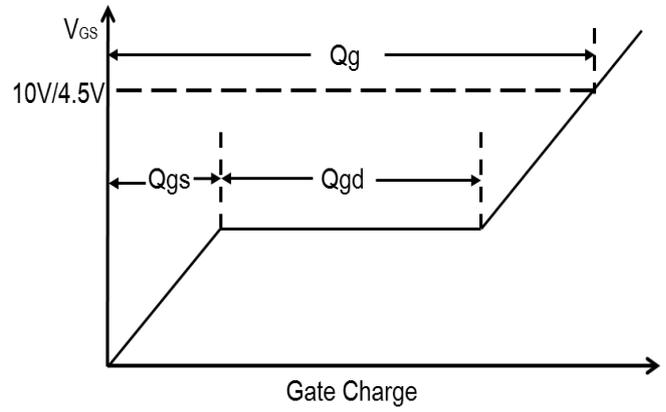
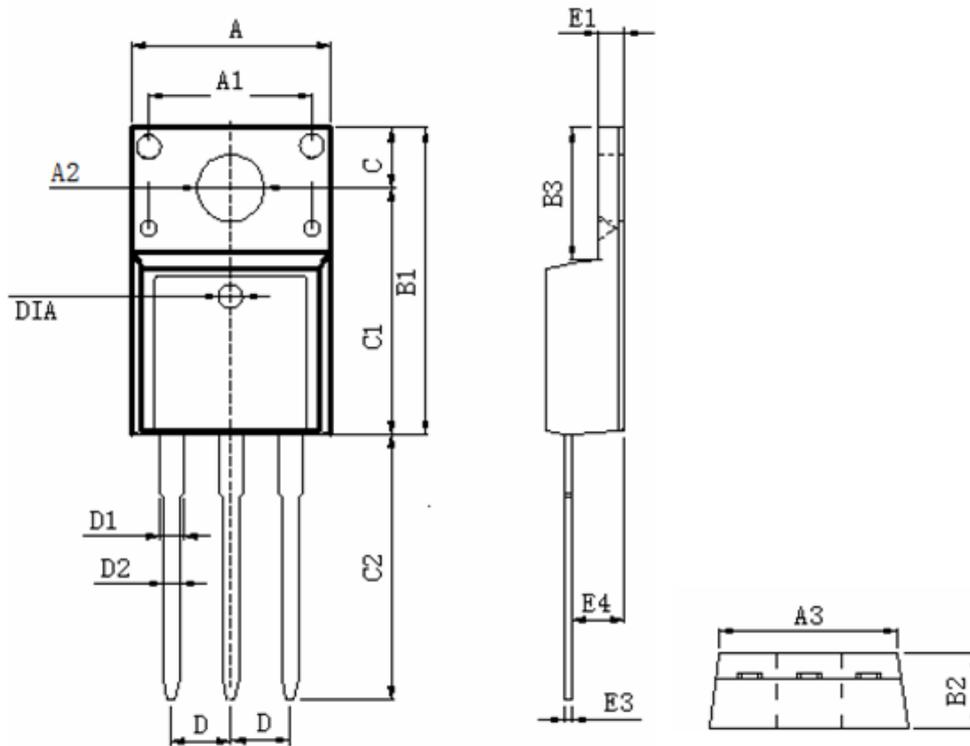


Fig.8 Gate Charge Waveform

TO220F PACKAGE INFORMATION



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------------|----------------------|----------------|
| | MAX | MIN | MAX | MIN |
| A | 10.460 | 9.860 | 0.412 | 0.388 |
| A1 | 7.100 | 6.900 | 0.280 | 0.272 |
| A2 | 3.500 | 3.100 | 0.138 | 0.122 |
| A3 | 9.900 | 9.500 | 0.390 | 0.374 |
| B1 | 16.170 | 15.570 | 0.637 | 0.613 |
| B2 | 4.900 | 4.500 | 0.193 | 0.177 |
| B3 | 6.880 | 6.480 | 0.271 | 0.255 |
| C | 3.500 | 3.100 | 0.138 | 0.122 |
| C1 | 12.870 | 12.270 | 0.507 | 0.483 |
| C2 | 13.380 | 12.580 | 0.527 | 0.495 |
| D | 2.590 | 2.490 | 0.102 | 0.098 |
| D1 | 1.470 | 1.070 | 0.058 | 0.042 |
| D2 | 0.900 | 0.700 | 0.035 | 0.028 |
| E1 | 2.740 | 2.340 | 0.108 | 0.092 |
| E3 | 0.600 | 0.400 | 0.024 | 0.016 |
| E4 | 2.960 | 2.560 | 0.117 | 0.101 |
| DIA | Φ1.5 TYP. | deep0.1 TYP. | Φ0.059 TYP. | deep0.004 TYP. |