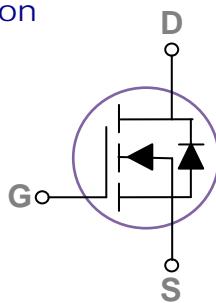
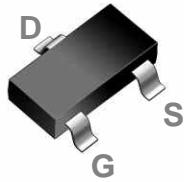


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
30V	32mΩ	5.3A

### Features

- 30V, 5.3 A, RDS(ON) = 32mΩ@VGS = 4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 2.5V Gate Drive Applications

### Applications

- Notebook
- Load Switch
- LED applications

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	5.3	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	3.4	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	21.2	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	°C/W



30V N-Channel MOSFETs

PDN3612S

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

## Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.06	---	$\text{V}/^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

## On Characteristics

$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}$ , $I_D=4\text{A}$	---	27	32	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$ , $I_D=3\text{A}$	---	32	40	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = 250\mu\text{A}$	0.4	0.6	0.9	V
			---	-3	---	
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_S=3\text{A}$	---	7	---	S

## Dynamic and switching Characteristics

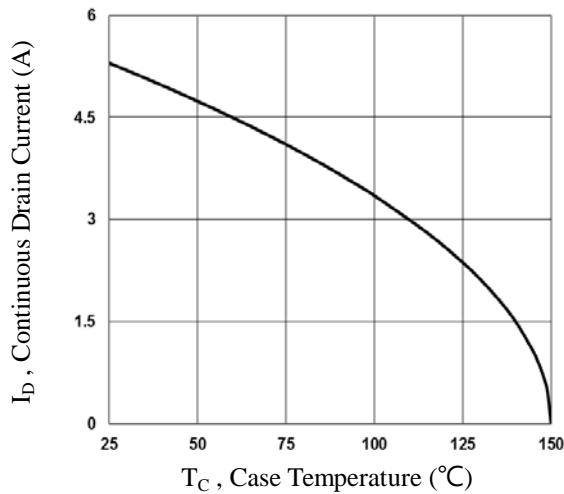
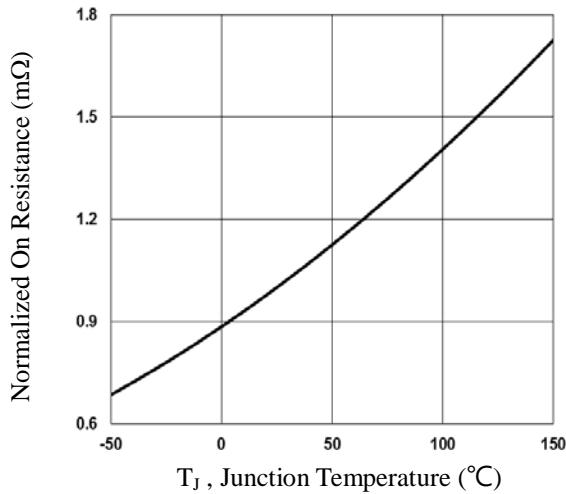
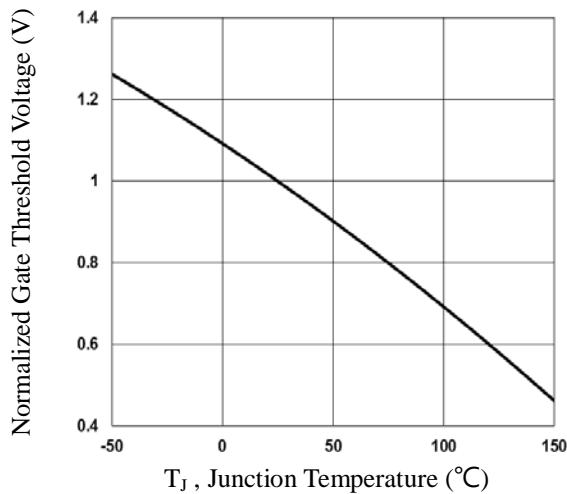
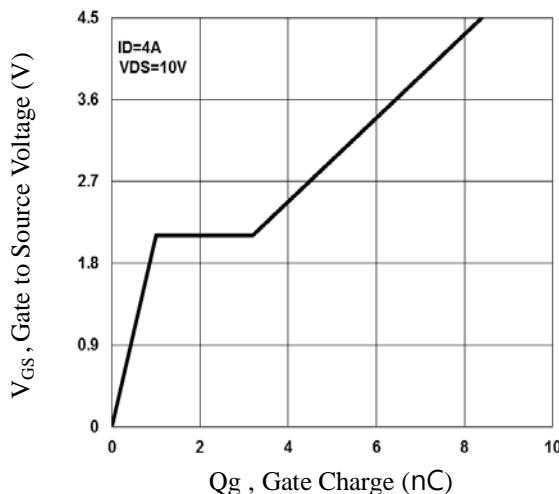
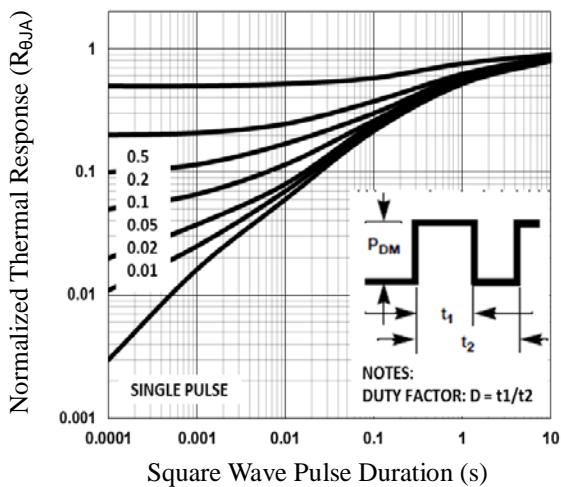
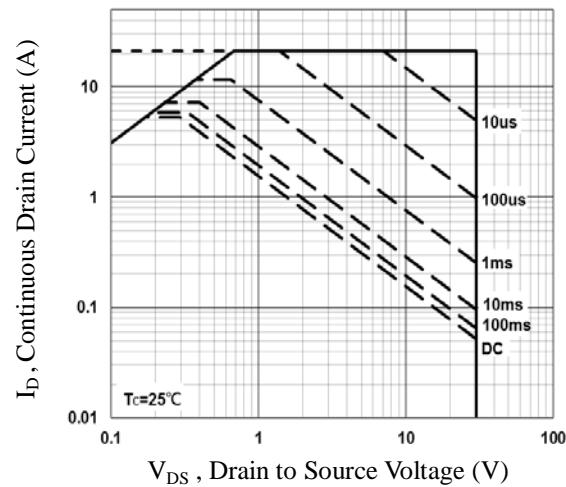
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{\text{DS}}=10\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=4\text{A}$	---	8.4	12	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>		---	1	2	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2,3</sup>		---	2.2	4	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time <sup>2,3</sup>	$V_{\text{DD}}=10\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $R_G=25\Omega$ $I_D=1\text{A}$	---	4.5	9	nS
$T_r$	Rise Time <sup>2,3</sup>		---	13	25	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time <sup>2,3</sup>		---	27	51	
$T_f$	Fall Time <sup>2,3</sup>		---	8.3	16	
$C_{\text{iss}}$	Input Capacitance		---	695	1000	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=10\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	45	65	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	36	50	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	1.5	3	$\Omega$

## Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	5.3	A
			---	---	21.2	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V

## Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1** Continuous Drain Current vs.  $T_C$ 

**Fig.2** Normalized RDSON vs.  $T_J$ 

**Fig.3** Normalized  $V_{th}$  vs.  $T_J$ 

**Fig.4** Gate Charge Waveform

**Fig.5** Normalized Transient Impedance

**Fig.6** Maximum Safe Operation Area

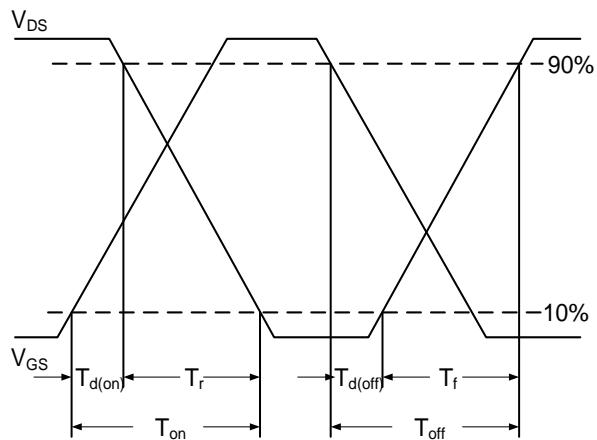


Fig.7 Switching Time Waveform

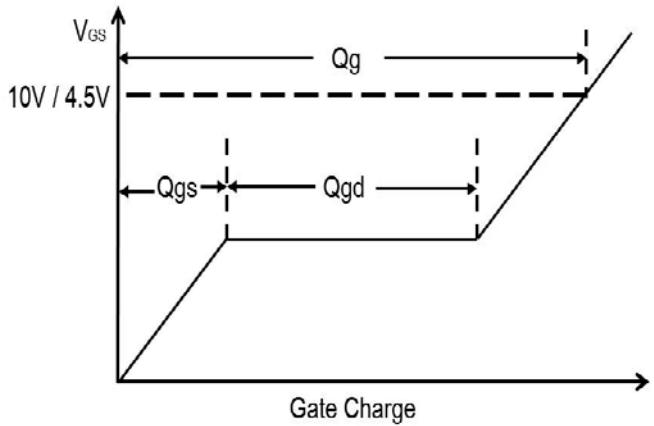
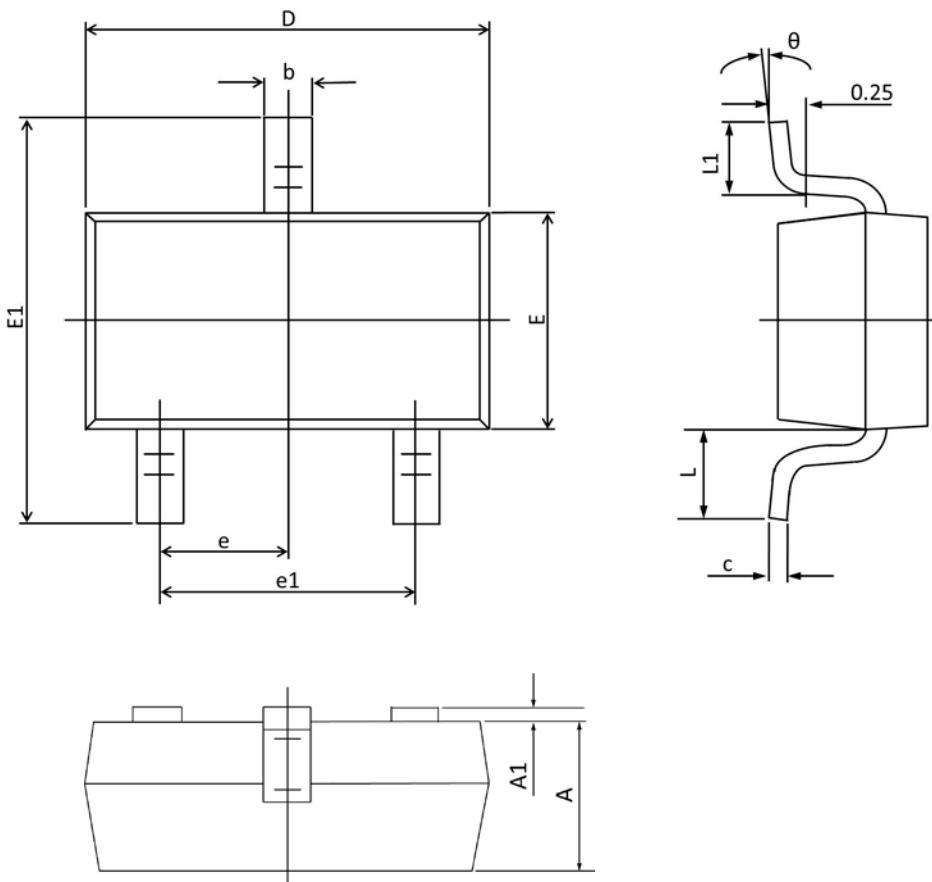


Fig.8 Gate Charge Waveform

## SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.000	0.035	0.039
A1	0.000	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.090	0.110	0.003	0.004
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
theta	1°	7°	1°	7°