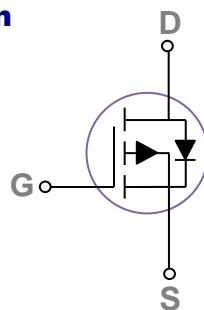
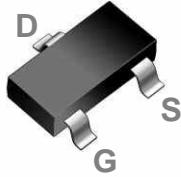


General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-3 Pin Configuration



BVDSS	RDSON	ID
-30V	32mΩ	-4.8A

Features

- -30V, -4.8A, $RDS(ON) = 32m\Omega @ VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -2.5V Gate Drive Applications
- RoHS compliant & Halogen Free

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Drain Current – Continuous ($T_A=25^\circ C$)	-4.8	A
	Drain Current – Continuous ($T_A=70^\circ C$)	-3.8	A
I_{DM}	Drain Current – Pulsed ¹	-19.2	A
P_D	Power Dissipation ($T_A=25^\circ C$)	1	W
	Power Dissipation – Derate above $25^\circ C$	0.008	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	125	$^\circ C/W$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	---	-0.016	---	$\text{V}/^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-2\text{A}$	---	26	32	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-1\text{A}$	---	30	38	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$, $I_{\text{D}}=-0.5\text{A}$	---	37	48	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-0.5	-0.7	-1.0	V
$\Delta V_{\text{GS}(\text{th})}$			---	-2.67	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_{\text{D}}=-1\text{A}$	---	5.5	---	S

Dynamic and switching Characteristics

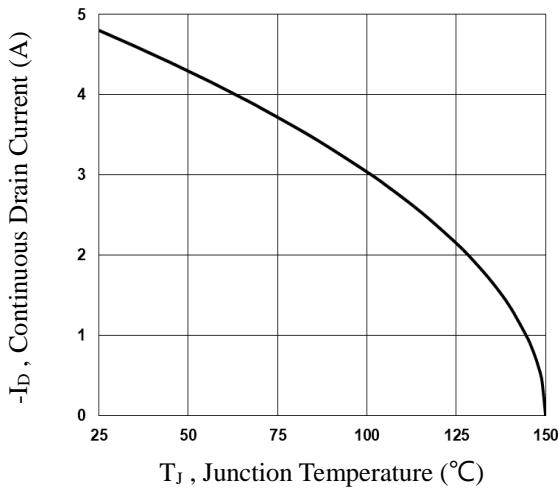
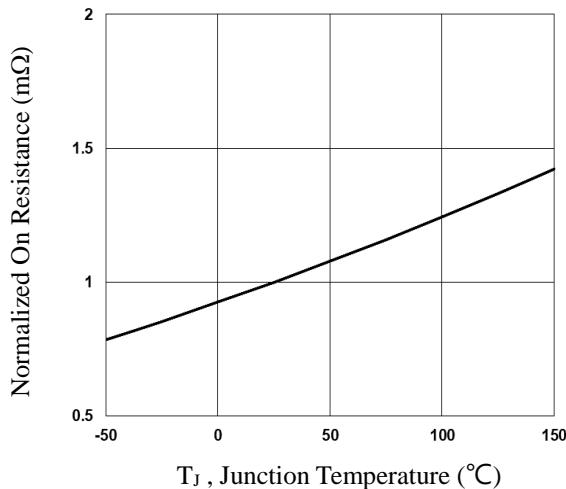
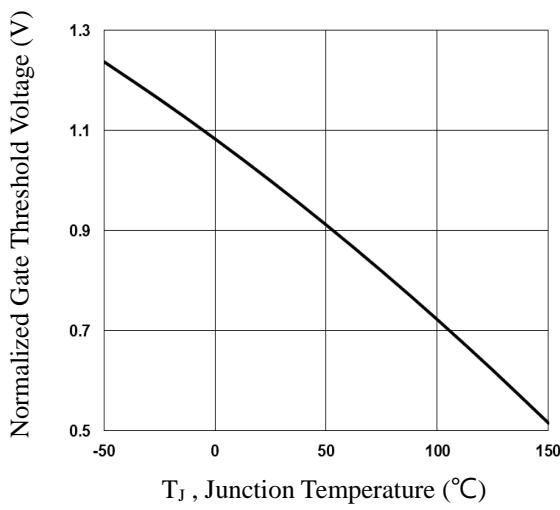
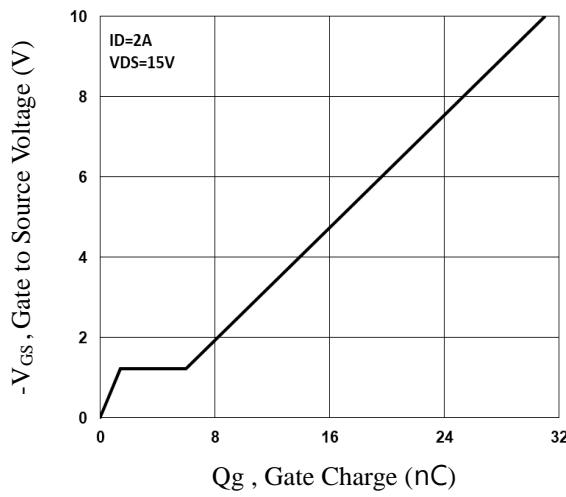
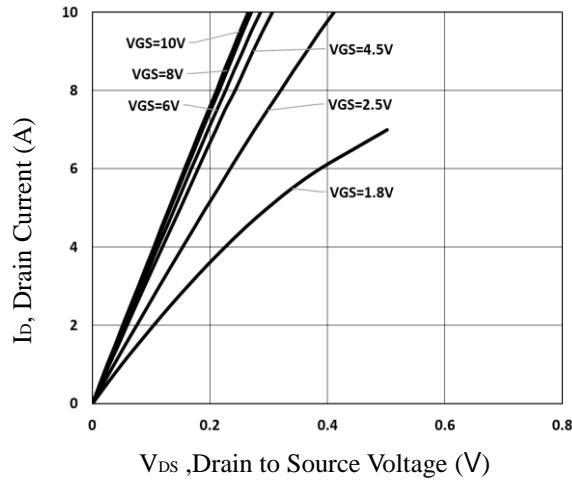
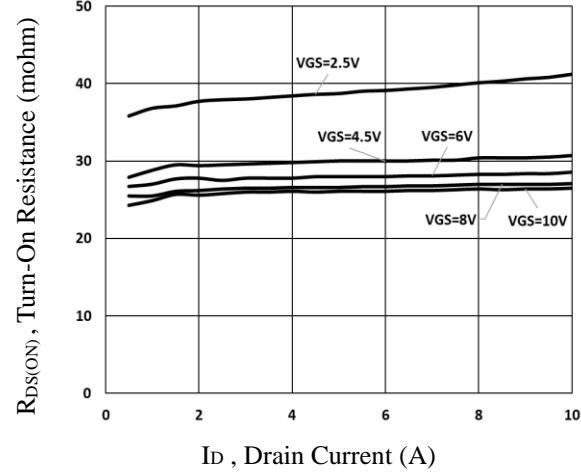
Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-2\text{A}$	---	31	62	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	1.4	3	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	4.6	9	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=-15\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_{\text{G}}=6\Omega$ $I_{\text{D}}=-1\text{A}$	---	7.9	16	ns
T_r	Rise Time ^{2,3}		---	13.2	26	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time ^{2,3}		---	38.6	76	
T_f	Fall Time ^{2,3}		---	12.5	25	
C_{iss}	Input Capacitance		---	1540	3000	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	142	280	
C_{rss}	Reverse Transfer Capacitance		---	118	240	

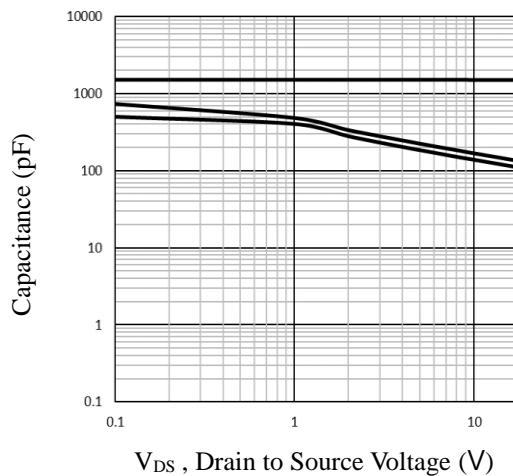
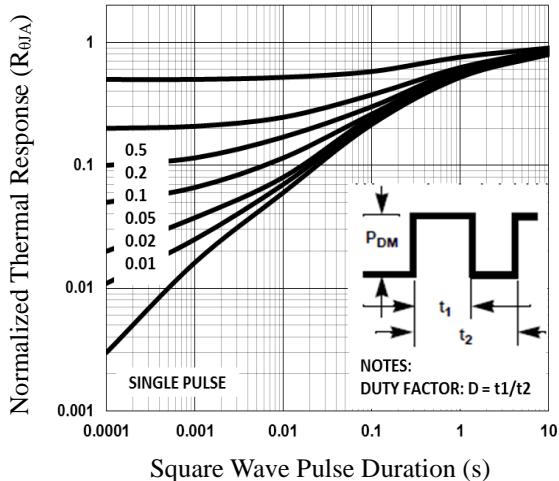
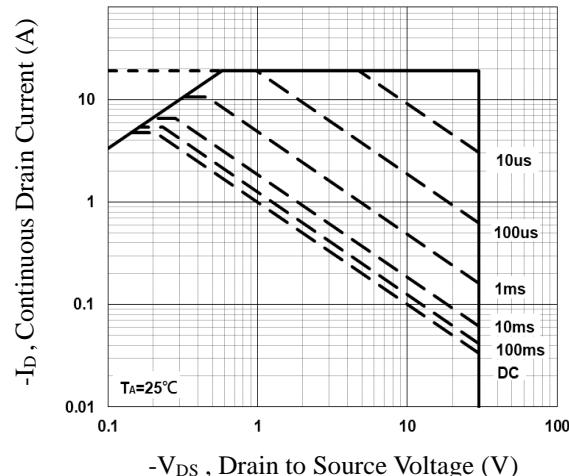
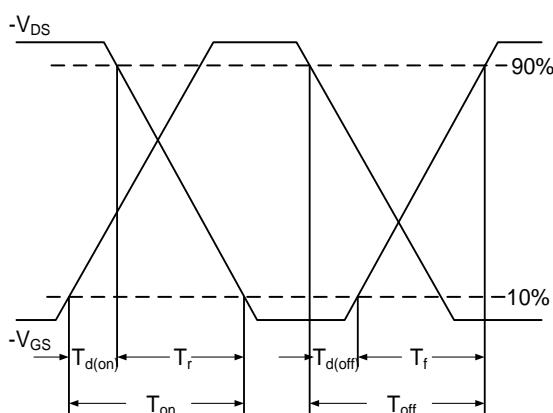
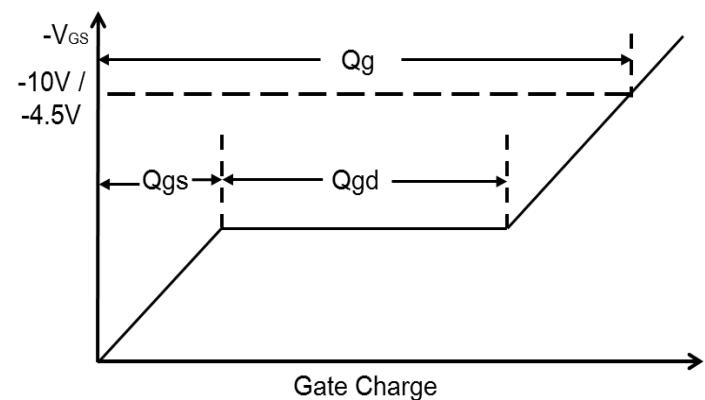
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-4.8	A
I_{SM}	Pulsed Source Current		---	---	-9.6	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

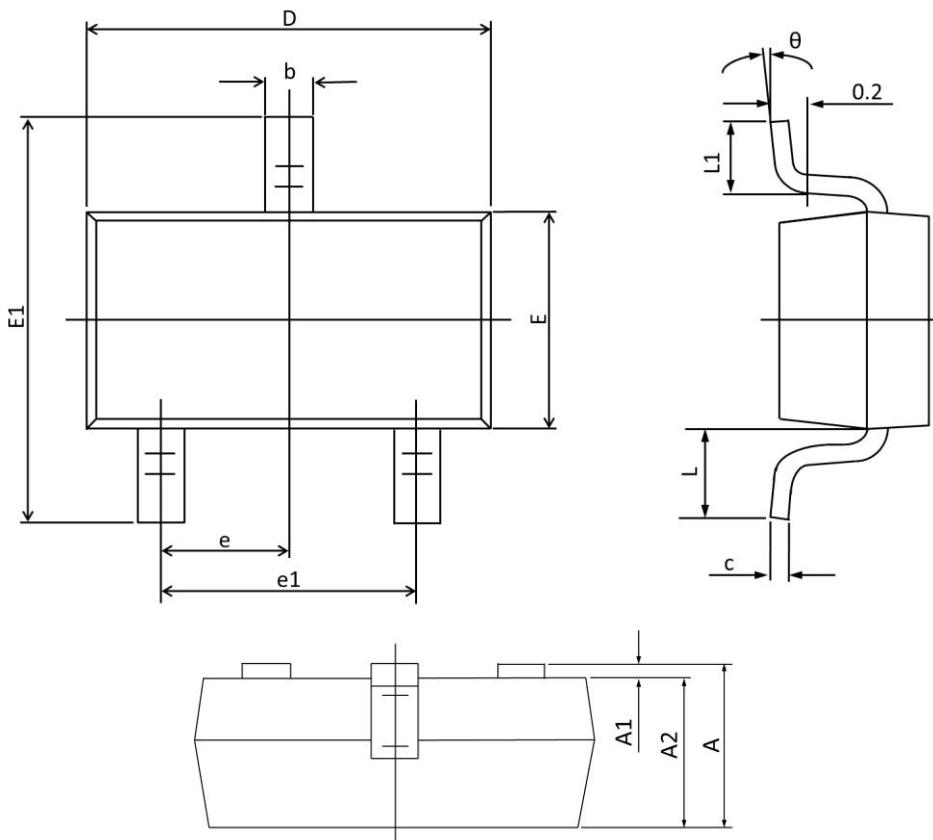
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_J

Fig.2 Normalized RDSON vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Typical Output Characteristics

Fig.6 Turn-On Resistance vs. ID


Fig.7 Capacitance Characteristics

Fig.9 Normalized Transient Impedance

Fig.9 Maximum Safe Operation Area

Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform

SOT23-3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.400	0.012	0.016
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.700 REF.		0.028 REF.	
L1	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°