

### General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
-25V	65mΩ	-4.1A

### Features

- -25V, -4.1A,  $RDS(ON) = 65m\Omega @ VGS = -4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.8V Gate Drive Applications

### Applications

- Notebook
- Load Switch
- Hand-Held Instruments

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-25	V
$V_{GS}$	Gate-Source Voltage	$\pm 10$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	-4.1	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	-2.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-16.4	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	1.56	W
	Power Dissipation – Derate above $25^\circ C$	0.012	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	$^\circ C/W$



25V P-Channel MOSFETs

PDN9313S

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

## Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-25	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1\text{mA}$	---	-0.02	---	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{\text{DS}}=-20\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	-10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 10\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

## On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-3\text{A}$	---	52	65	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$ , $I_D=-2\text{A}$	---	73	85	
		$V_{\text{GS}}=-1.8\text{V}$ , $I_D=-1.5\text{A}$	---	105	130	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = -250\mu\text{A}$	-0.4	-0.6	-0.8	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	2	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$ , $I_S=-3\text{A}$	---	5.5	---	S

## Dynamic and switching Characteristics

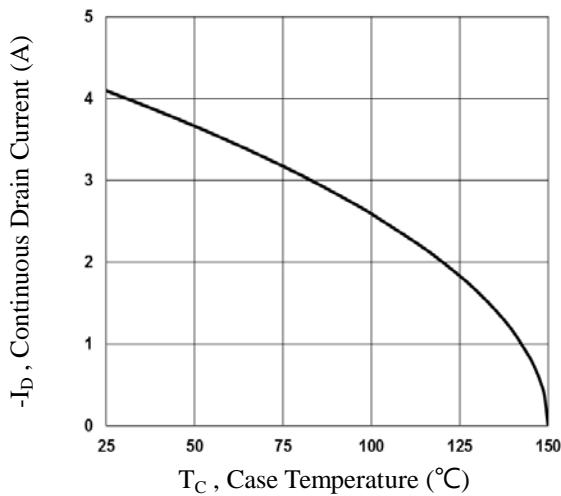
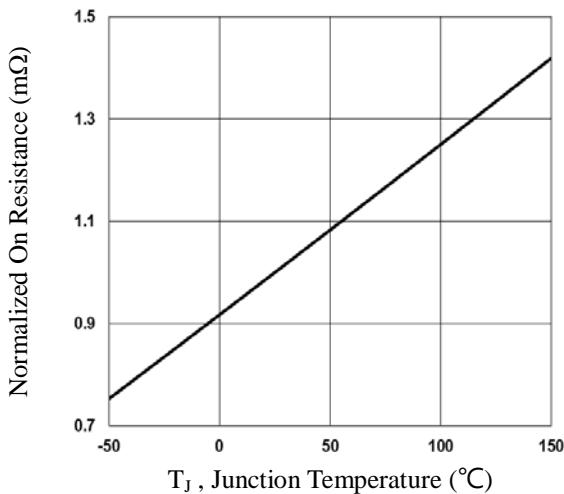
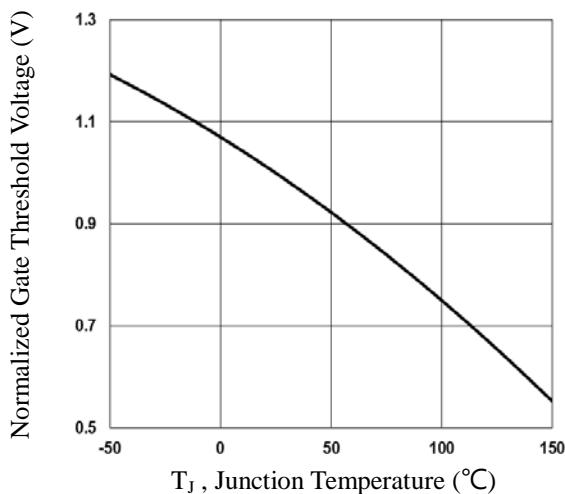
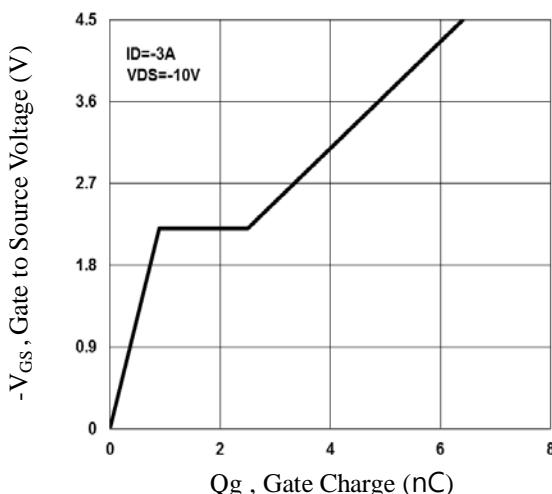
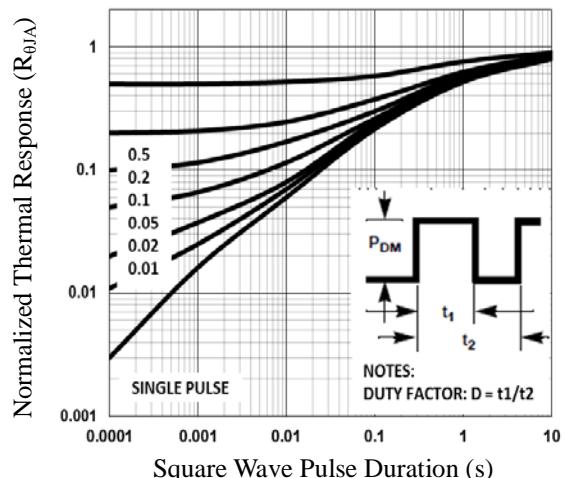
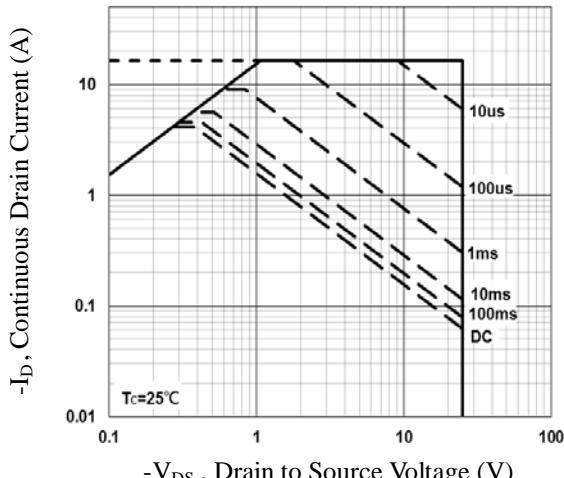
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_D=-3\text{A}$	---	6.4	9	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>		---	0.9	1	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2,3</sup>		---	1.6	3	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2,3</sup>	$V_{\text{DD}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $R_G=25\Omega$ $I_D=-1\text{A}$	---	5	9	nS
$T_r$	Rise Time <sup>2,3</sup>		---	17.4	33	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2,3</sup>		---	40.7	80	
$T_f$	Fall Time <sup>2,3</sup>		---	11.4	23	
$C_{\text{iss}}$	Input Capacitance		---	515	745	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	55	80	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	20	30	

## Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-4.1	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-16.4	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
- Essentially independent of operating temperature.


**Fig.1** Continuous Drain Current vs.  $T_C$ 

**Fig.2** Normalized RDSON vs.  $T_J$ 

**Fig.3** Normalized  $V_{th}$  vs.  $T_J$ 

**Fig.4** Gate Charge Waveform

**Fig.5** Normalized Transient Impedance

**Fig.6** Maximum Safe Operation Area

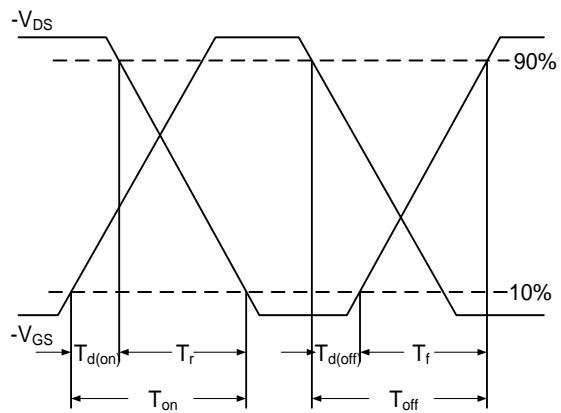


Fig.7 Switching Time Waveform

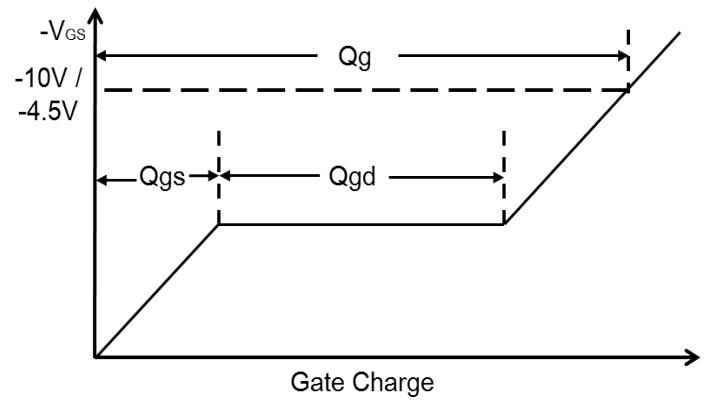
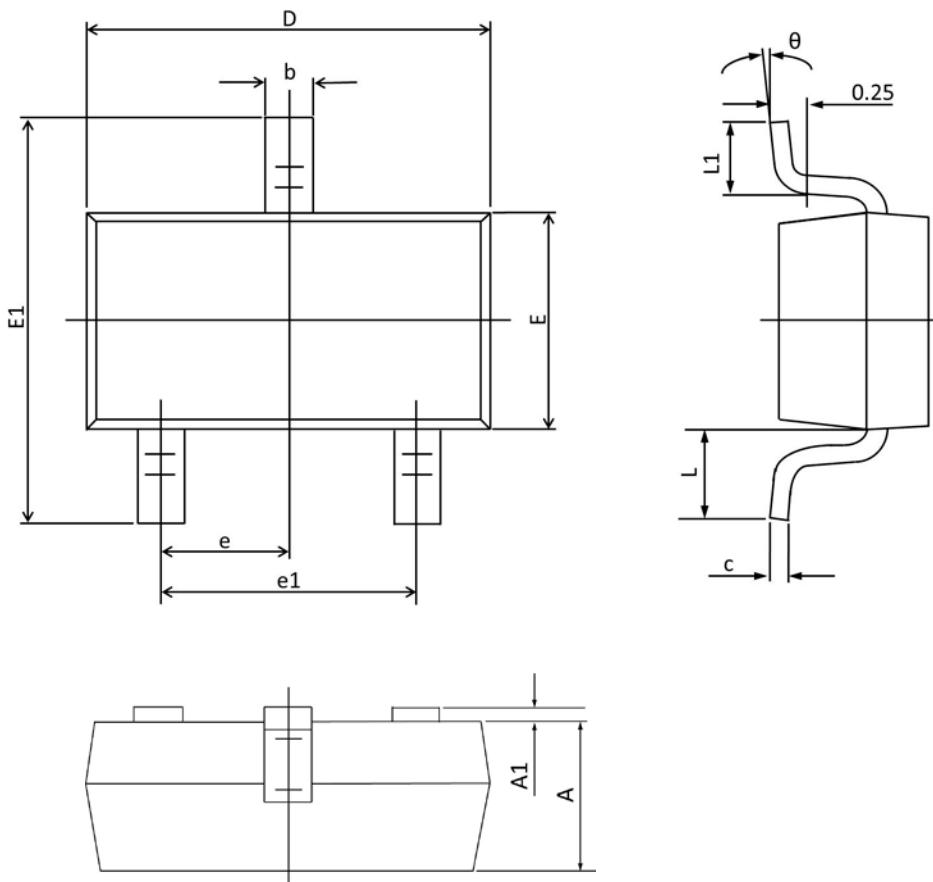


Fig.8 Gate Charge Waveform

## SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.000	0.035	0.039
A1	0.000	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.090	0.110	0.003	0.004
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
theta	1°	7°	1°	7°