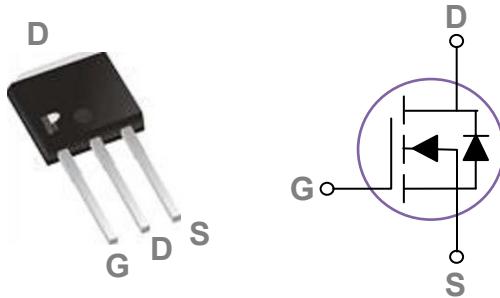


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO251 Pin Configuration



BVDSS	RDSON	ID
40V	8.5mΩ	50A

### Features

- 40V, 50A,  $RDS(ON)=8.5m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

### Applications

- Notebook
- Load Switch
- LED applications
- Hand-Held Device

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	50	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	31.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	200	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	54	W
	Power Dissipation – Derate above $25^\circ C$	0.43	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.31	$^\circ C/W$

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	40	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.03	---	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=40\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=32\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=85^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=8\text{A}$	---	6.5	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=4\text{A}$	---	9	12	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	1.2	1.8	2.5	V
			---	-5	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=10\text{A}$	---	13	---	S

**Dynamic and switching Characteristics**

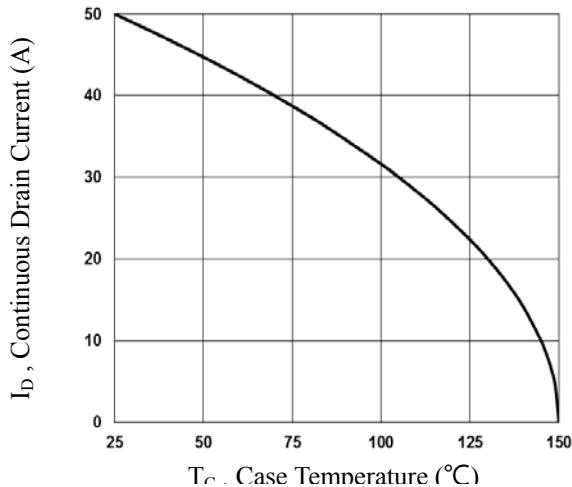
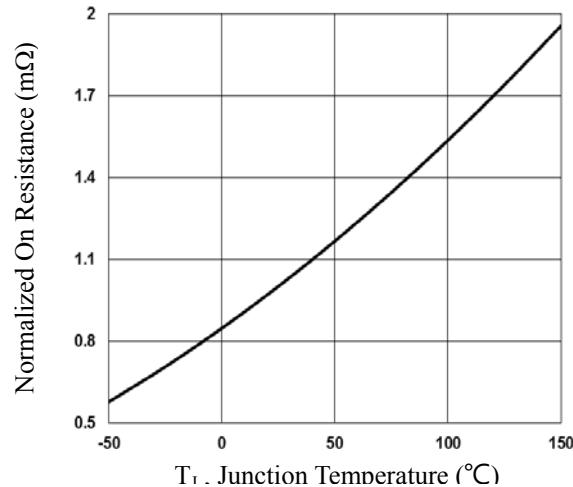
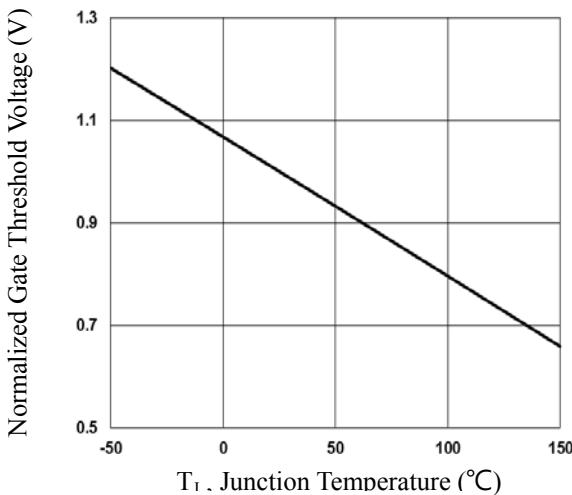
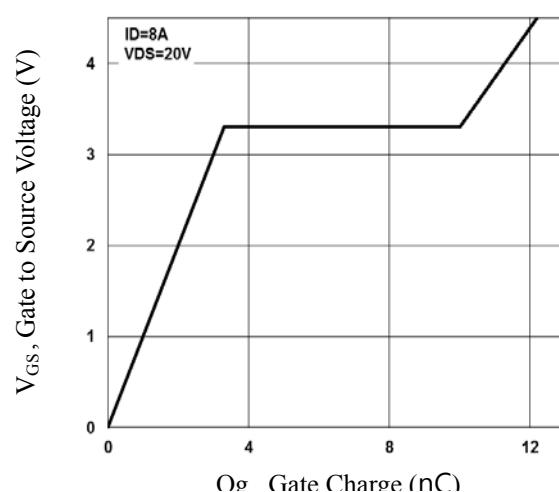
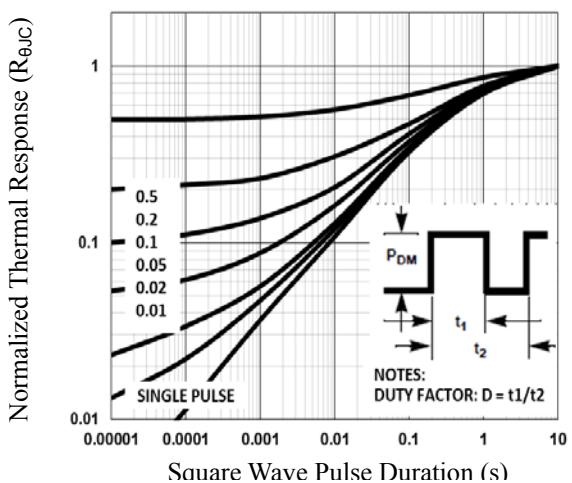
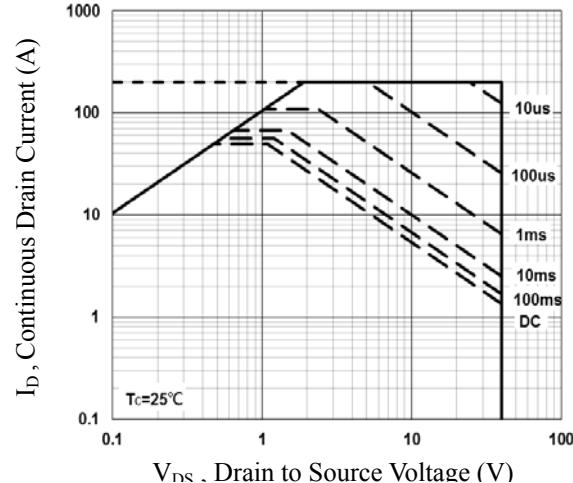
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=8\text{A}$	---	12.2	24	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>		---	3.3	7	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2,3</sup>		---	6.7	13	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2,3</sup>	$V_{\text{DD}}=15\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3.3\Omega$ $I_D=1\text{A}$	---	13.2	25	ns
$T_r$	Rise Time <sup>2,3</sup>		---	2.2	5	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2,3</sup>		---	72	130	
$T_f$	Fall Time <sup>2,3</sup>		---	4.5	10	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	1220	2200	pF
$C_{\text{oss}}$	Output Capacitance		---	130	250	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	55	110	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	2.2	---	$\Omega$

**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	50	A
			---	---	100	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{\text{rr}}$	Reverse Recovery Time <sup>3</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	---	17	---	ns
		$T_J=25^\circ\text{C}$	---	2.8	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1** Continuous Drain Current vs. T<sub>C</sub>

**Fig.2** Normalized RDS(on) vs. T<sub>J</sub>

**Fig.3** Normalized V<sub>th</sub> vs. T<sub>J</sub>

**Fig.4** Gate Charge Waveform

**Fig.5** Normalized Transient Impedance

**Fig.6** Maximum Safe Operation Area

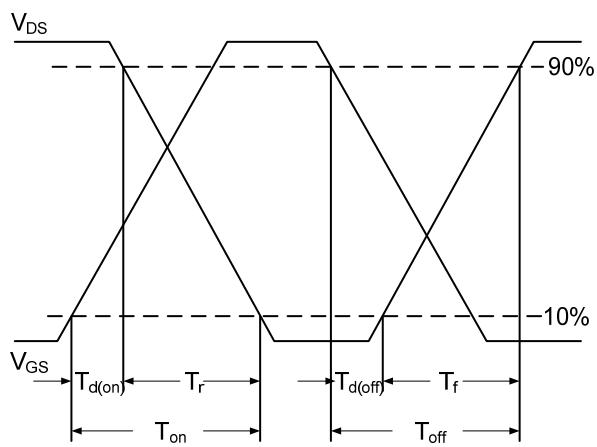


Fig.7 Switching Time Waveform

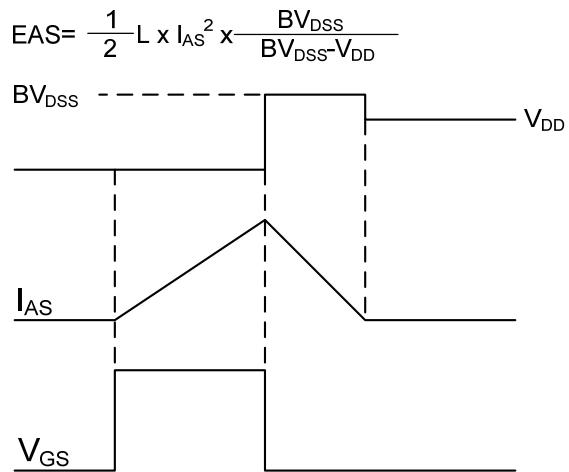
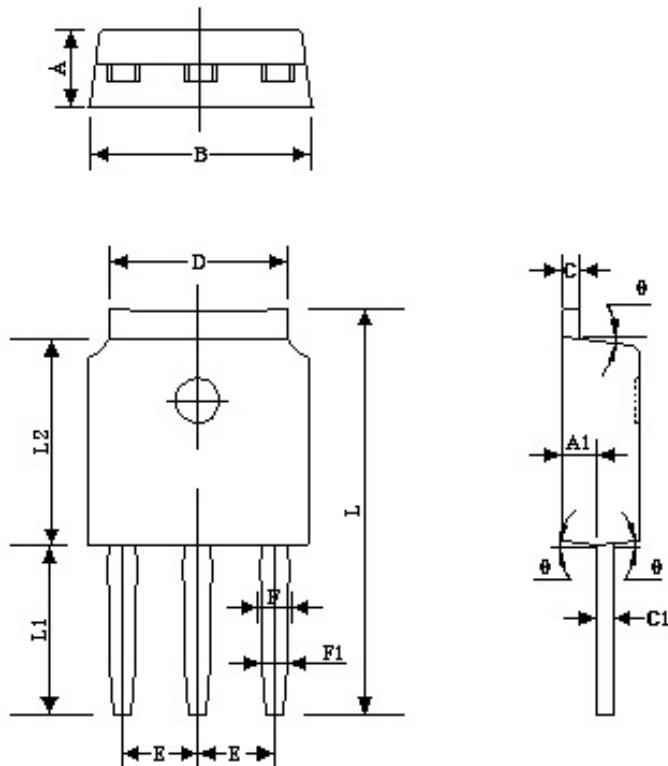


Fig.8 EAS Waveform

## TO251 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
B	6.700	6.500	0.264	0.256
C	0.580	0.460	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.460	5.100	0.215	0.201
E	2.386	2.186	0.094	0.086
F	0.940	0.740	0.037	0.029
F1	0.860	0.660	0.034	0.026
L	12.300	11.700	0.484	0.461
L1	5.200	4.800	0.205	0.189
L2	6.200	6.000	0.244	0.236
θ	9°	3°	9°	3°