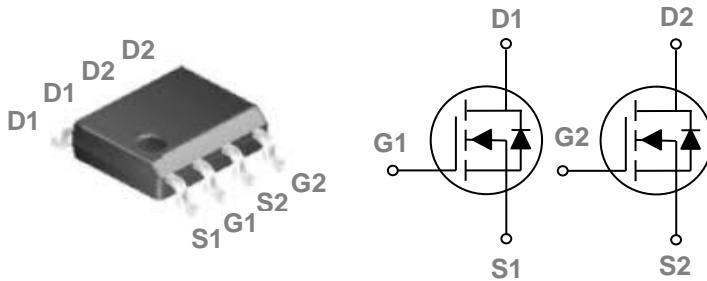


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOP8 Dual Pin Configuration



BVDSS	RDS(ON)	ID
40V	18mΩ	8A

### Features

- 40V, 8A, RDS(ON) = 18mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>Ds</sub>	Drain-Source Voltage	40	V
V <sub>Gs</sub>	Gate-Source Voltage	$\pm 20$	V
I <sub>D</sub>	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	8	A
	Drain Current – Continuous ( $T_A=70^\circ\text{C}$ )	6.4	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	32	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	4.9	mJ
I <sub>AS</sub>	Single Pulse Avalanche Current <sup>2</sup>	9.9	A
P <sub>D</sub>	Power Dissipation ( $T_A=25^\circ\text{C}$ )	2	W
	Power Dissipation – Derate above 25°C	0.016	W/°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	62.5	°C/W

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	---	16	18	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	---	20	25	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.6	2.5	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =1A	---	5	---	S

**Dynamic and switching Characteristics**

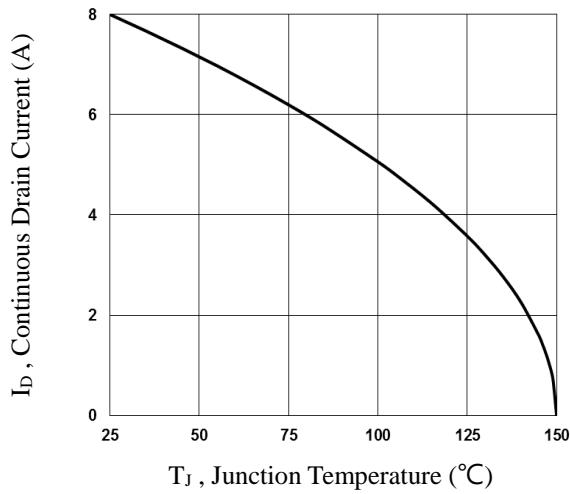
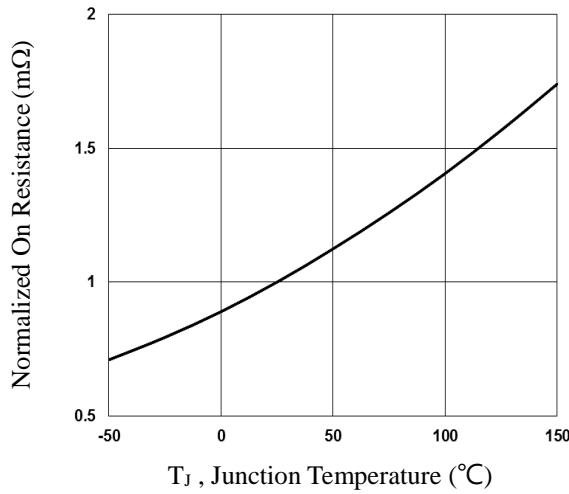
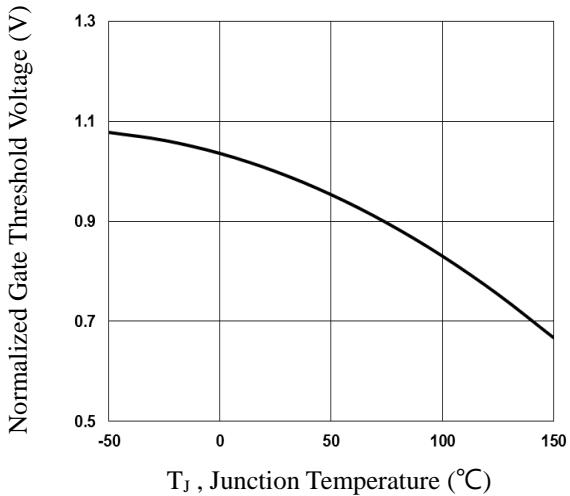
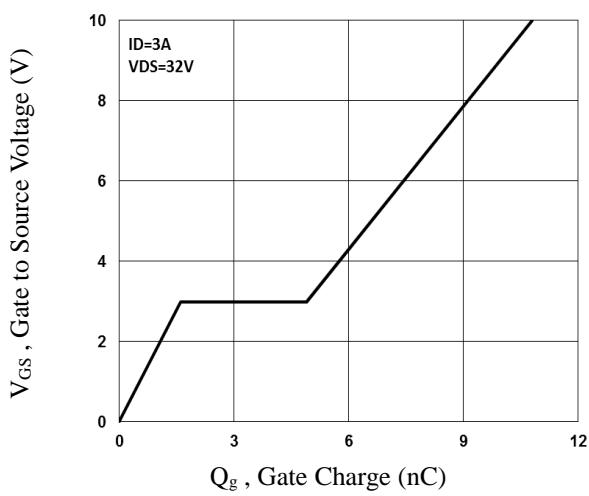
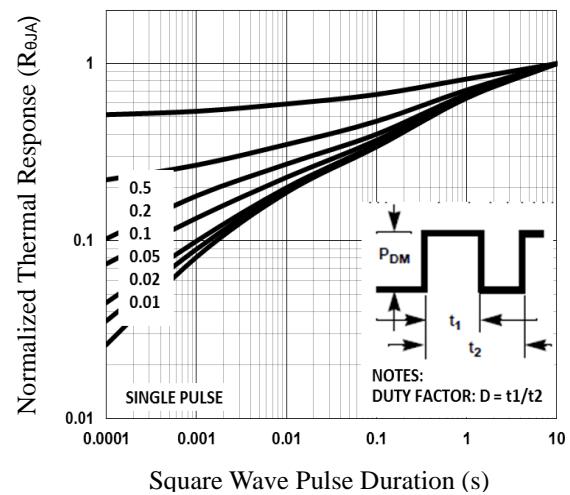
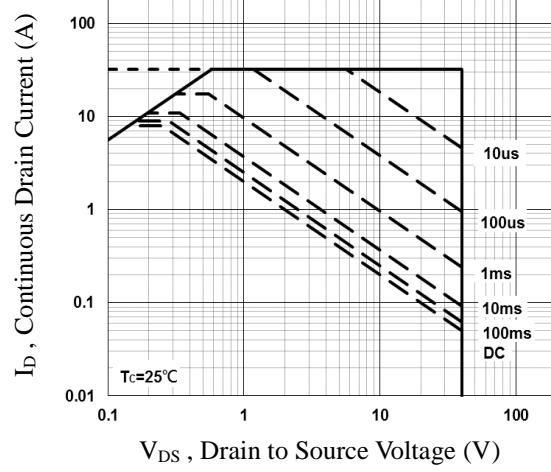
Q <sub>g</sub>	Total Gate Charge <sup>2, 3</sup>	V <sub>DS</sub> =32V, V <sub>GS</sub> =10V, I <sub>D</sub> =3A	---	10.8	21.6	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2, 3</sup>		---	1.6	3.2	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2, 3</sup>		---	3.3	6.6	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2, 3</sup>	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω I <sub>D</sub> =1A	---	3.8	7.6	ns
T <sub>r</sub>	Rise Time <sup>2, 3</sup>		---	10.5	21	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2, 3</sup>		---	22.2	45	
T <sub>f</sub>	Fall Time <sup>2, 3</sup>		---	6.6	13.2	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1MHz	---	724	1450	pF
C <sub>oss</sub>	Output Capacitance		---	70	140	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	109	220	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	2.6	---	Ω

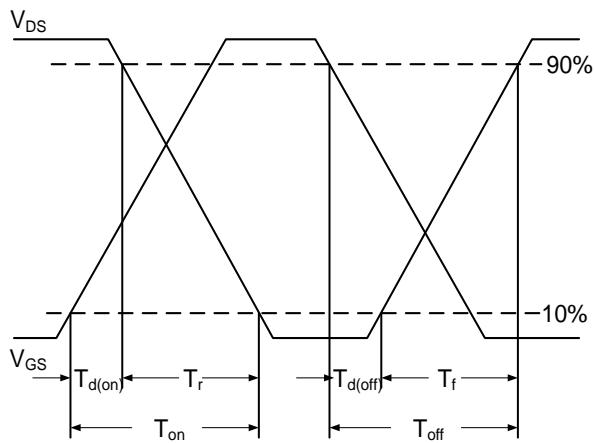
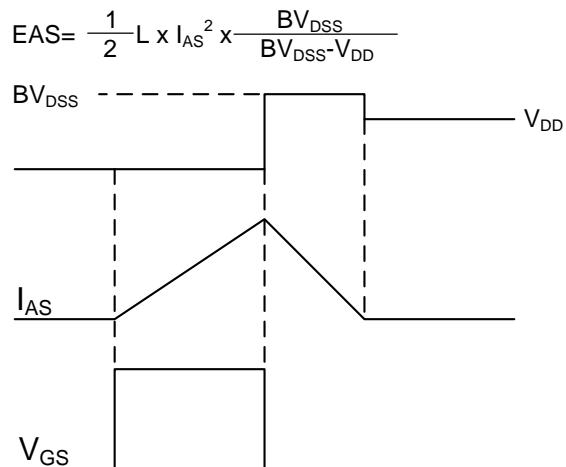
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>s</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	8	A
I <sub>SM</sub>	Pulsed Source Current		---	---	16	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>s</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

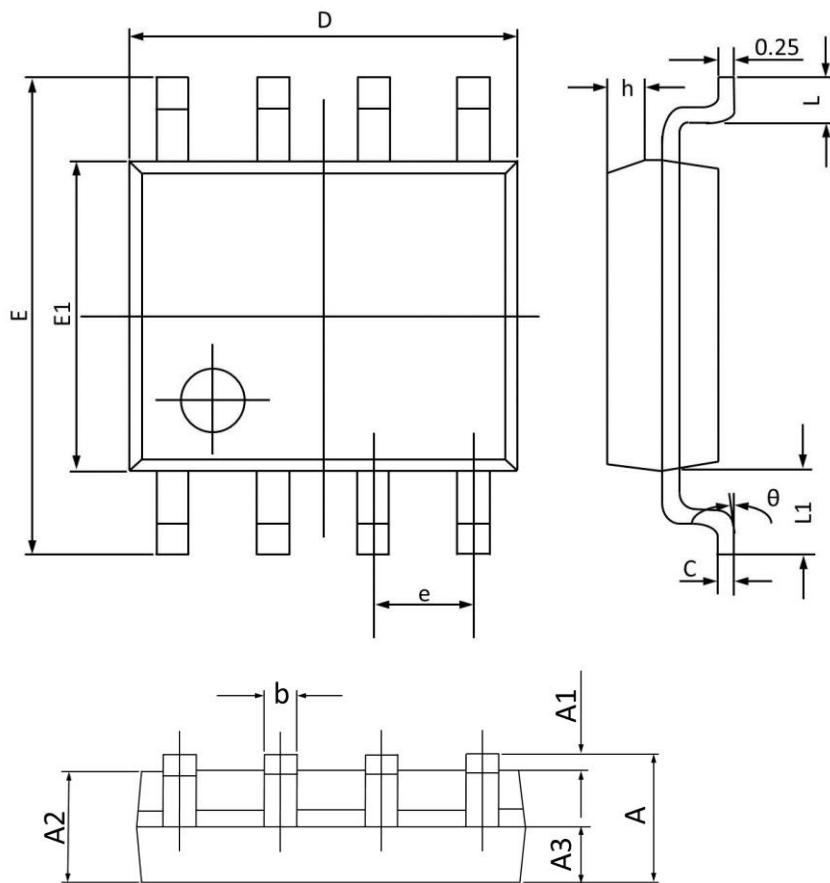
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=9.9A, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_J$** 

**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## SOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050(BSC)		0.041(BSC)	
θ	0°	8°	0°	8°