

PDTA144EU-Q

50 V, 100 mA PNP resistor-equipped transistor; R1 = 47 k Ω , R2 = 47 k Ω

21 March 2025

Product data sheet

1. General description

PNP Resistor-Equipped Transistor (RET) in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTC144EU-Q

2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- · Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- · Digital applications in automotive and industrial segments
- Cost-saving alternative for BC847/857 series in digital applications
- · Control of IC inputs
- · Switching loads

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-50	V
Io	output current		-	-	-100	mA
R1	bias resistor 1 (input)	T _{amb} = 25 °C	33	47	61	kΩ
R2/R1	bias resistor ratio		0.8	1	1.2	



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	□ 3	
2	G	GND (emitter)		R ₁ O
3	0	output (collector)	SC-70 (SOT323)	R2 GND sym003

6. Ordering information

Table 3. Ordering information

Type number Package				
	Name	Description	Version	
PDTA144EU-Q	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323	

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PDTA144EU-Q	807

[1] % = placeholder for manufacturing site code

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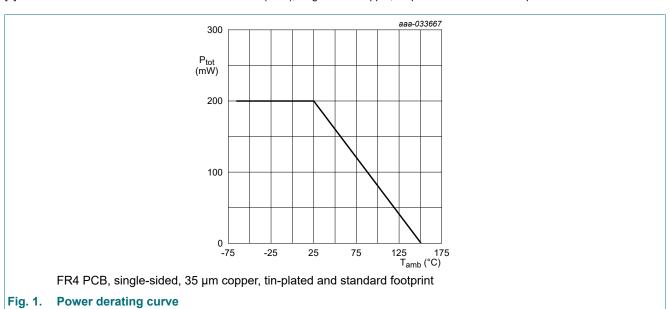
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-50	V
V_{CEO}	collector-emitter voltage	open base		-	-50	V
V_{EBO}	emitter-base voltage	open collector		-	-10	V
VI	input voltage	positive		-40	10	V
Io	output current			-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

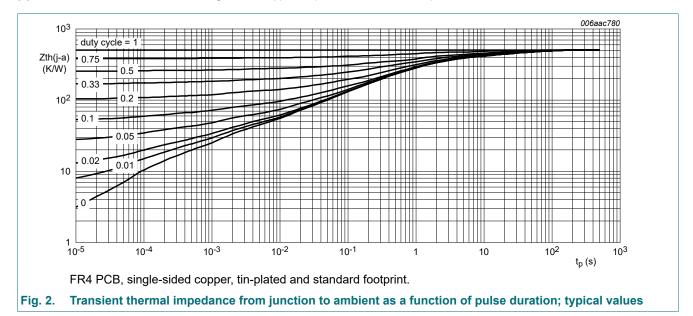


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
11(J-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

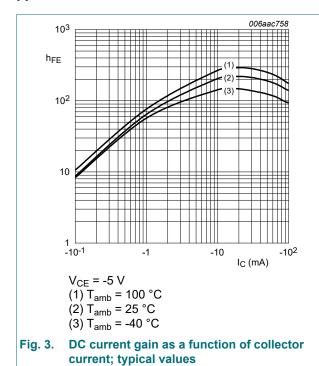


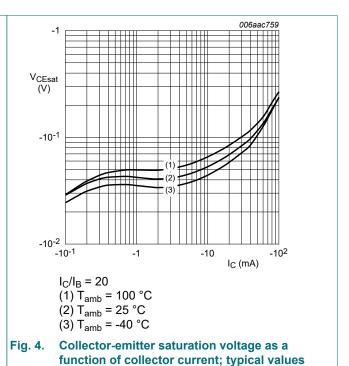
10. Characteristics

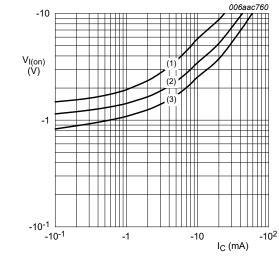
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C		-	-	-100	nA
I _{CEO} collector-emitter cut-of	collector-emitter cut-off	V _{CE} = -30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	-100	nA
	current	V _{CE} = -30 V; I _B = 0 A; T _j = 150 °C		-	-	-5	μΑ
I _{EBO}	emitter-base cut-off current	_{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	-90	μΑ
h _{FE}	DC current gain	V_{CE} = -5 V; I_{C} = -5 mA; T_{amb} = 25 °C		80	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = -10 mA; I_B = -0.5 mA; T_{amb} = 25 °C		-	-	-150	mV
V _{I(off)}	off-state input voltage	V _{CE} = -5 V; I _C = -100 μA; T _{amb} = 25 °C		-	-1.2	-0.8	V
V _{I(on)}	on-state input voltage	V_{CE} = -0.3 V; I_{C} = -2 mA; T_{amb} = 25 °C		-3	-1.6	-	V
R1	bias resistor 1 (input)	T _{amb} = 25 °C		33	47	61	kΩ
R2/R1	bias resistor ratio			0.8	1	1.2	
C _c	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	-	3	pF
f _T	transition frequency	V_{CE} = -5 V; I_{C} = -10 mA; f = 100 MHz; T_{amb} = 25 °C	[1]	-	180	-	MHz

[1] Characteristics of built-in transistor.

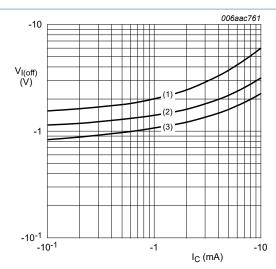






V_{CE} = -0.3 V (1) T_{amb} = -40 °C (2) T_{amb} = 25 °C (3) T_{amb} = 100 °C





V_{CE} = -5 V (1) T_{amb} = -40 °C (2) T_{amb} = 25 °C

(3) $T_{amb} = 100 \, ^{\circ}C$

Off-state input voltage as a function of collector current; typical values

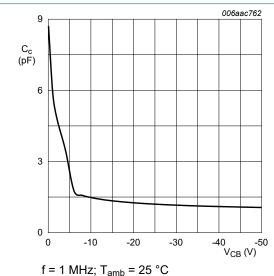
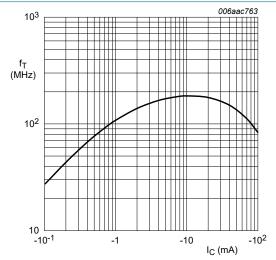


Fig. 7. Collector capacitance as a function of collector- Fig. 8. base voltage; typical values of built-in transistor



 V_{CE} = -5 V; T_{amb} = 25 °C

Transition frequency as a function of collector current; typical values of built-in transistor

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

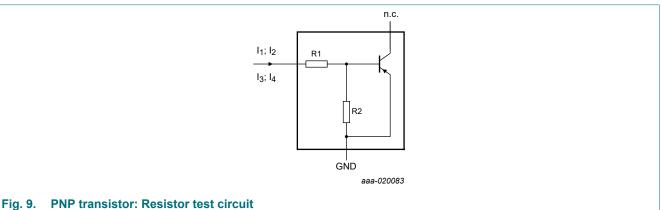
Resistor calculation

Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$



Resistor test conditions

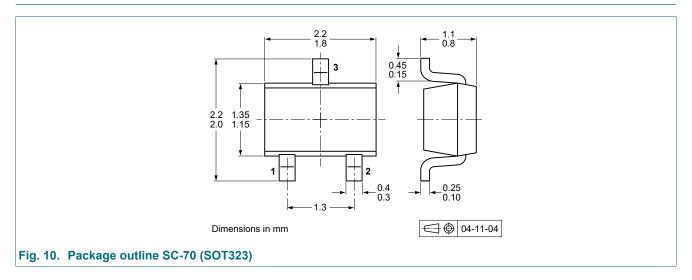
Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I ₁	l ₂	l ₃	14
PDTA144EU-Q	47	47	-55 µA	-105 μA	55 µA	105 μΑ

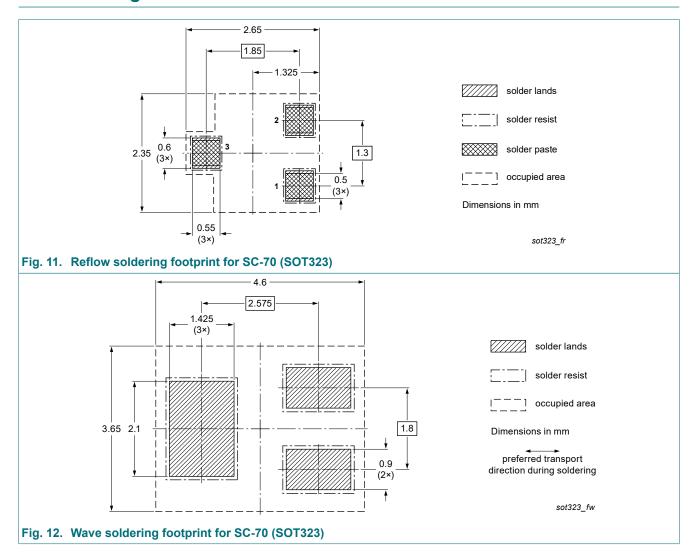
Nexperia PDTA144EU-Q

50 V, 100 mA PNP resistor-equipped transistor; R1 = 47 k Ω , R2 = 47 k Ω

12. Package outline



13. Soldering



14. Revision history

Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTA144EU-Q v.1	20250321	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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