

## P-Channel Enhancement Mode Power MOSFET

### Description

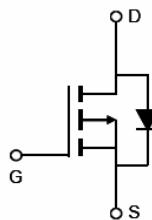
The PE01P30 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### General Features

- $V_{DS} = -100V, I_D = -30A$
- $R_{DS(ON)} < 58m\Omega @ V_{GS} = -10V$  (Typ:  $50m\Omega$ )
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density cell design for ultra low On-Resistance

### Application

- Portable equipment and battery powered systems



Schematic diagram



TO-220-3L top view

### Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	-30	A
Drain Current-Continuous( $T_C=100^\circ C$ )	$I_D (100^\circ C)$	-21	A
Pulsed Drain Current	$I_{DM}$	-140	A
Maximum Power Dissipation	$P_D$	120	W
Derating factor		0.8	W/ $^\circ C$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

**Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	1.25	$^{\circ}\text{C}/\text{W}$
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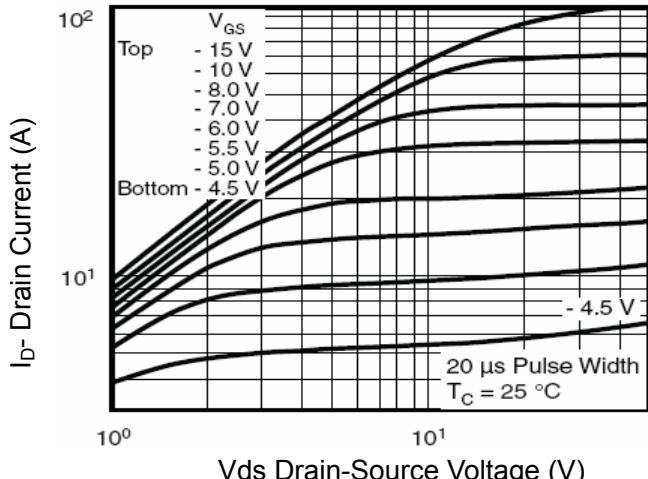
**Electrical Characteristics ( $T_c=25^{\circ}\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-100	-	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-100\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.5	-1.9	-3	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-15\text{A}$	-	50	58	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-50\text{V}, I_{\text{D}}=-10\text{A}$	5	-	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-25\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	2700	-	PF
Output Capacitance	$C_{\text{oss}}$		-	790	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	450	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=-50\text{V}, I_{\text{D}}=-15\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{GEN}}=9.1\Omega$	-	17	-	nS
Turn-on Rise Time	$t_r$		-	80	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	45	-	nS
Turn-Off Fall Time	$t_f$		-	65	-	nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-50\text{V}, I_{\text{D}}=-15\text{A}, V_{\text{GS}}=-10\text{V}$	-	90	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	15	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	35	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-10\text{A}$	-	-	-1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$I_{\text{S}}$	-	-	-	-30	A
Reverse Recovery Time	$t_{\text{rr}}$	$T_J = 25^{\circ}\text{C}, IF = -15\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$ <sup>(Note 3)</sup>	-	90	-	nS
Reverse Recovery Charge	$Q_{\text{rr}}$		-	70	-	nC
Forward Turn-On Time	$t_{\text{on}}$	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

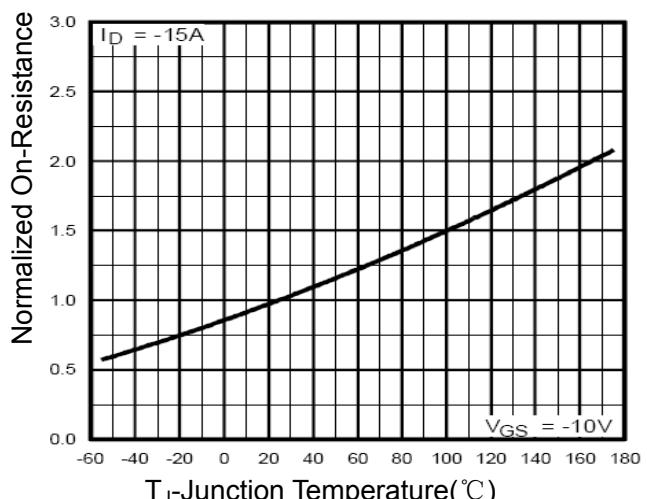
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition:  $T_j=25^{\circ}\text{C}, V_{\text{DD}}=-50\text{V}, V_G=-10\text{V}, L=0.5\text{mH}, R_g=25\Omega$

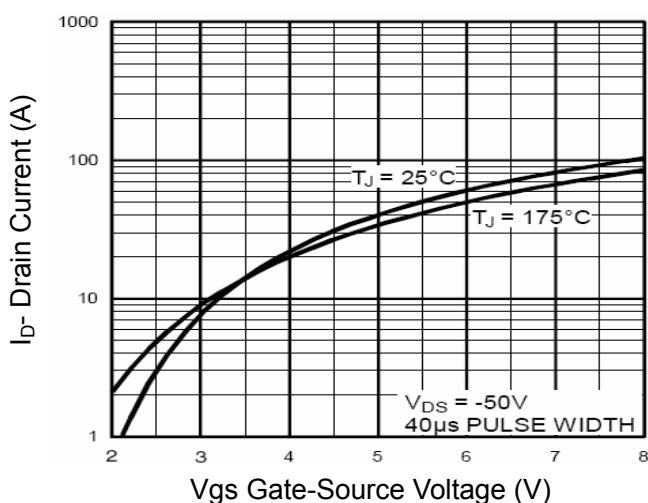
### Typical Electrical and Thermal Characteristics (Curves)



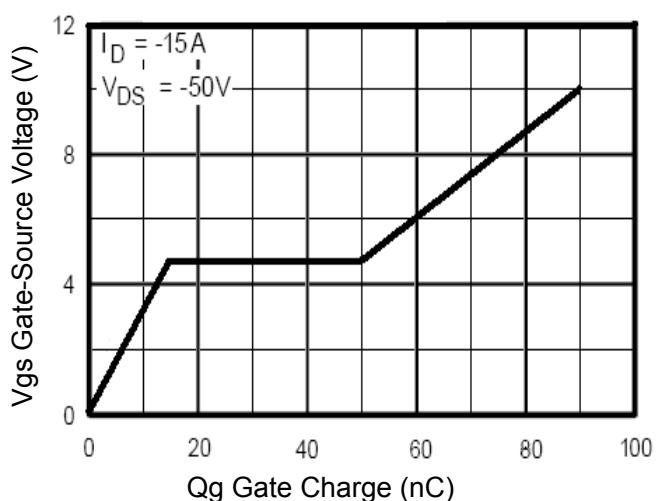
**Figure 1 Output Characteristics**



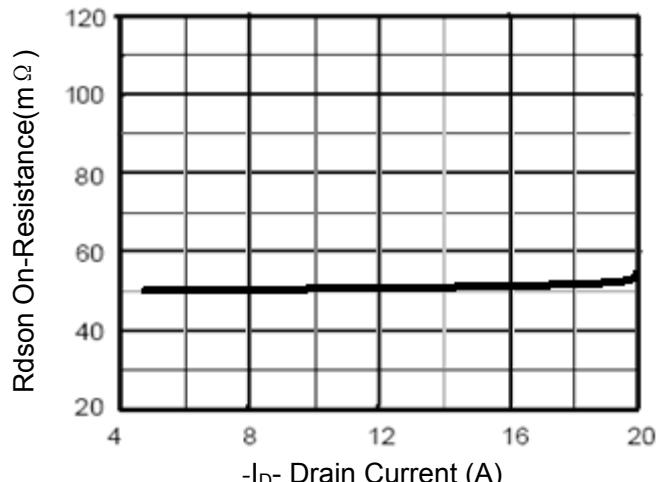
**Figure 4 Rdson-JunctionTemperature**



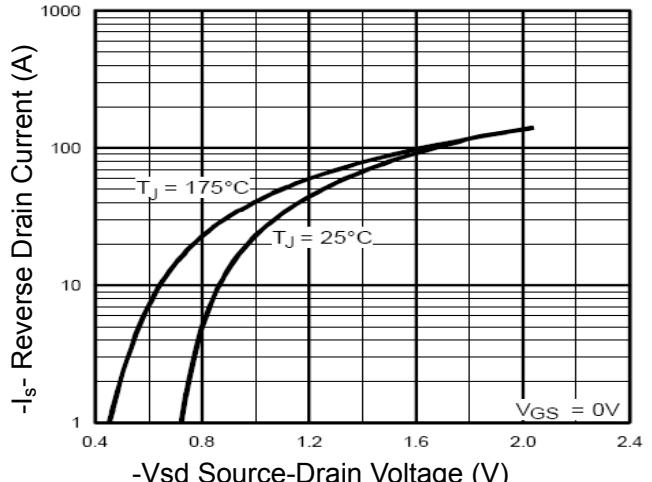
**Figure 2 Transfer Characteristics**



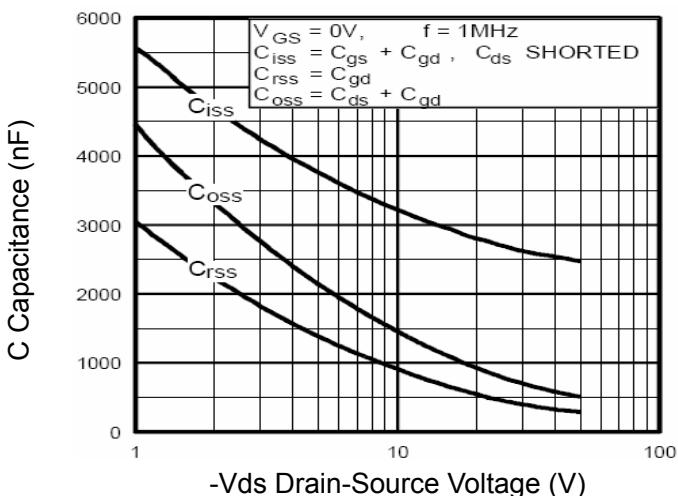
**Figure 5 Gate Charge**



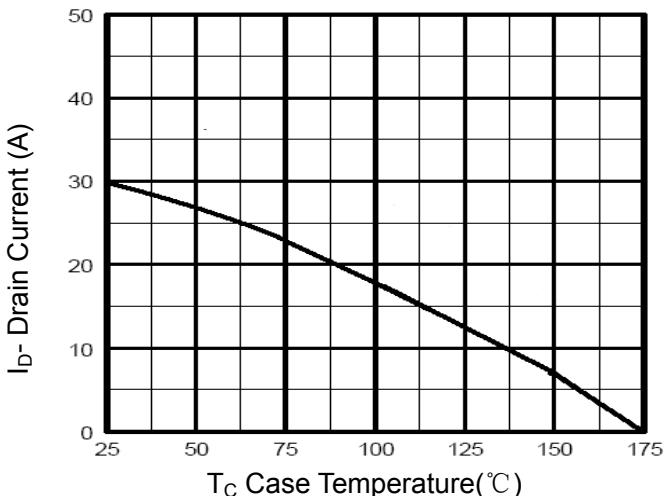
**Figure 3 Rdson- Drain Current**



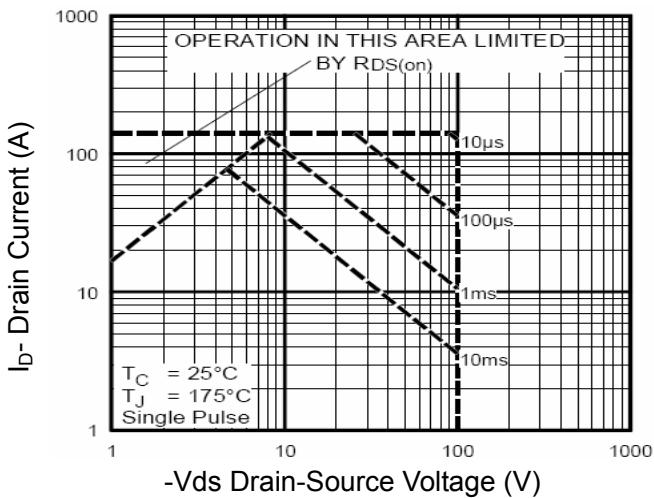
**Figure 6 Source- Drain Diode Forward**



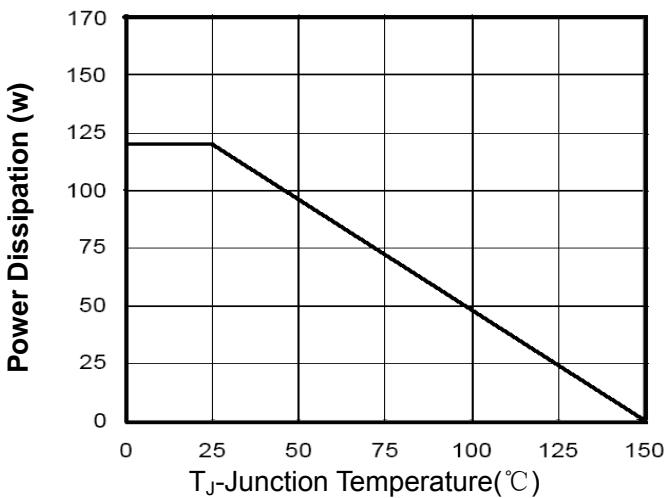
**Figure 7 Capacitance vs Vds**



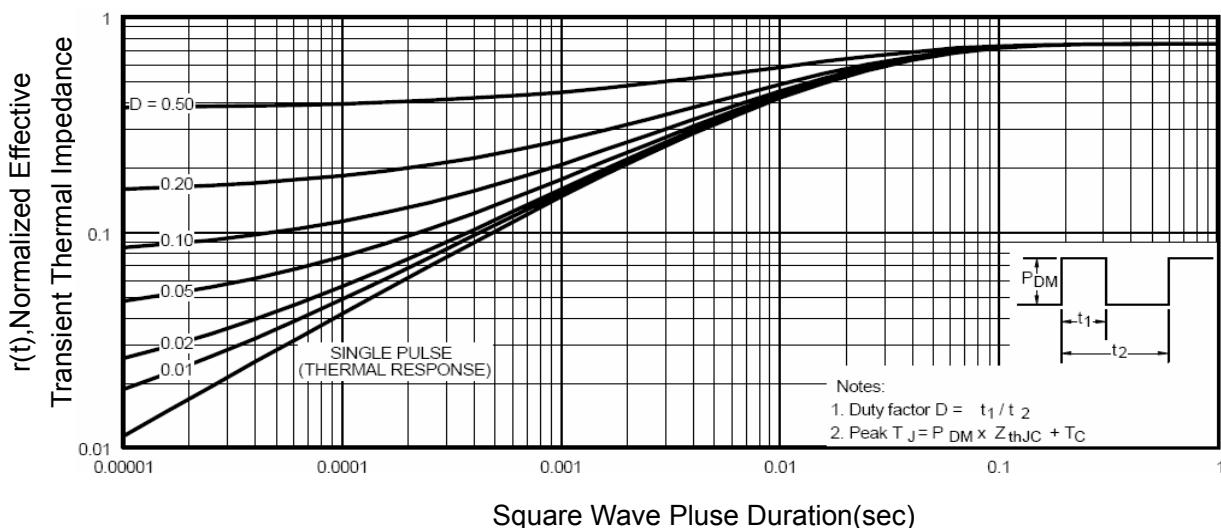
**Figure 9 Drain Current vs Case Temperature**



**Figure 8 Safe Operation Area**

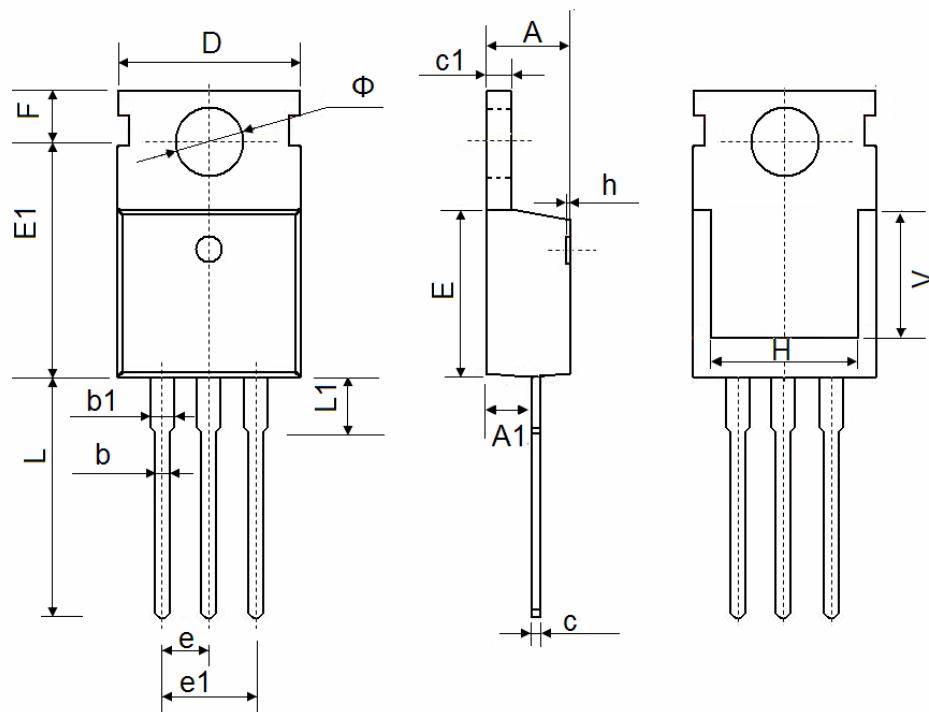


**Figure 10 Power De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

## TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
Φ	3.400	3.800	0.134	0.150