

	SPECIFIC	ATIONS					
CUSTOMER	:	CDE030					
SAMPLE CODE	SAMPLE CODE . SE12864LRF-042-H-Q						
MASS PRODUCTION CODE	: _	PE12864LRF-0	42-H-Q				
SAMPLE VERSION	: _	01					
SPECIFICATIONS EDITION	: _	002					
DRAWING NO. (Ver.)	:	DTE-08339 (Ve	r.0)				
PACKAGING NO. (Ver.)	:	DPK-08582 (Ve	er.0)				
		C	Date:				
Approved	Cheo	ked	Designer				
建 一种		Nor 14 g	下日前14 李贵忠 POWERTIP				
Preliminary specificationSpecification for sample a			2008.11.17 HK RD APR				
	POWERTIP T	ECH. CORP.					
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RECORDS OF REVISION

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
10/07/2008	01	001	New Drawing		黃杰峰
11/11/2008	01	002	First Sample		黃杰峰
				Tota	l: 26pages



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1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	128*64 Dots
LCD Type	FSTN, Positive, Transflective, Extended Temperature
Driver Condition	LCD Module: 1/65 Duty, 1/9 Bias
Viewing Direction	6 O'clock
Backlight	Yellow-Green LED B/L
Weight	29.4 g
Interface	4-line serial interface
Other(controller / driver IC)	ST7567
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web side :
	http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value			
Outline Dimension	80.0(L) *54.0(w) (except FPC length) * 9.7(H)			
Viewing Area	70.7 (W) * 38.8 (L)	mm		
Active Area	66.545(W) *33.265 (L)	mm		
Dot Size	0.505 (W) * 0.505 (L)	mm		
Dot Pitch	0.52(W) * 0.52 (L)	mm		

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	VDD	_	-0.3	3.6	V
LCD Driver Supply Voltage	V0-XV0	_	-0.3	16	V
Operating Temperature	Т _{ОР}	_	-20	70	°C
Storage Temperature	T _{ST}	_	-30	+80	°C
Storage Humidity	H _D	Ta<60 °C	-	90	%RH

1.4 DC Electrical Characteristics

				$V_{SS} =$	OV, Ta	= 25°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	VDD	-	2.7	3.0	3.3	V
"H" Input Voltage	V _{IH}	-	0.7 VDD	-	VDD	V
"L" Input Voltage	V _{IL}	-	VSS	-	0.3VDD	V
"H" Output Voltage	V _{OH}	I _{OUT} =1mA,VDD=3.0V	0.8VDD	-	VDD	V
"L" Output Voltage	V _{OL}	I _{OUT} =-1mA,VDD=3.0V	Vss	-	0.2 VDD	V
Sumply Current	-	VDD=3.0V;VOP=8.5V; Pattern= Full display	-	0.31	-	
Supply Current	I _{dd}	V _{DD} =3.0V;V _{OP} =8.5V; Pattern= Horizontal line*1	-	0.75	1.5	mA
		-20°C		8.7	8.9	
LCM Driver Voltage	V _{OP} *2	25°C	8.3	8.5	8.7	V
		70°C	8.2	8.4	8.6	

NOTE: *1 The Maximum current display;

*2 The VOP test point is the capacitance of C10.



1.5 Optical Characteristics

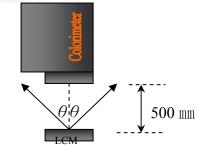
				D Panel ·	1/65Duty '	1/9Blas , v	² LCD - 8.3	v, $1a = 25$
Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Response Time	Rise	tr		-	100	150	me	Note2
Response Time	Fall	tf		-	250	375	ms	Note2
	Тор	$\Theta Y+$	C <u>></u> 2.0,	-	-	40		
Viewing angle	Bottom	ΘY-	$\emptyset = 270^{\circ}$	-	-	40	Dec	Notes 1
range	Left	ΘX-		-		45	Deg.	notes 1
	Right	ΘX+		- ,	-	45		
Contrast Rat	tio	С	$\theta = 0^{\circ},$ $\emptyset = 270^{\circ}$	2	4	-	-	Note 3
Average Bright (with LCD)		IV		2.5	3.0	-	cd/m ²	
Wavelength Uniformity *1		Hue	IF=80mA	569	571	576	nm	Note 4
		∆B		70	-	-	%	

LCD Panel : 1/65Duty , 1/9Bias , $V_{LCD} = 8.5$ V , Ta = 25° C

Note 4 :

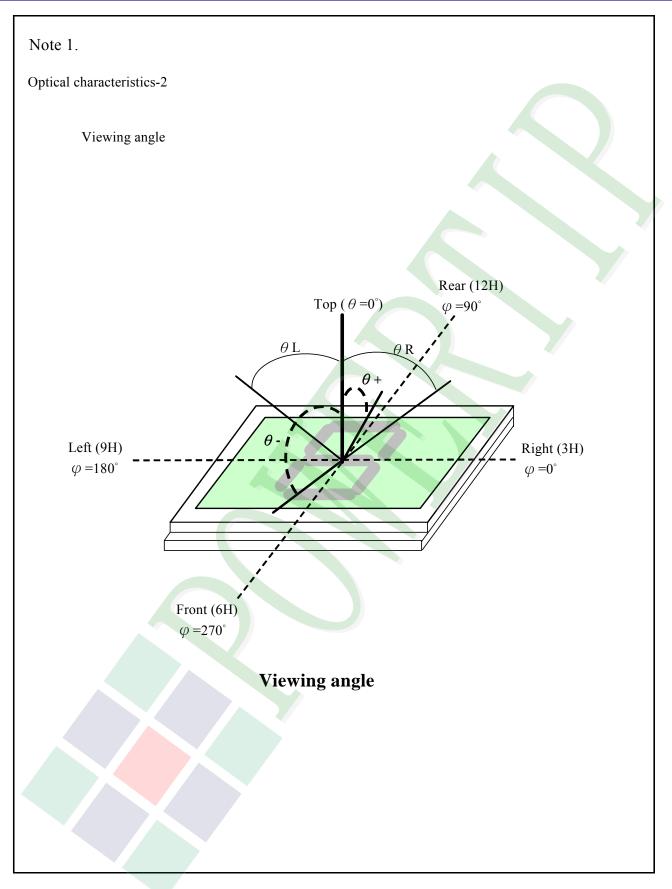
- $1 : \triangle B = B(\min) / B(\max) * 100\%$
- 2: Measurement Condition for Optical Characteristics:
 - a : Environment: 25℃ ±5℃ / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm}$, ($\theta = 0^{\circ}$)
 - c: Equipment: TOPCON BM-7 fast , (field 1°), after 10 minutes operation.
 - d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



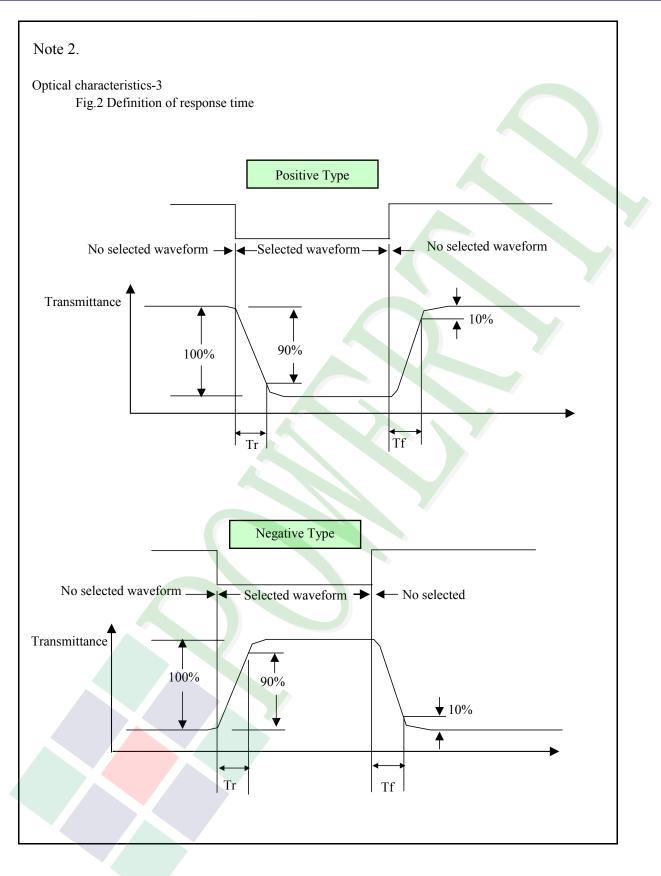


Colorimeter=BM-7 fast

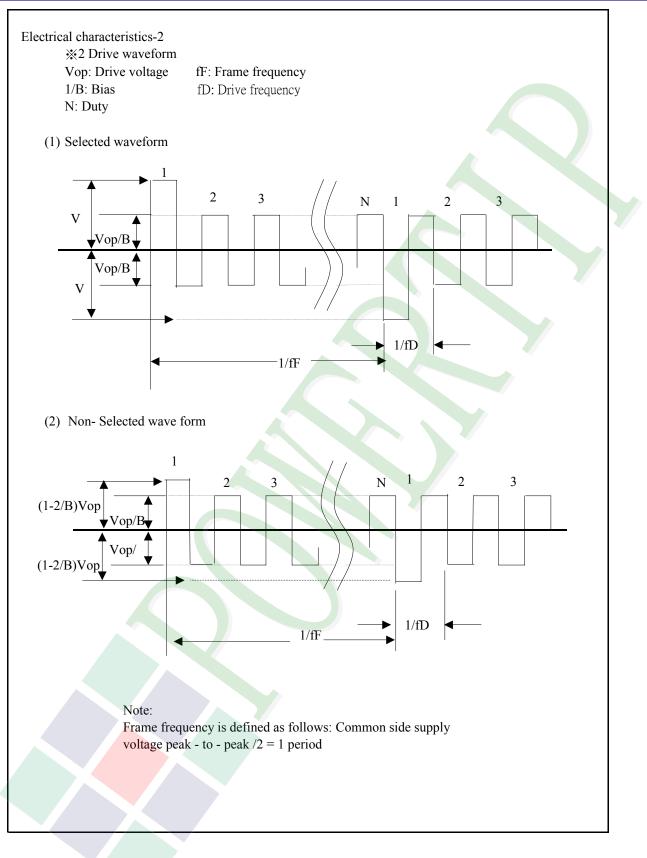




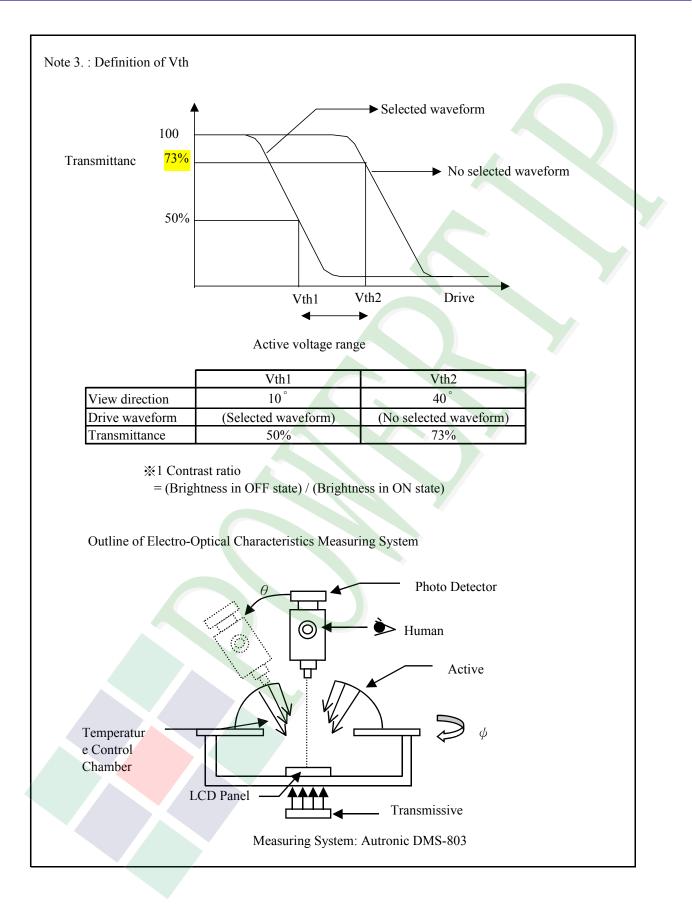














1.6 Backlight Characteristics

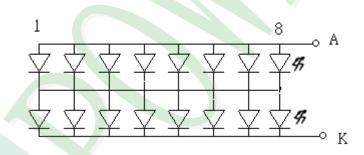
LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Ratings	Unit
Forward Current	IF	Ta =25℃	80	mA
Power Dissipation	PD	Ta =25℃	336	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage	VF	IF=80mA	-	4.2	4.6	V	
Average Brightness (without LCD)	IV	IF=80mA	9.6	12.0	V _	cd/m ²	
Color	YELLOW-GREEN						





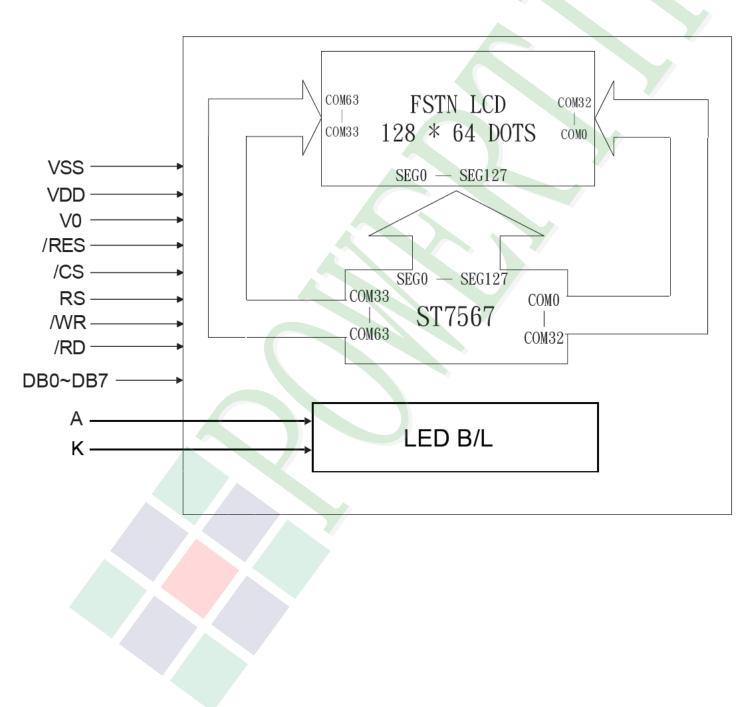
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



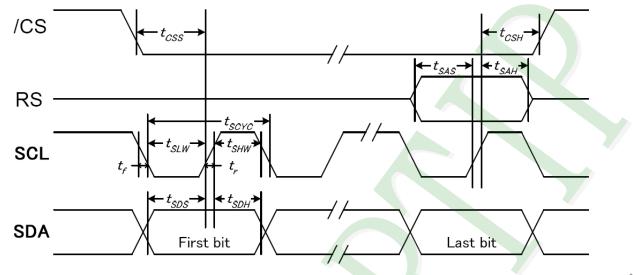
2.2 Interface Pin Description

Pin No.	Symbol	Function
1	VSS	Power Supply (VSS=0)
2	VDD	Power Supply (VDD>VSS)
3	V0	NO Connection
4	/RES	Controller reset (module reset)
5	/CS	Used to enter chip select signal
6	RS	Select control data or display data for read/write operation "L"=control data "H"=display data
7	/WR	Must be connected to VDD
8	/RD	Must be connected to VDD
9	DB0	Must be connected to VDD
10	DB1	Must be connected to VDD
11	DB2	Must be connected to VDD
12	DB3	Must be connected to VDD
13	DB4	Must be connected to VDD
14	DB5	Must be connected to VDD
15	DB6(SCL)	Serial data input
16	DB7(SDA)	serial clock input
17	А	Power supply LED backlight(+)
18	K	Power supply LED backlight(-)



2.3 Timing Characteristics

System Bus Timing for 4-Line Serial Interface



(VDD = 3.3V , Ta =-30~85°C)

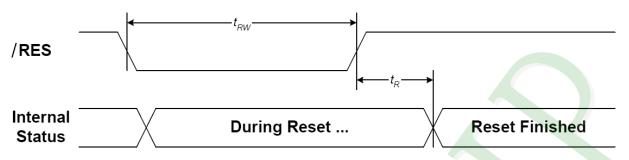
			`	. ,	/
Signal	Symbol	Condition	Min.	Max.	Unit
	tSCYC		50	—	
SCL	tSHW		25	_	
	tSLW		25	_	
RS	tSAS		20	—	
	tSAH		10	_	ns
SDA	tSDS		20	_	
SDA	tSDH		10	_	
/CS	tCSS		20	_	
	tCSH		40	_	
	SCL RS SDA	SCL SCL tSCYC tSHW tSLW tSAS tSAH tSDA tSDH tCSS tSSS tCSS tSCYC tSCYC tSCYC tSCYC tSCYC tSCYC tSCYC tSCYC tSCYC tSCYC tSCYC tSCYC tSLW tSLW tSLW tSAS tSAH tSDS tSDH tSCSC tSDH tSDS tSDH tSCSC tSDH tSDS tSDH tSCSC tSDH tSDS tSDH tSCSC tSDH tSDS tSDH tSDS tSDH tSDS tSDH tSDS tSDH tSCSC tSDH tSDS tSDH tSDS tSDH tSDS tSDH tSSS tSDH tSSS tSSS tSDH tSSS tSSS tSDH tSSS tSSS tSSS tSDH tSSS tSSS tSSS tSDH tSSS tSSS tSSS tSDH tSSS	Image: science of the science of t	SignalSymbolConditionMin.SCLtSCYC50tSLW25tSLW25tSLW25tSAS20tSAH10SDAtSDS20tCSS10tCSS20	tSCYC 50 SCL tSCYC 50 tSHW 25 tSLW 25 RS tSAS 20 tSAH 10 SDA tSDS 20 tSDH 100 tSDH 200 tSDA 200 tSDH 200 tCSS 200

(VDD = 2.8V , Ta =-30~85°C)

				(,	00 00 0,
Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		100	_	
SCLK "H" pulse width	SCL	tSHW		50	—	
SCLK "L" pulse width		tSLW		50	_	
Address setup time	DO	tSAS		30	_	
Address hold time	RS	tSAH		20	_	ns
Data setup time	SDA	tSDS		30	_	
Data hold time	SDA	tSDH		20	_	
CSB-SCLK time	/CS	tCSS		30	—	
CSB-SCLK time	103	tCSH		60	—	



Hardware Reset Timing



(VDD = 3.3V , Ta =-30~85°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		—	1.0	110
Reset "L" pulse width	tRW		1.0	_	us

(VDD = 2.8V , Ta =-30~85°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR			2.0	
Reset "L" pulse width	tRW		2.0	_	us

2.4 Display command

COMMAND BYTE							DESCRIPTION				
INSTRUCTION	A0	(RWR)	D7	D6	D5	D4	D3	D2	D1	D0	DESCRIPTION
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address
(4)	0	0	0	0	0	1	X7	X6	X5	X4	Set column address (MSB)
Set Column Address	0	0	0	0	0	0	Х3	X2	X1	X0	Set column address (LSB)
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM
(8) SEG Direction	0	0	1	0	1	0	0	0	0	МХ	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset
(15) COM Direction	0	0	1	1	0	0	MY	1	•	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio
(18) Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set
(10) Set LV	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume (EV) level
40.0.10.1	0	0	1	1	1	1	1	0	0	0	Double command!!
(19) Set Booster	0	0	0	0	0	0	0	0	BL1	BL0	Set booster level: 00=4X, 01=5X, 10=6X
(20) Power Save	0	0			Co	mpound	Comm	and			Display OFF + All Pixel ON
(21) NOP	0	0	1	1	1	0	0	0	1	1	No operation
(22) Test	0	0	1	1	1	1	1	1	1	-	Do NOT use. Reserved for testing.

Note: Symbol "-" means this bit can be "H" or "L".

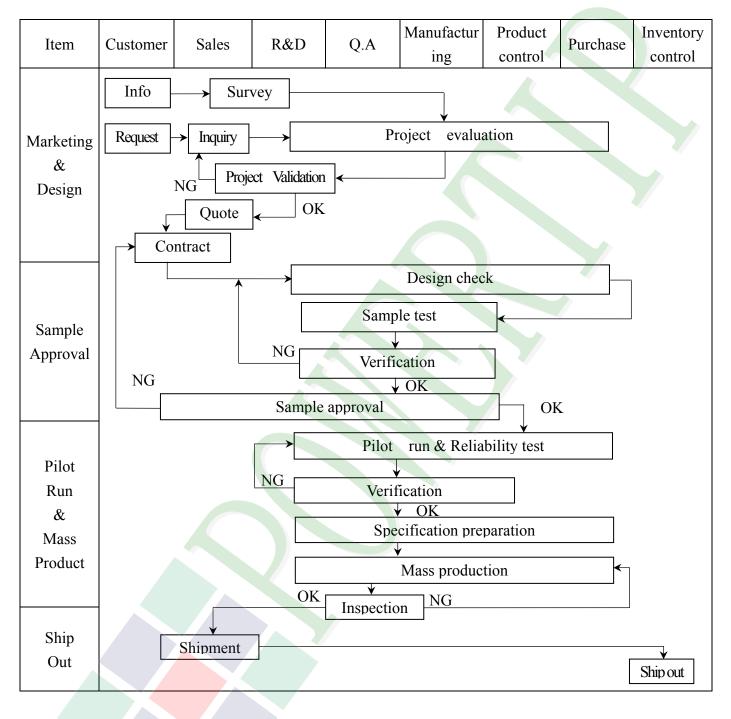
2.5 Jumper

J1(2.3)/J2(2.3)/J(2.3)/J6:SHORT;OTHER:OPEN



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart

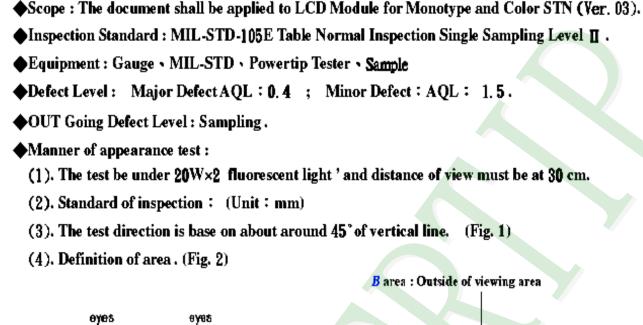


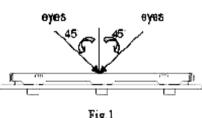


Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
Sales Service	Info	→ Claim sis report	[Trackin	Failure an Corrective			
Q.A Activity	1. ISO 9001 3. Equipme 5. Standardi		n		ocess improv Education An	1 1		

Inspection Specification

3.2







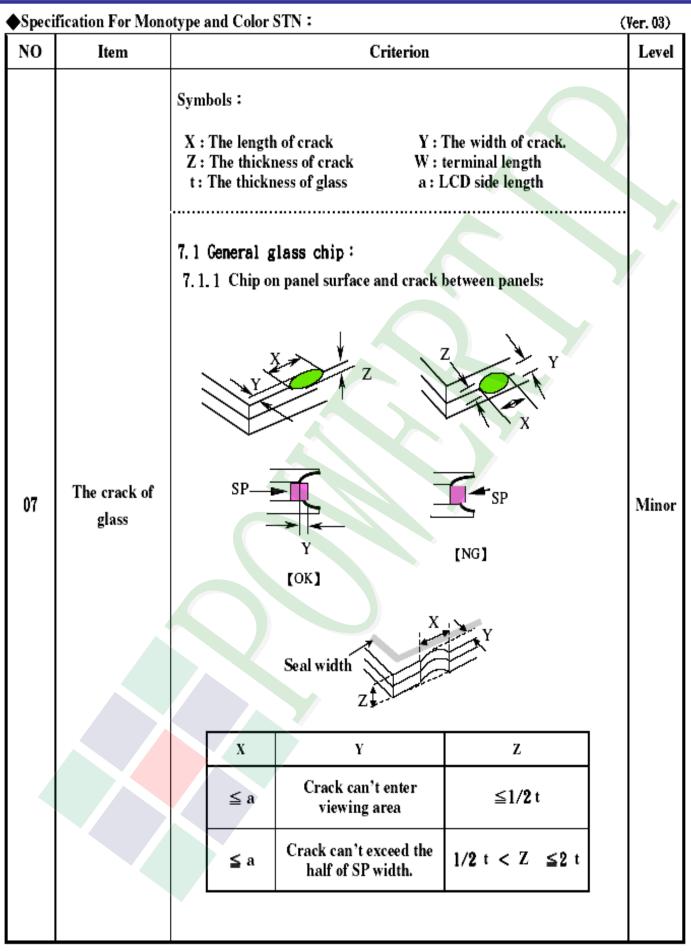
Specification:

NO	Item	Criterion	level
		 The part number is inconsistent with work order of Production. 	Major
01	Product condition	1. 2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
		4.1 Missing line character and icon.	Major
04	Electrical Testing	4. 2 No function or no display.	Major
		4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major

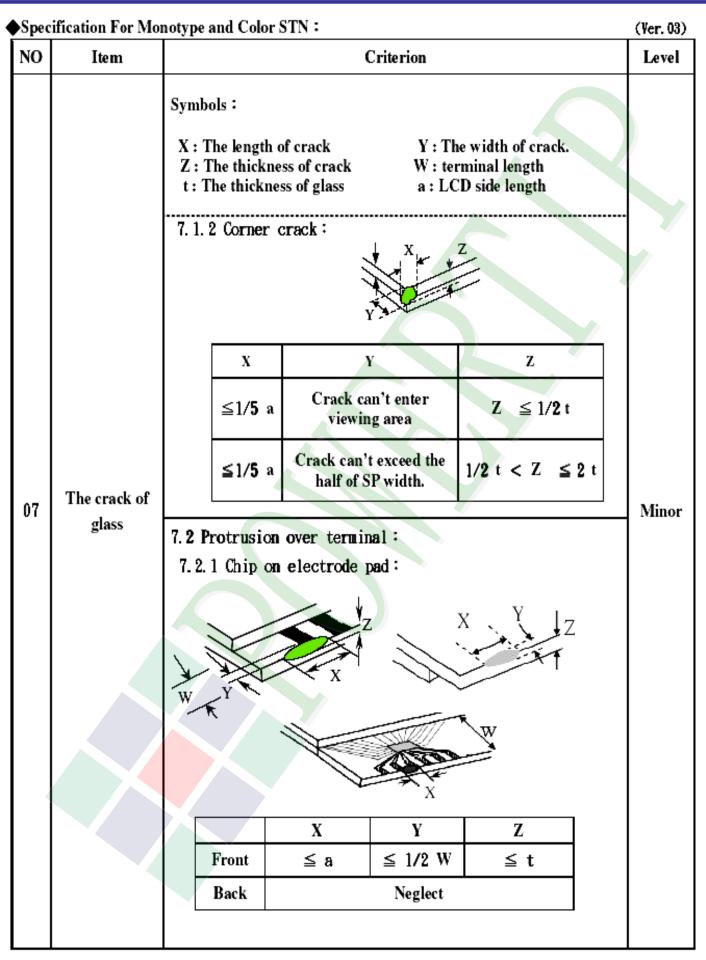


· -	pecification For Monotype and Color STN : (Ver. 0) Item Criterion lev							
NO	Item	(Criterion					
	Black or white dot 、scratch 、 contamination	 5. 1 Round type: 5. 1. 1 display only : White and black spots on display ≤ 0. 30 mm, no more than 4 white or black spots present. Densely spaced : NO more than two spots or lines within 3 mm. 						
		5. 1. 2 Non-display :						
	Round type	Dimension		Acceptance (Q'ty)	Minor		
	Kound type	(diameter : Φ)		A area	B area			
	le la	$\Phi \leq 0.10$ A		ept no dense				
<u>م</u> ۲	Y	$0.10 < \Phi \leq 0.20$		3				
05	-	$0.20 < \Phi \leq 0.30$		2	Ignore			
	Φ=(x+y)/2	Total quantity		4	4			
		5. 1. 3 Line type: Dimension		Acceptance (Q'ty)				
	Line type $\downarrow^{\downarrow}W$ $\downarrow^{\downarrow}W$ $\downarrow^{\downarrow}W$ $\downarrow^{\downarrow}W$ $\downarrow^{\downarrow}W$	Length (L) Width (W)		A area	B area			
		W≦	D. 0 3	Accept no dens	æ			
		$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$		4	Ignore			
		W >0	. 075	Asro	ound type			
		Dimension		Acceptance	(O'tv)			
		(diameter : Φ)		A area	B area			
		Φ ≦ 0 . 20	Accept no dense					
06	Polarizer	$0.20 < \Phi \leq 0.50$		3		Minor		
	Bubble	$0.50 < \Phi \leq 1.00$		2	Ignore			
		Φ > 1.00		0				
		Total quantity		4				

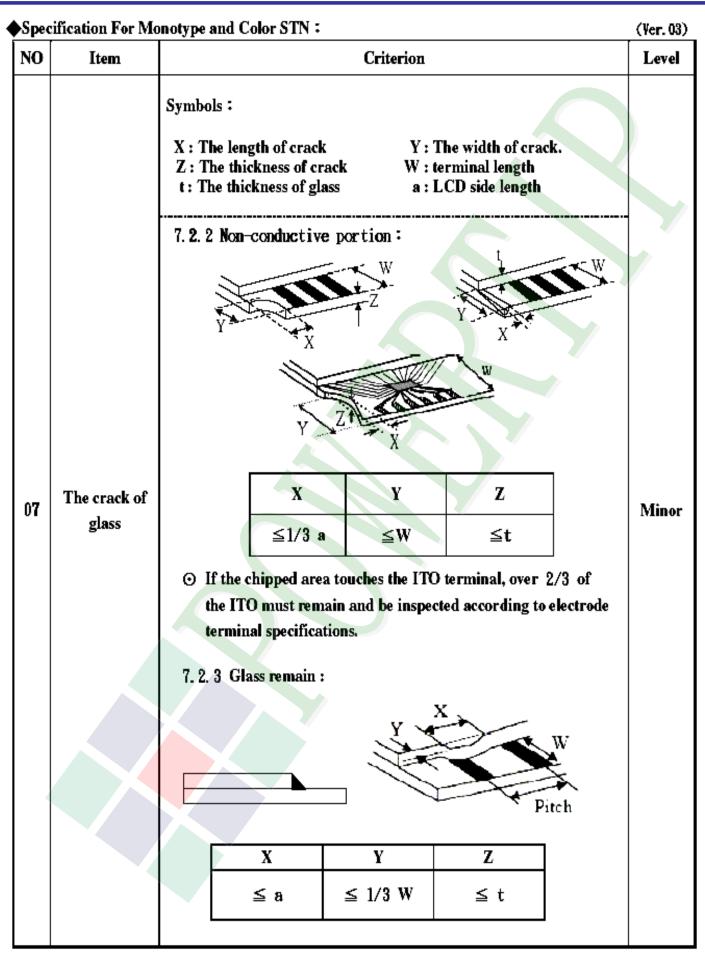














♦ Speci	Specification For Monotype and Color STN : (Yer. 03							
NO	Item	Criterion	Level					
		8. 1 Backlight can't work normally.	Major					
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major					
		8. 3 Illumination source flickers when lit.	Major					
		9. 1 Pin type must match type in specification sheet.	Major					
		9. 2 No short circuits in components on PCB or FPC.	Major					
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor					
	9. 4 The folding and peeled off in polarizer are not acceptable.	9. 4 The folding and peeled off in polarizer are not acceptable.	Minor					
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1.5 mm.	Minor					



4. RELIABILITY TEST

4.1 Reliability Test Condition

	Kenability Test Condition						
NO.	TEST ITEM	TEST CONDITION					
1	High Temperature Storage Test	Keep in 80 $\pm 2^{\circ}$ C 96 hrs					
		Surrounding temperature, then storage at normal condition 4hrs					
2	Low Temperature Storage Test	Keep in -30 $\pm 2^{\circ}$ C 96 hrs					
		Surrounding temperature, then storage at normal condition 4hrs					
		Keep in +60°℃/90%RH duration for 96 hrs					
3	High Humidity Storage	Surrounding temperature, then storage at normal condition 4hrs					
		Air Discharge:Contact Discharge:					
		Apply 2 KV with 5 times Apply 250V with 5 times					
		Discharge for each polarity +/- discharge for each polarity +/-					
		1. Temperature Ambient: $15^{\circ}C \sim 35^{\circ}C$					
		2. Humidity relative: $30\% \sim 60\%$					
4	ESD Test	3. Energy Storage Capacitance(Cs+Cd):150pF±10%					
		4. Discharge Resistance(Rd):330 $\Omega \pm 10\%$					
		5. Discharge, mode of operation:					
		Single Discharge (time between successive discharges at least 1 s)					
		(Tolerance If the output voltage indication: ±5%)					
		-20° C $\rightarrow 25^{\circ}$ C $\rightarrow 70^{\circ}$ C $\rightarrow 25^{\circ}$ C					
5	Temperature Cycling Test	(30mins) (5mins) (30mins) (5mins)					
5	Temperature Cycning Test	10 Cycle					
		Surrounding temperature, then storage at normal condition 4hrs					
		1. Sine wave $10 \sim 55$ HZ frequency (1 min)					
6	Vibration Test (Packaged)	2. The amplitude of vibration :1.5 mm					
		3. Each direction (XYZ) duration for 2 Hrs					
		Packing Weight (Kg) Drop Height (cm)					
		$\begin{array}{c c c c c c c c c c c c c c c c c c c $					
		45.4 ~ 90.8 76					
7	Drop Test (Packaged)						
	Diop rest (Fackageu)	90.8~454 61					
		Over 454 46					
		Drop direction : 3 comer /1 edges /6 sides etch 1 times					
L							

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

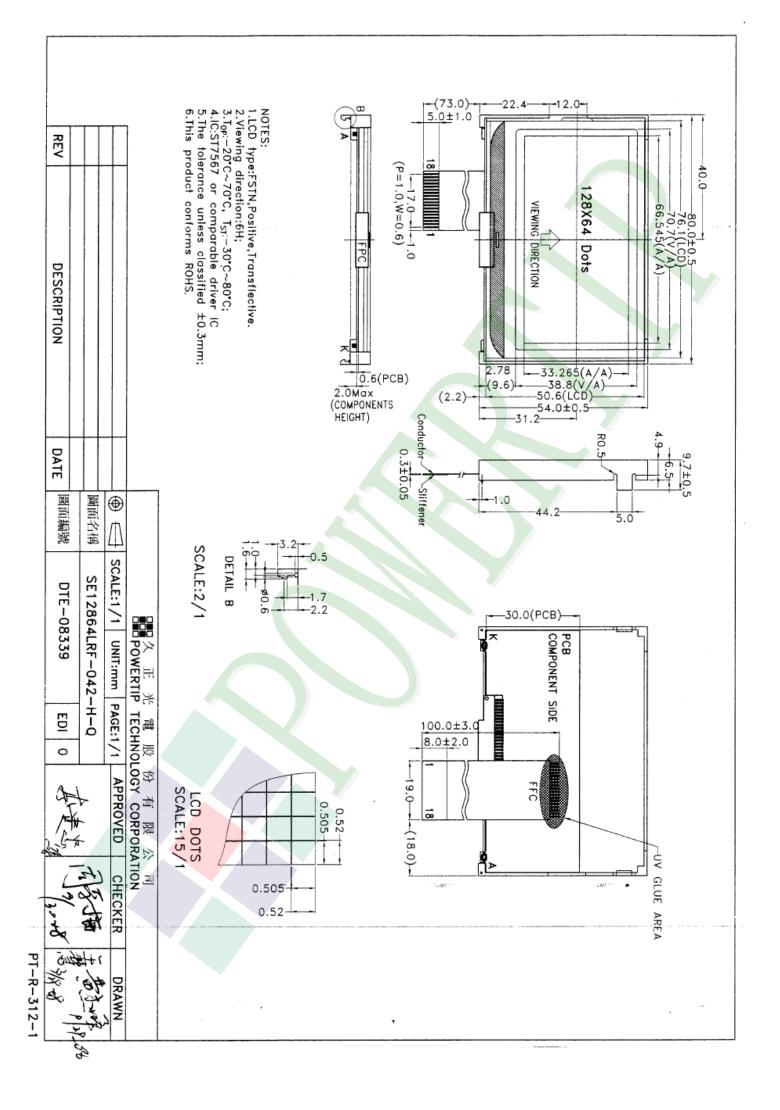
5.4 TERMS OF WARRANTY

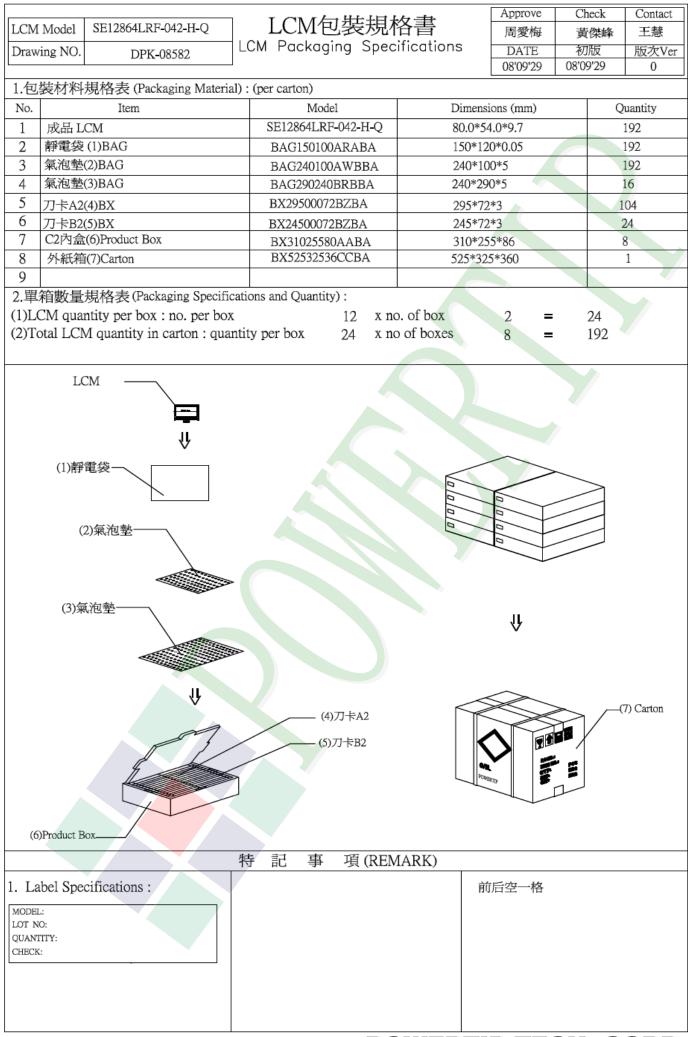
5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





POWERTIP TECH. CORP.