

PE506BA

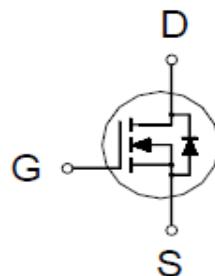
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	6mΩ @ $V_{GS} = 10V$	38A



PDFN 3X3P



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current ³	I_D	38	A
		24	
		12	
		9.8	
Pulsed Drain Current ¹	I_{DM}	60	
Avalanche Current	I_{AS}	32	
Avalanche Energy	E_{AS}	53	mJ
Power Dissipation	P_D	15.6	W
		6.3	
		1.6	
		1	
Operating Junction & Storage Temperature Range	T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$	77	8	°C / W
Junction-to-Case	$R_{\theta JC}$			

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ C$.

³Package limitation current is 19A.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.6	3	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 12\text{A}$		6.2	9	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 12\text{A}$		5.4	6	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 12\text{A}$		80		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1\text{MHz}$		1818		pF
Output Capacitance	C_{oss}			263		
Reverse Transfer Capacitance	C_{rss}			218		
Total Gate Charge ²	$Q_{\text{g}}(V_{\text{GS}}=10\text{V})$	$V_{\text{DS}} = 15\text{V}, I_D = 12\text{A}$		43.8		nC
	$Q_{\text{g}}(V_{\text{GS}}=4.5\text{V})$			23.1		
Gate-Source Charge ²	Q_{gs}			5.5		
Gate-Drain Charge ²	Q_{gd}			12.7		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 15\text{V}, I_D \geq 12\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		25		nS
Rise Time ²	t_r			24		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			99		
Fall Time ²	t_f			57		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current ³	I_S				13	A
Forward Voltage ¹	V_{SD}	$I_F = 12\text{A}, V_{\text{GS}} = 0\text{V}$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 12\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		22		nS
Reverse Recovery Charge	Q_{rr}		$V_{\text{GS}} = 0\text{V}$		12	nC

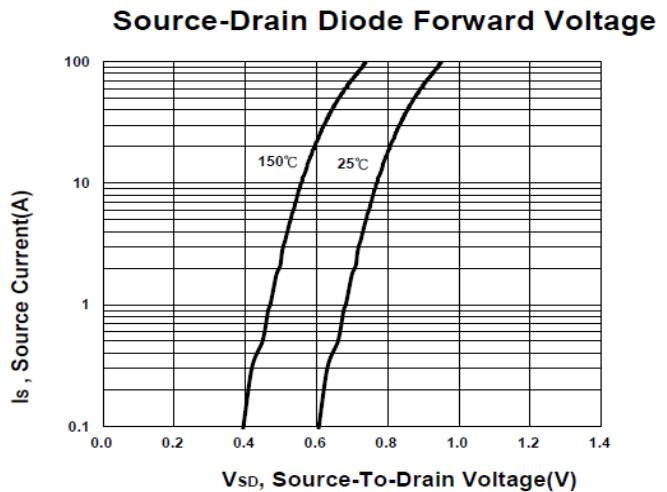
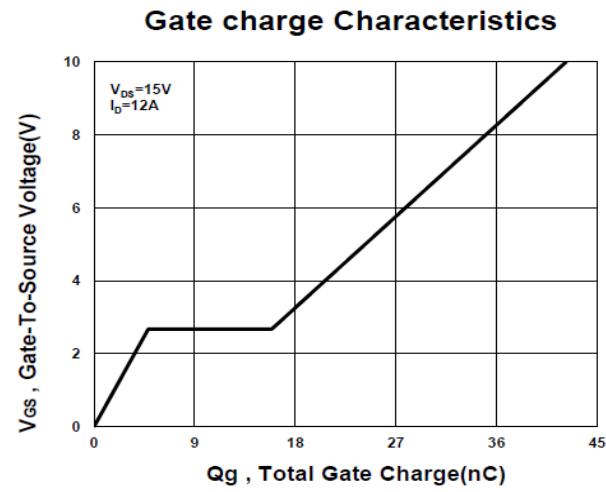
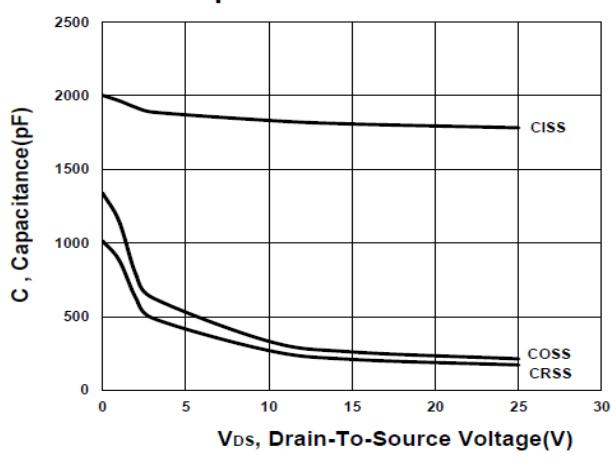
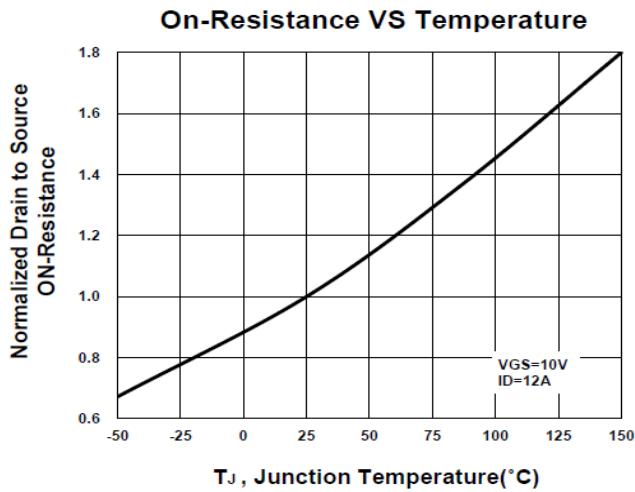
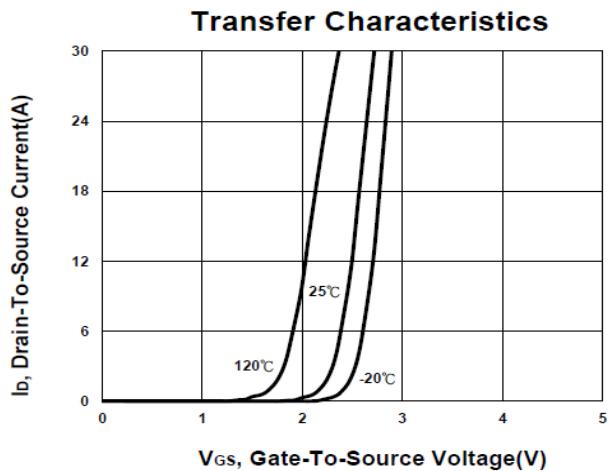
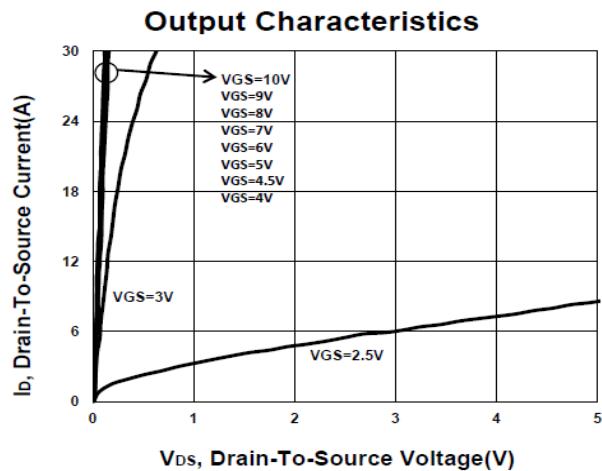
¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

³Package limitation current is 19A.

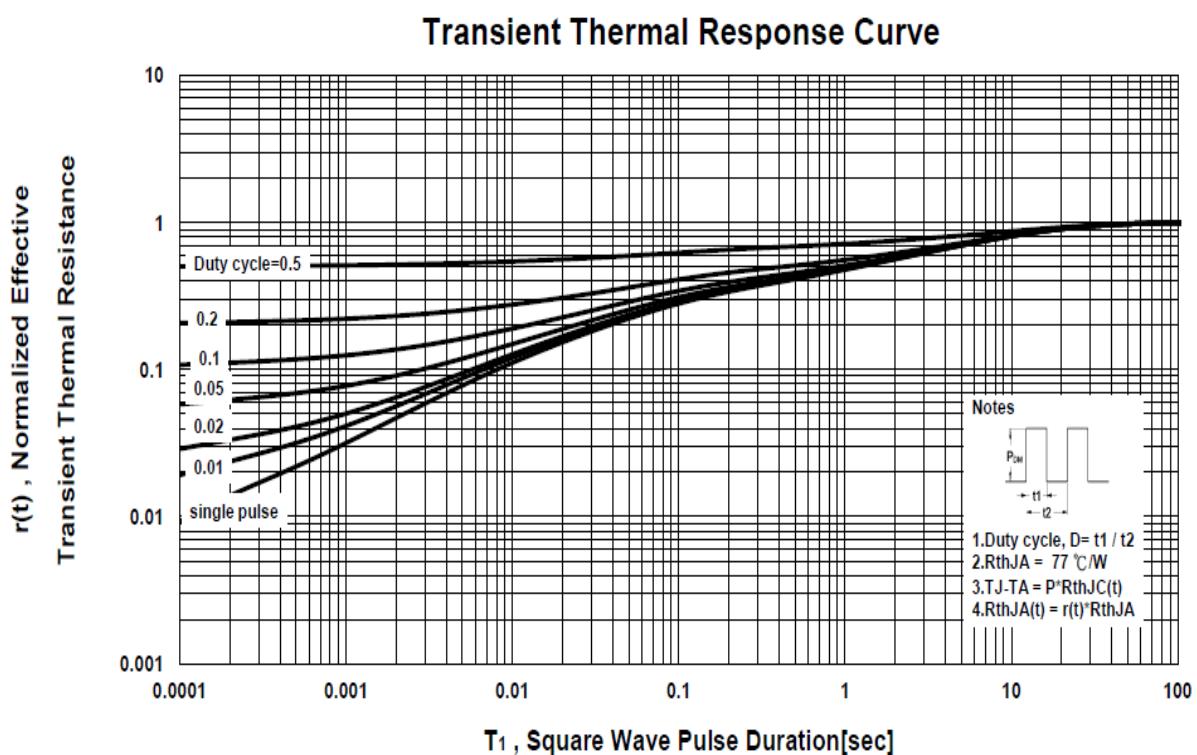
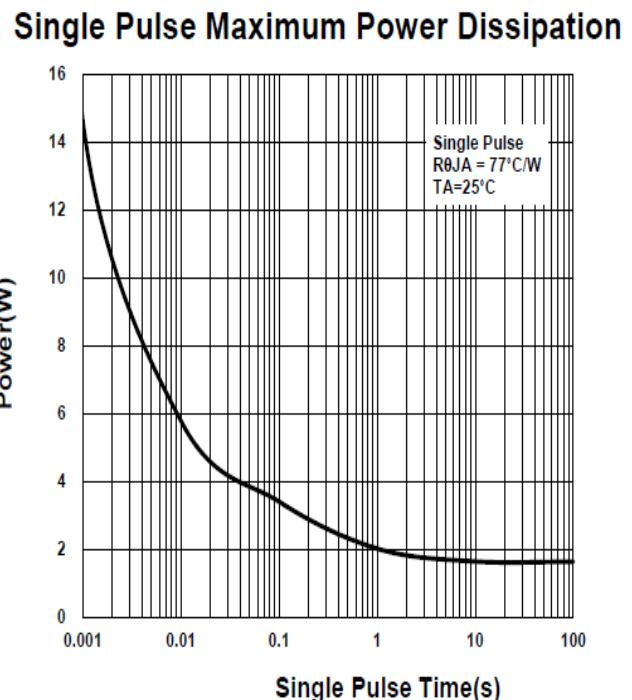
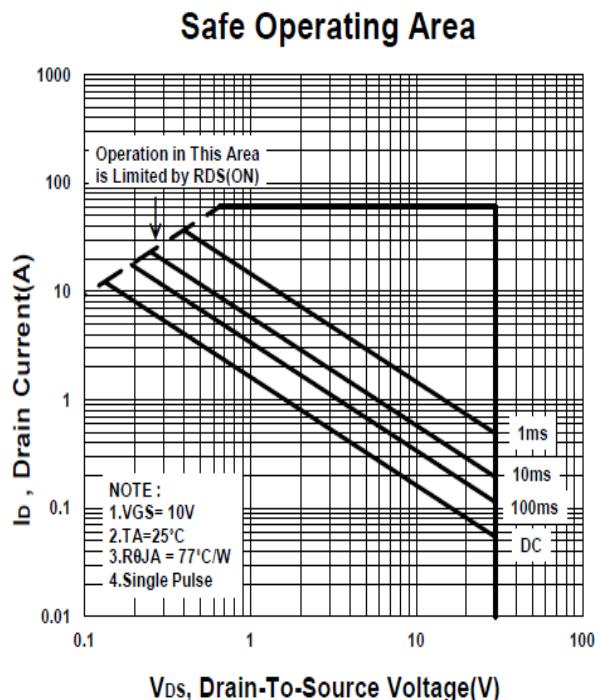
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Package Dimension

PDFN 3x3P MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	3		3.6	I	0.7		1.12
B	2.88		3.2	J	0.1		0.33
C	2.9		3.2	K	0.6		
D	1.98		2.69	L	0°	10°	12°
E	3		3.6	M	0.14		0.41
F	0		0.455	N	0.6		0.7
G	1.47		2.2	O	0.12		0.36
H	0.15		0.56	P	0		0.2

