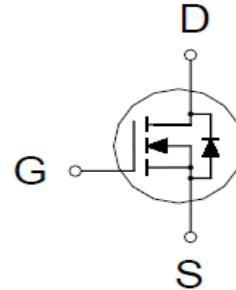


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PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	6mΩ @ $V_{GS} = 10V$	39A



100% UIS Tested
100% Rg Tested

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current ⁴	$T_c = 25\text{ °C}$	I_D	39	A
	$T_c = 100\text{ °C}$		25	
Pulsed Drain Current ¹		I_{DM}	100	
Continuous Drain Current ⁴	$T_A = 25\text{ °C}$	I_D	16	
	$T_A = 70\text{ °C}$		13	
Avalanche Current		I_{AS}	33	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	54	mJ
Power Dissipation	$T_c = 25\text{ °C}$	P_D	17.8	W
	$T_c = 100\text{ °C}$		7	
Power Dissipation ³	$T_A = 25\text{ °C}$		3	
	$T_A = 70\text{ °C}$		2	
Operating Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$t \leq 10\text{s}$	$R_{\theta JA}$		40	°C / W
Junction-to-Ambient ²	Steady-State	$R_{\theta JA}$		60	
Junction-to-Case	Steady-State	$R_{\theta JC}$		7	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25\text{ °C}$.

³The Power dissipation is based on $R_{\theta JA}$ $t \leq 10\text{s}$ value.

⁴Package limitation current is 13A.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.3	2	2.3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	μA
		V _{DS} = 20V, V _{GS} = 0V, T _J = 55 °C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 8.8A		6	9	mΩ
		V _{GS} = 10V, I _D = 11A		4	6	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 11A		36		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		1004		pF
Output Capacitance	C _{oss}			324		
Reverse Transfer Capacitance	C _{rss}			190		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.8		Ω
Total Gate Charge ²	Q _g (V _{GS} =10V)	V _{DS} = 15V , V _{GS} = 10V, I _D = 11A		23		nC
	Q _g (V _{GS} =4.5V)			13		
Gate-Source Charge ²	Q _{gs}			2.4		
Gate-Drain Charge ²	Q _{gd}			7.9		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 15V, I _D ≅ 11A, V _{GS} = 10V, R _{GEN} = 6Ω		24		nS
Rise Time ²	t _r			25		
Turn-Off Delay Time ²	t _{d(off)}			50		
Fall Time ²	t _f			22		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current ³	I _S				15	A
Forward Voltage ¹	V _{SD}	I _F = 11A, V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 11A, dl _F /dt = 100A / μS		21		nS
Reverse Recovery Charge	Q _{rr}			7.2		nC

¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

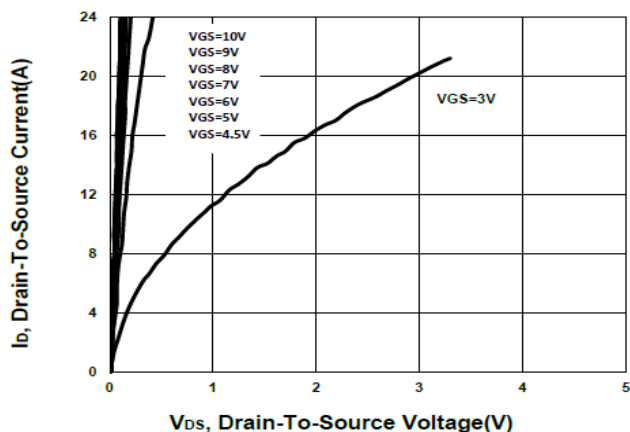
²Independent of operating temperature.

³Package limitation current is 13A.

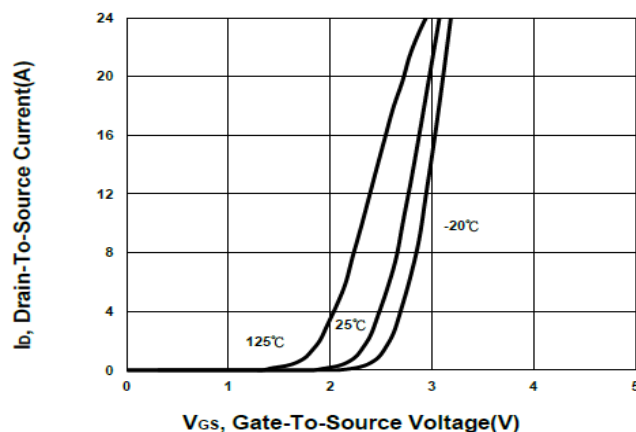
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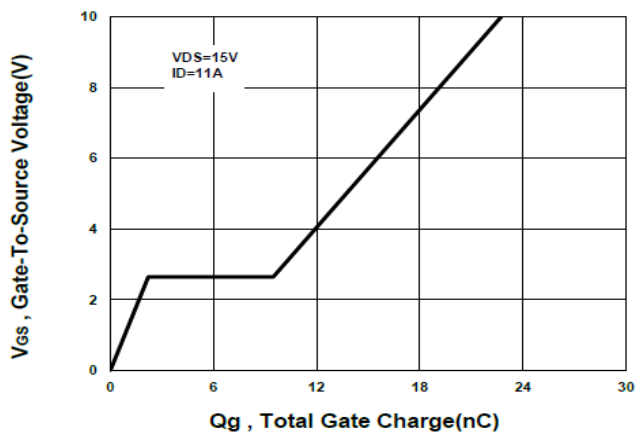
Output Characteristics



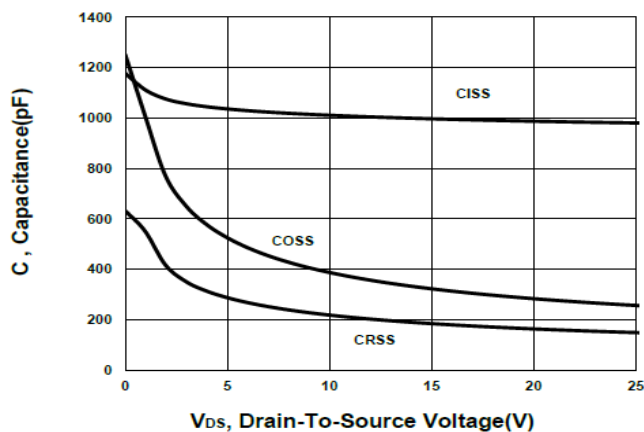
Transfer Characteristics



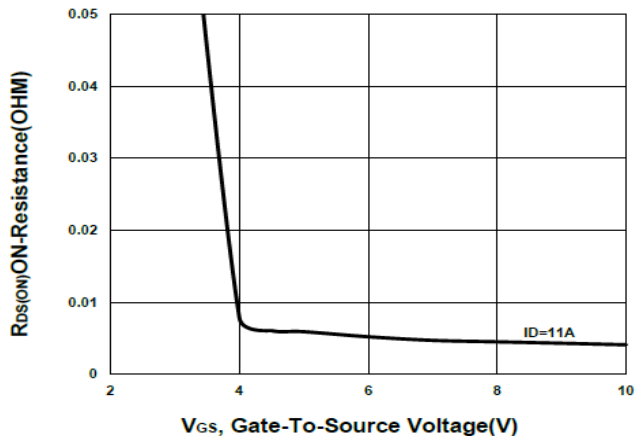
Gate charge Characteristics



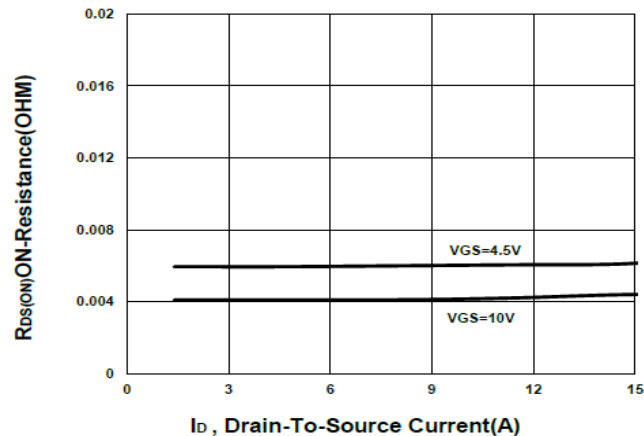
Capacitance Characteristic



On-Resistance VS Gate-To-Source



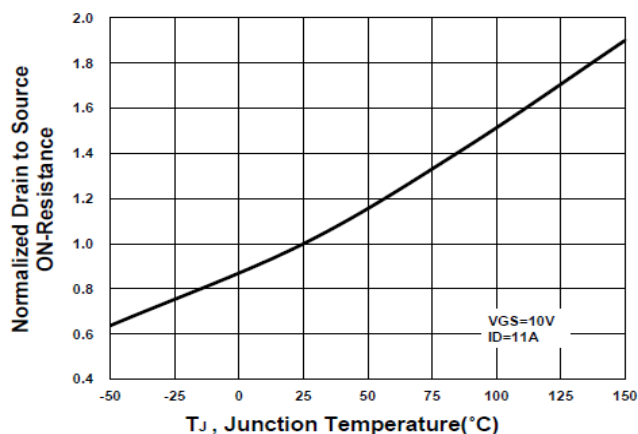
On-Resistance VS Drain Current



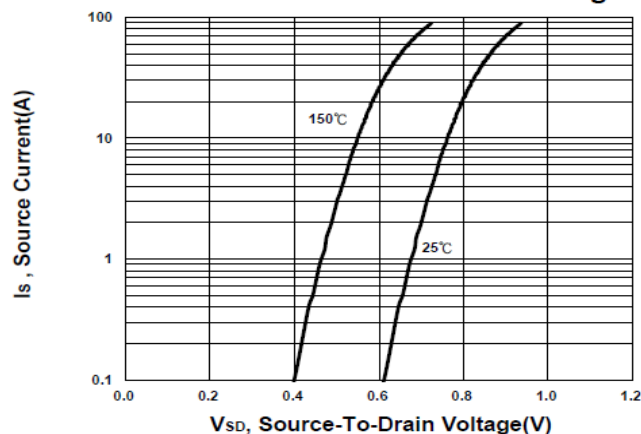
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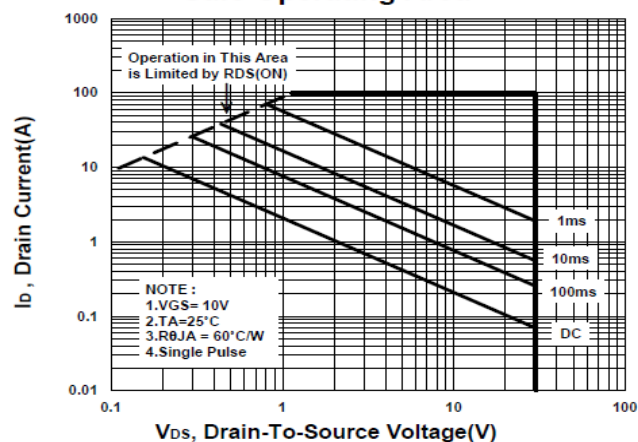
On-Resistance VS Temperature



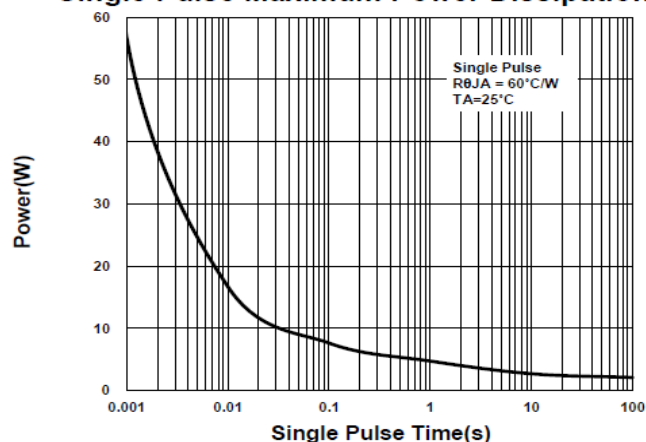
Source-Drain Diode Forward Voltage



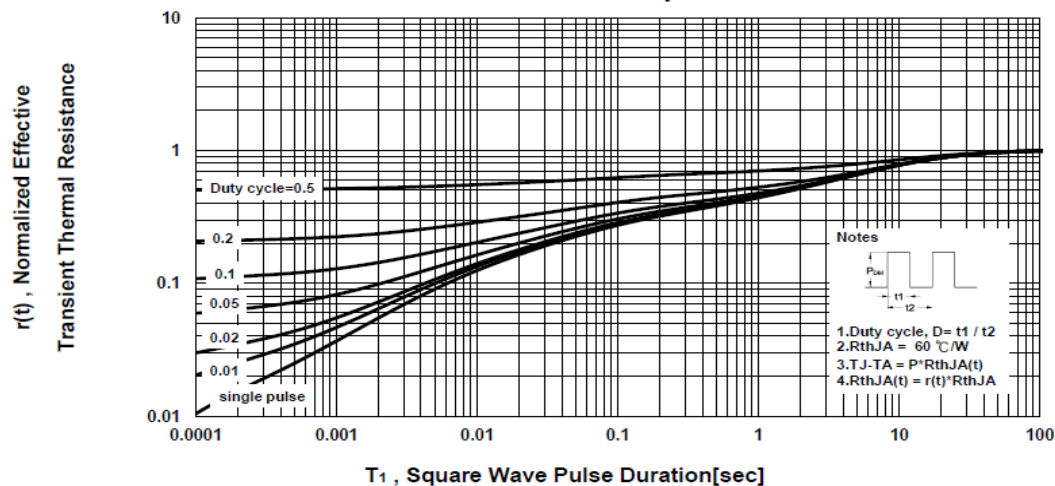
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



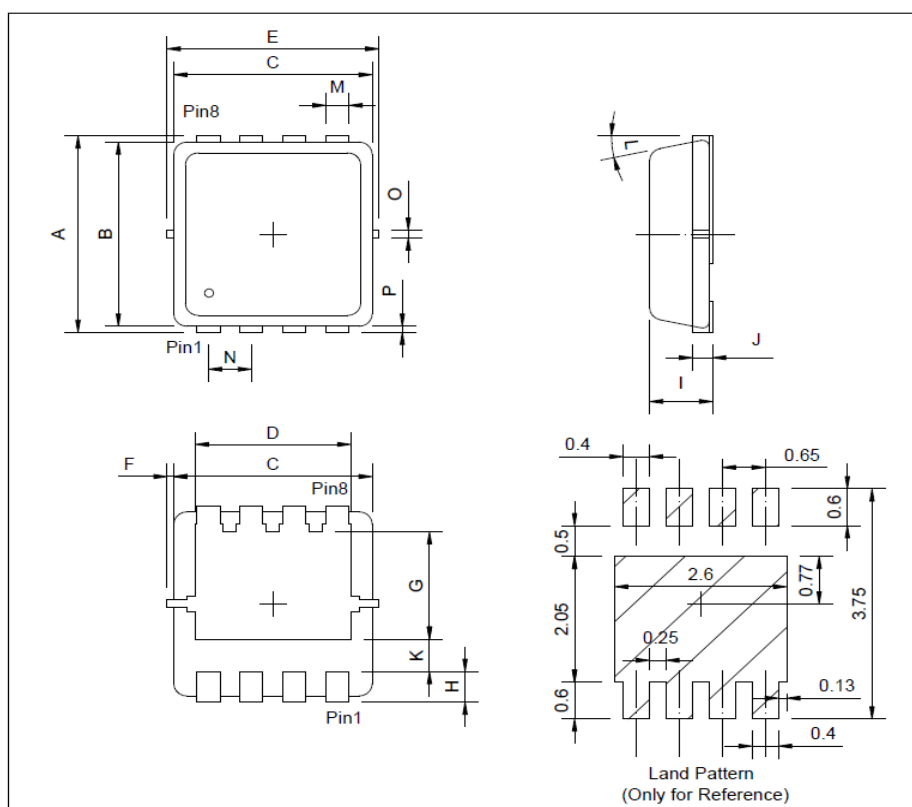
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Package Dimension

PDFN 3x3P MECHANICAL DATA

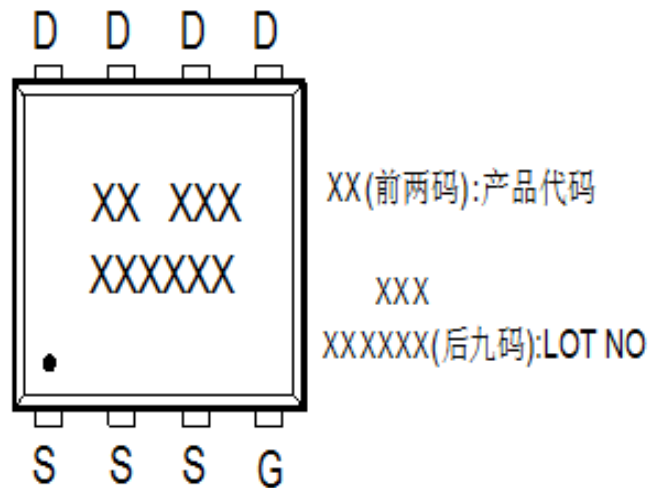
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	3	3.3	3.6	I	0.65	0.8	0.9
B	2.88	3	3.2	J	0.1	0.15	0.25
C	2.9	3	3.25	K	0.59		
D	2.29	2.45	2.69	L	0°	10°	12°
E	3	3.3	3.6	M	0.14	0.3	0.4
F	0	0.1	0.2	N	0.55	0.65	0.75
G	1.35	1.75	2.2	O		0.2	
H	0.15	0.3	0.55	P	0		0.2



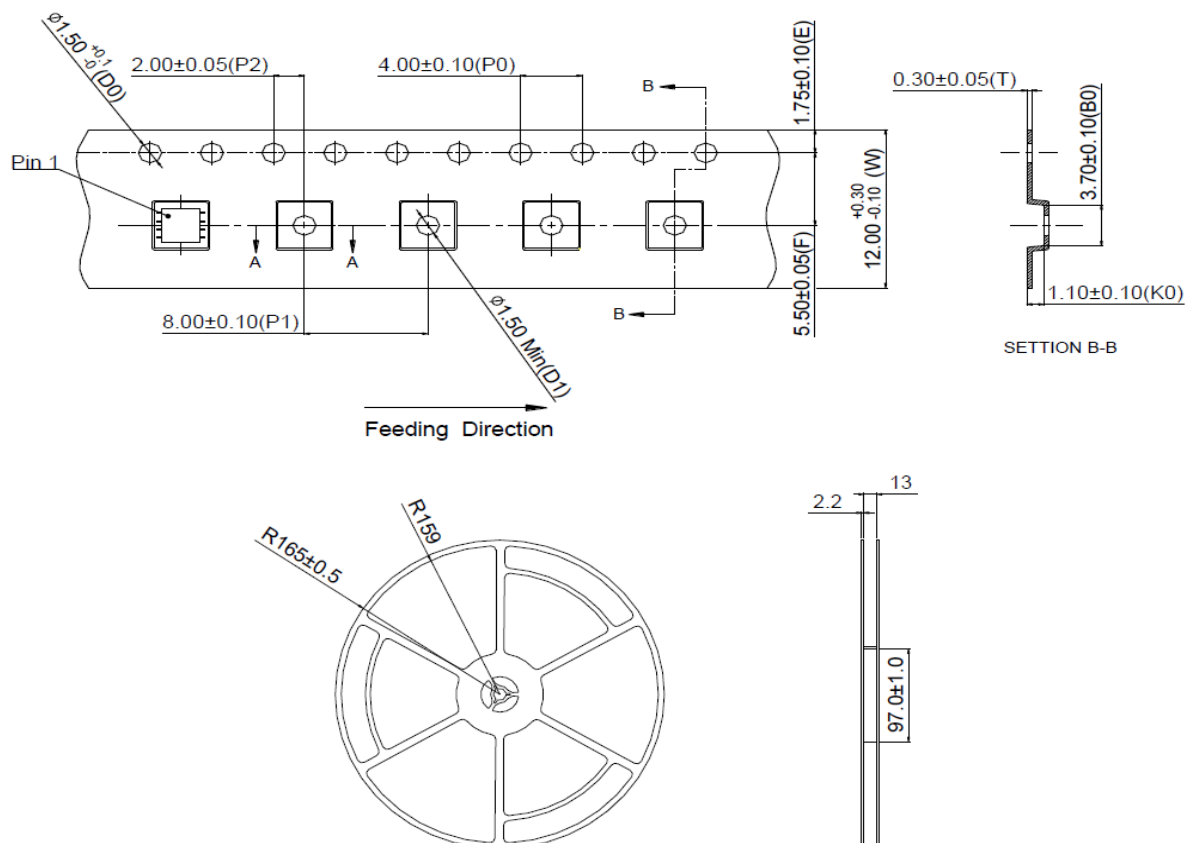
PE5E6BA

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A. Marking Information(此产品代码为: L3)



B. Tape&Reel Information:5000pcs/Reel

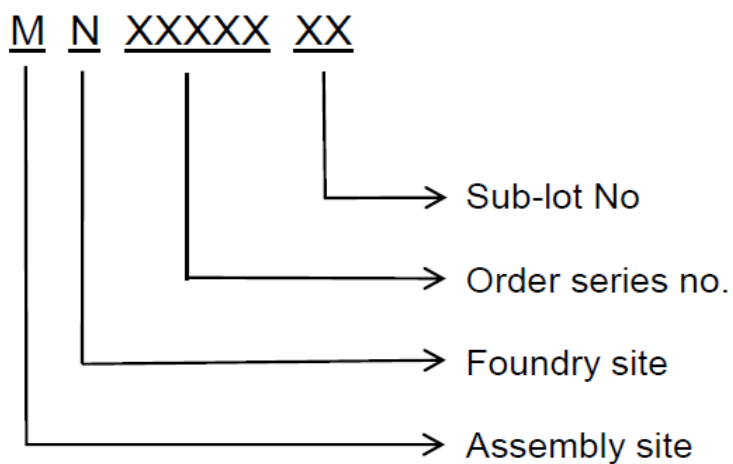


PE5E6BA

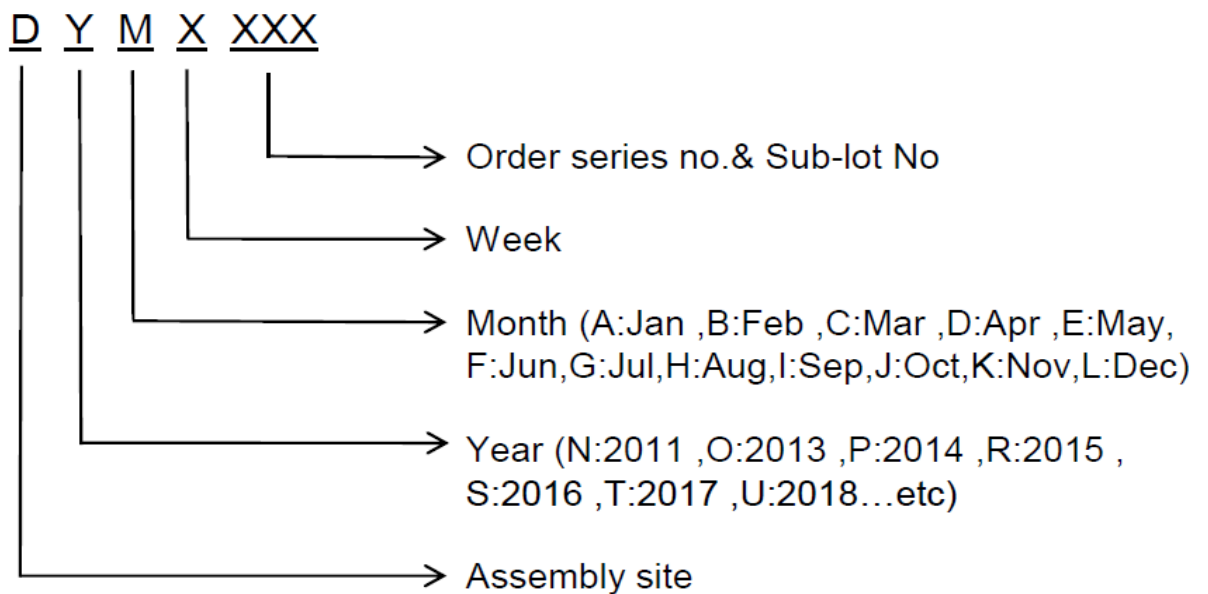
N-Channel Enhancement Mode MOSFET

C. Lot No.&Date Code rule

1.Lot No.



2.Date Code



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D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文"0"和数字"0", "G"和"Q"的字型即可)
3	U-NIKC	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	RoHS label	 long axis: 12 mm minor axis: 6 mm bottom color: White Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial
12	Scan information	Device / Lot / D/C / QTY , Insert " / " between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least