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# PF08114B

MOS FET Power Amplifier Module  
for E-GSM and DCS1800 Dual Band Handy Phone



ADE-208-1029B (Z)

Rev.2  
Dec. 2001

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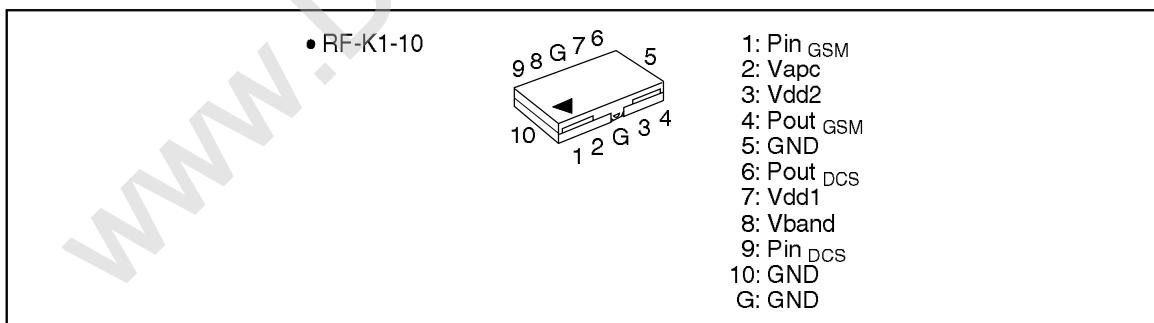
## Application

- Dual band amplifier for E-GSM (880 MHz to 915 MHz) and DCS1800 (1710 MHz to 1785 MHz)
- For 3.5 V nominal operation

## Features

- 2 in / 2 out dual band amplifier
- Simple external circuit including output matching circuit
- Simple power control
- 2stage amplifier : 10 dBm input Typ
- Lead less thin & small package :  $8 \times 12.3 \times 1.6$  mm Typ
- High efficiency : 54% Typ at 34.5 dBm for E-GSM  
: 52% Typ at 31.5 dBm for DCS1800

## Pin Arrangement



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### Absolute Maximum Ratings

(Tc = 25°C)

Item	Symbol	Rating	Unit
Supply voltage	Vdd	7 * <sup>1</sup>	V
Vapc voltage	Vapc	4.3 * <sup>2</sup>	V
Input power	Pin	15	dBm
Operating case temperature	Tc (op)	-25 to +100	°C
Storage temperature	Tstg	-30 to +100	°C

Notes: 1. This value is specified at no operation. (Vapc = 0 V)

2. This value is specified at no operation. (Vdd = 0 V)

At Vdd > 0, Vapc controlled, Idd = 0 to x A, where x = current at Pout = 34.5 dBm (@GSM), 31.5 dBm (@DCS), 50 Ω Load, Vdd = 3.5 V and Tcase = 25°C

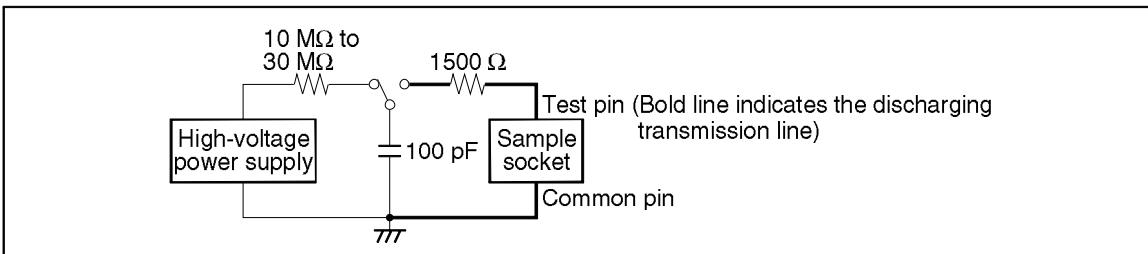
### Electrical Characteristics for DC

(Tc = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Drain cutoff current	Ids	—	—	10	μA	Vdd = 4.5 V, Vapc = 0 V, Vband = 0 V
		—	—	500	μA	Vdd = 4.5 V, Vapc = 0 V, Vband = 2 V
Vapc control current	Iapc	—	—	3	mA	Vdd = 3.5 V, Pin = 8 to 12 dBm, Pout = 34.5 dBm @GSM900 Pout = 31.5 dBm @DCS1800 Vapc controlled, Rg = Rl = 50 Ω

### ESD

Product quality guide level for ESD is 500 V at following test circuit.



**Electrical Characteristics for E-GSM mode**

(Tc = 25°C)

Test conditions unless otherwise noted:

Vdd1 = Vdd2 = 3.5 V, Pin = 8 to 10 dBm, Vband = 0 V, Rg = RL = 50 Ω, Tc = 25°C,

Pulse operation with pulse width 577 μs and duty cycle 1:8 shall be used.

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Frequency range	f	880	—	915	MHz	
Band select (GSM active)	Vband	0.0	—	0.2	V	
Input power	Pin	8	10	12	dBM	
Supply voltage	Vdd	2.9	3.5	4.5	V	
Total efficiency	η <sub>T</sub>	45	54	—	%	Pout <sub>GSM</sub> = 34.5dBm, Vapc = controlled
2nd harmonic distortion	2nd H.D.	—	-50	-41.5	dBc	
3rd harmonic distortion	3rd H.D.	—	-50	-41.5	dBc	
Input VSWR	VSWR (in)	—	1.5	3	—	
Output power (1)	Pout (1)	34.5	35.0	—	dBm	Vapc = 2.2V
Output power (2)	Pout (2)	32.9	33.5	—	dBm	Vdd = 2.9V, Vapc = 2.2V, Tc = +90°C
Isolation	—	—	-40	-25	dBm	Vapc = 0.2 V, Pin = 12dBm
Isolation at DCS RF-output when GSM is active	—	—	-30	-20	dBm	Pout <sub>GSM</sub> = 34.5dBm, Measured at f = 1760 to 1830MHz
Switching time	t <sub>s</sub> , t <sub>r</sub>	—	1	2	μs	Pout <sub>GSM</sub> = -10 to 34.5dBm, t = 90%
Stability	—	No parasitic oscillation > -36 dBm			—	All combinations of the following parameters: Vapc controlled *, Pin = min to max, Vdd = 2.9 to 4.5V, Tcase = -20 to 90°C, Load VSWR = 7.5 : 1, All phase angles
Load VSWR tolerance	—	No degradation			—	All combinations of the following parameters: Vapc controlled *, Pin = min to max, Vdd = 2.9 to 4.5V, Tcase = -20 to 90°C, Load VSWR = 7.5 : 1, All phase angles

Note: 1. Id = 0 A to x A, where x = current at Pout = 34.5 dBm, 50 Ω load, Vdd = 3.5 V and Tcase = 25°C.  
Vapc can range from 0.2 V to 4.3 V to control Id.

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### Electrical Characteristics for DCS1800 mode

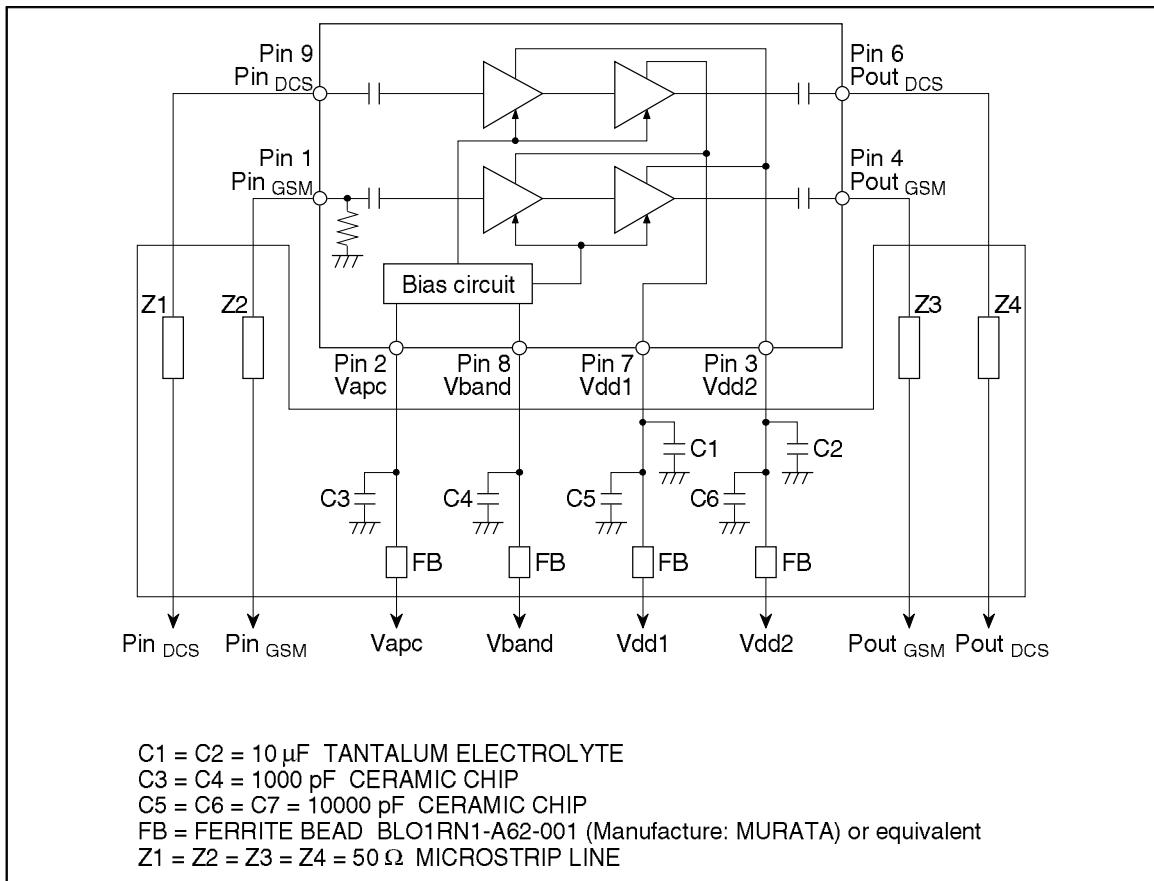
(Tc = 25°C)

Test conditions unless otherwise noted:

Vdd1 = Vdd2 = 3.5 V, Pin = 8 to 10 dBm, Vband = 2 V, Rg = RL = 50 Ω, Tc = 25°C,  
Pulse operation with pulse width 577 μs and duty cycle 1:8 shall be used.

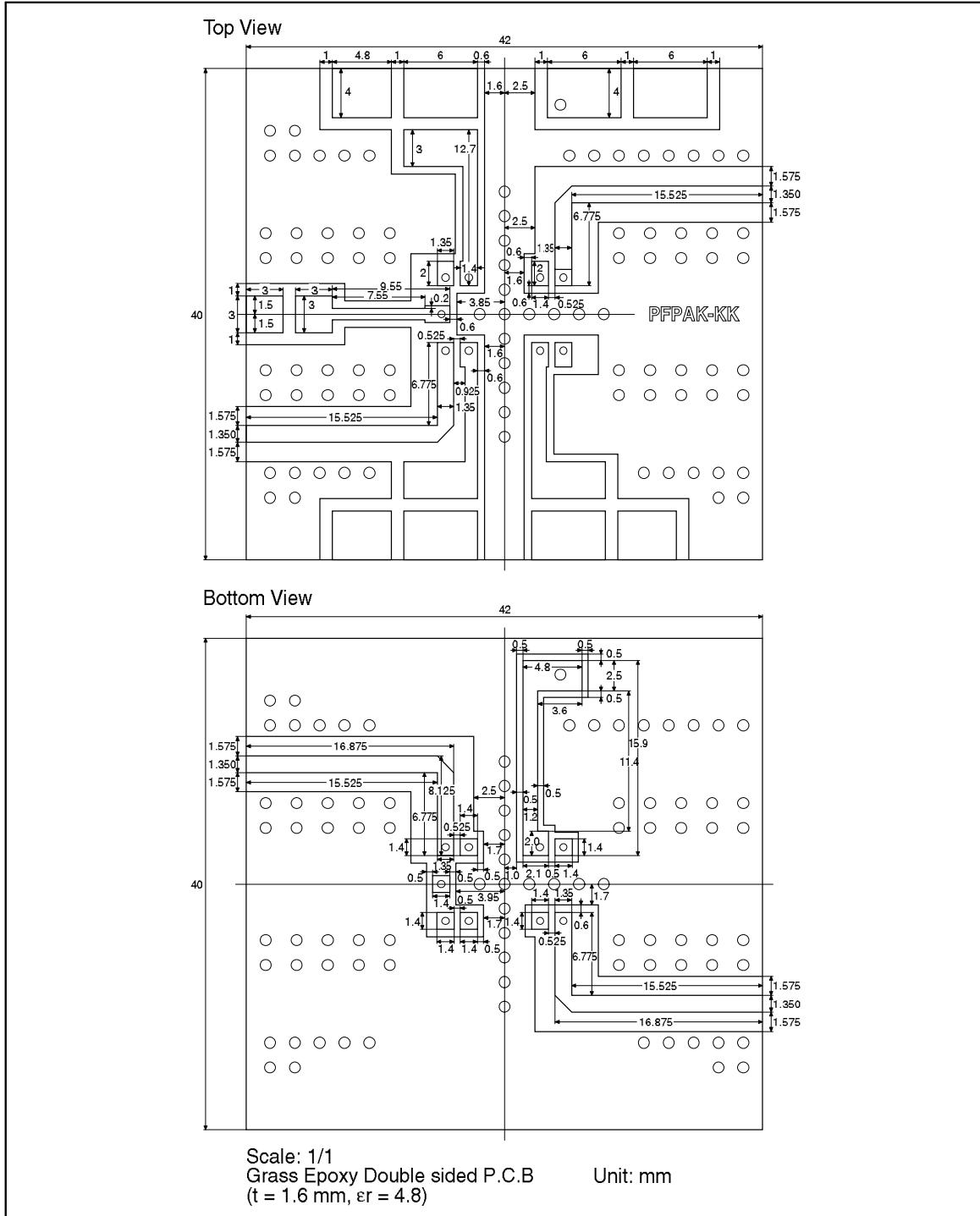
Item	Symbol	Min	Typ	Max	Unit	Test Condition
Frequency range	f	1710	—	1785	MHz	DCS1800 (1710 to 1785MHz)
Band select (DCS active)	Vctl	1.9	—	2.9	V	
Input power	Pin	8	10	12	dBM	
Supply voltage	Vdd	2.9	3.5	4.5	V	
Total efficiency	η <sub>T</sub>	45	52	—	%	Pout <sub>DCS</sub> = 31.5dBm, Vapc = controlled
2nd harmonic distortion	2nd H.D.	—	-50	-38.5	dBc	
3rd harmonic distortion	3rd H.D.	—	-50	-38.5	dBc	
Input VSWR	VSWR (in)	—	1.5	3	—	
Output power (1)	Pout (1)	31.5	32.5	—	dBm	Vapc = 2.2V
Output power (2)	Pout (2)	30.0	31.0	—	dBm	Vdd = 2.9V, Vapc = 2.2V, Tc = +90°C
Isolation	—	—	-42	-36	dBm	Vapc = 0.2 V
Switching time	t <sub>s</sub> , t <sub>r</sub>	—	1	2	μs	Pout <sub>DCS</sub> = -10 to 31.5dBm, t = 90%
Stability	—	No parasitic oscillation > -36 dBm			—	All combinations of the following parameters: Vapc controlled *, Pin = min to max, Vdd = 2.9 to 4.5V, Tcase = -20 to 90°C, Load VSWR = 7.5 : 1, All phase angles
Intermodulation	—	—	-59	-52	dBc	Pout = 31.5dBm, Pinterferer at output, Fo + 3MHz at -11.5dBm, Measure Fo - 3MHz, RBW = 300kHz
Load VSWR tolerance	—	No degradation			—	All combinations of the following parameters: Vapc controlled *, Pin = min to max, Vdd = 2.9 to 4.5V, Tcase = -20 to 90°C, Load VSWR = 7.5 : 1, All phase angles

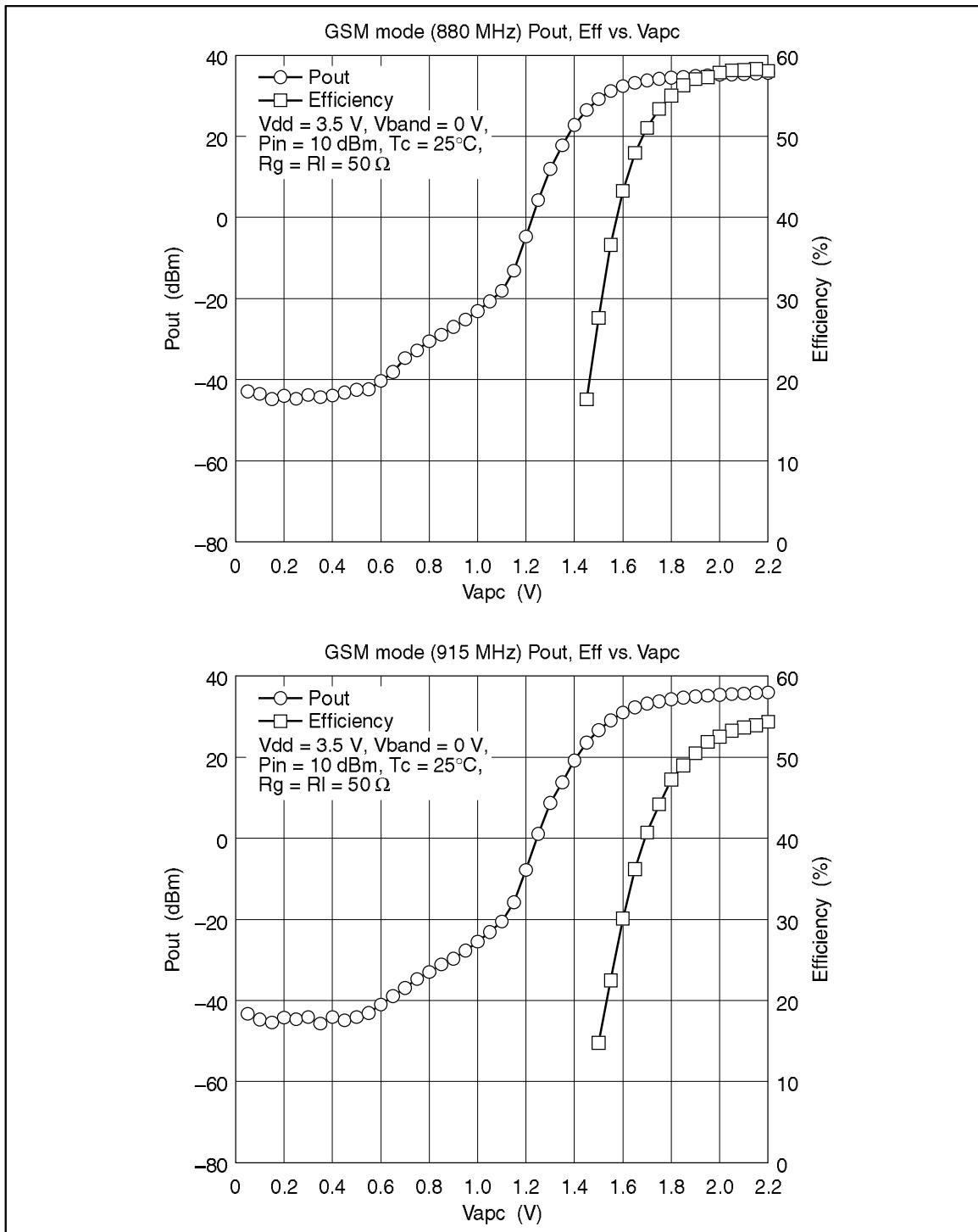
Note: 1. Id = 0 A to x A, where x = current at Pout = 31.5 dBm, 50 Ω load, Vdd = 3.5 V and Tcase = 25°C.  
Vapc can range from 0.2 V to 4.3 V to control Id.

**Internal Diagram and External Circuit**

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### Test Fixture Pattern

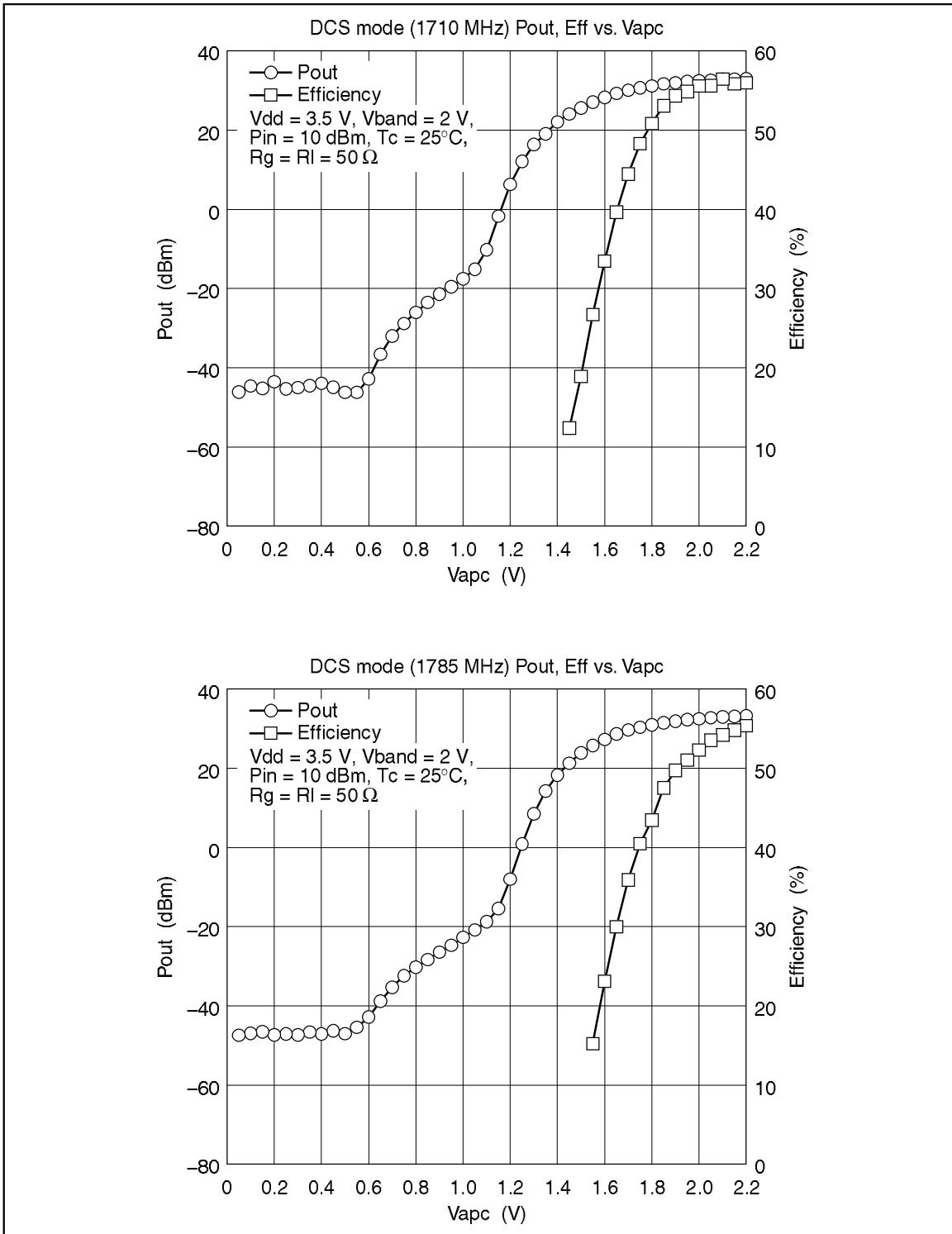


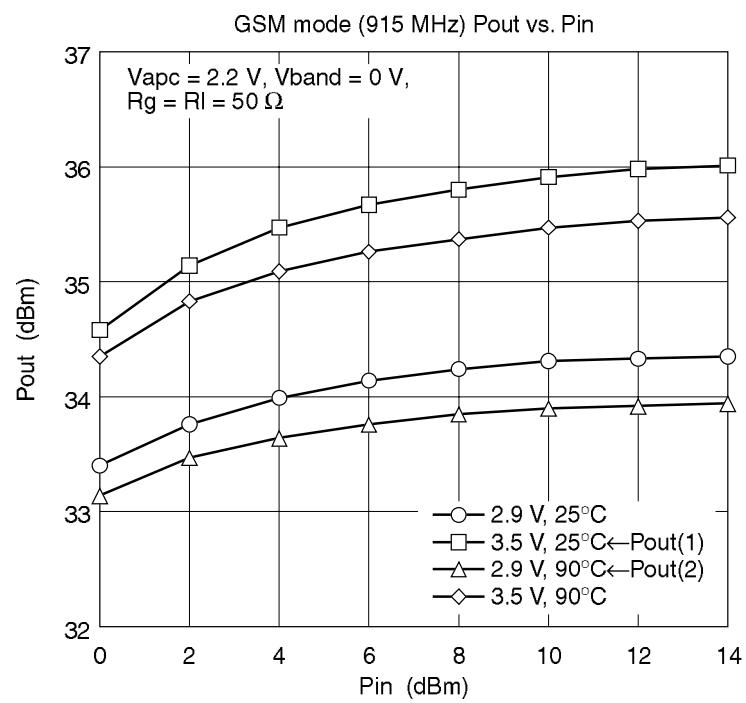
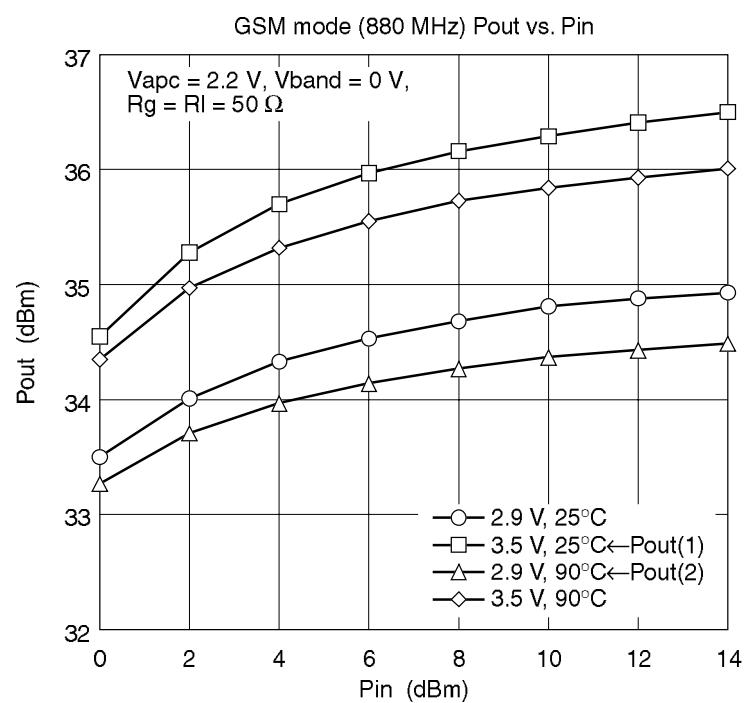
**Characteristic Curves**

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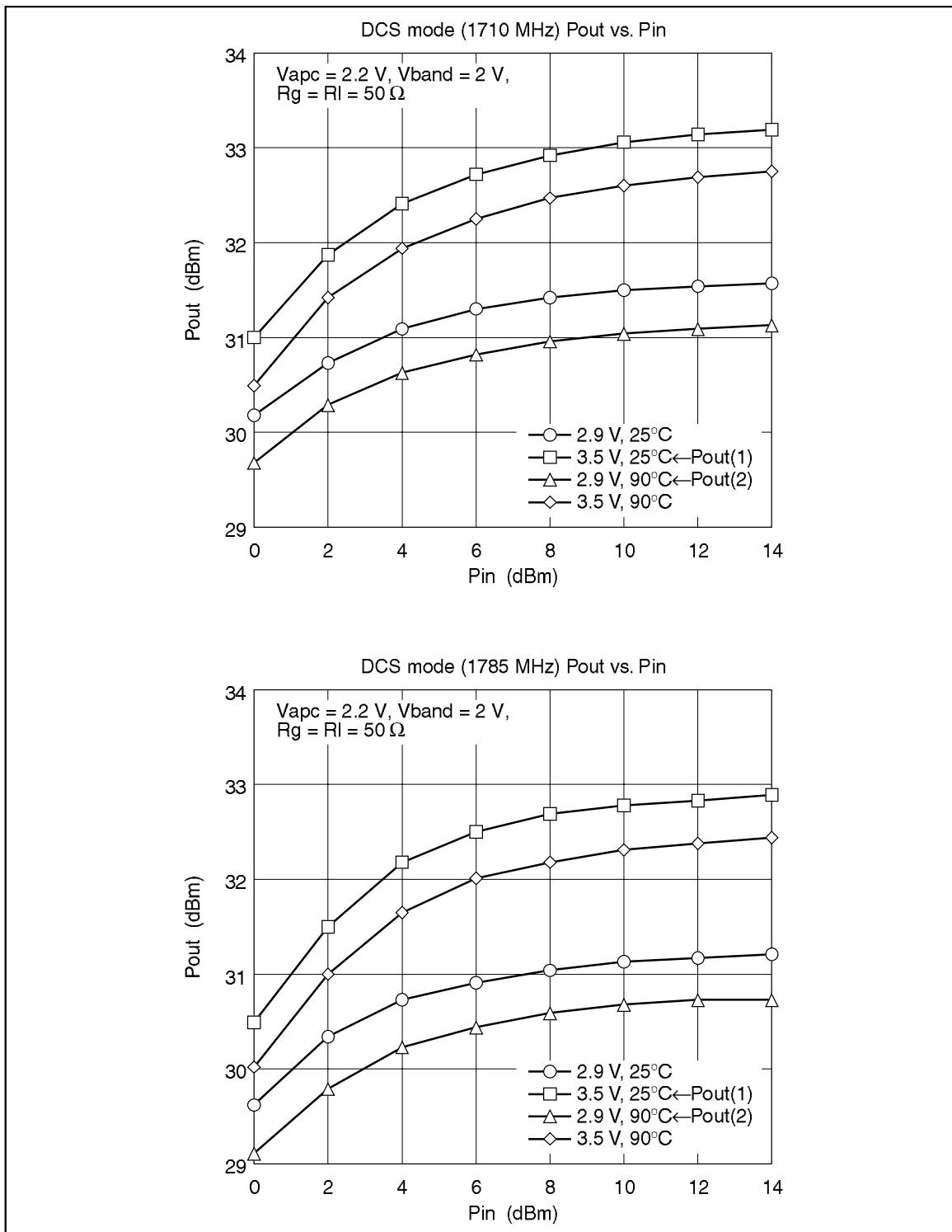


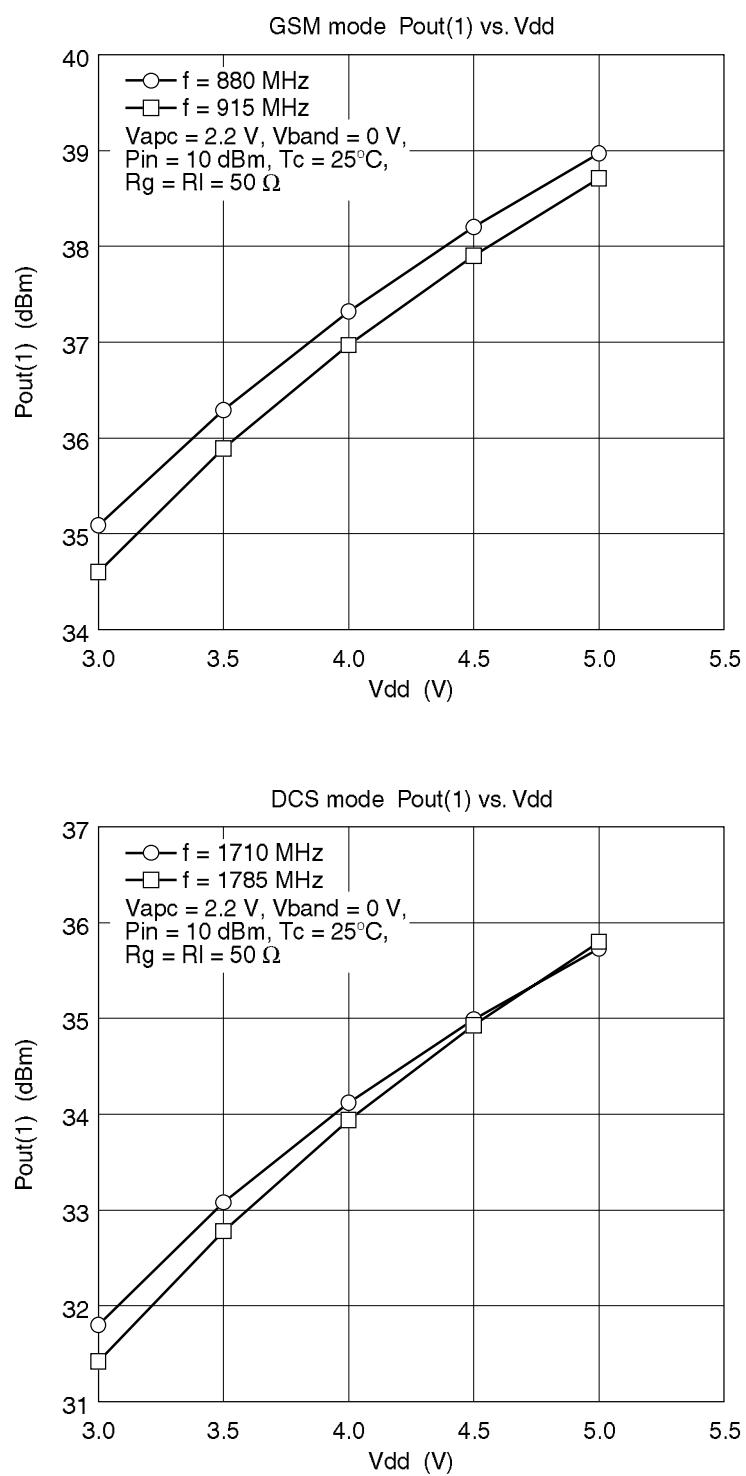


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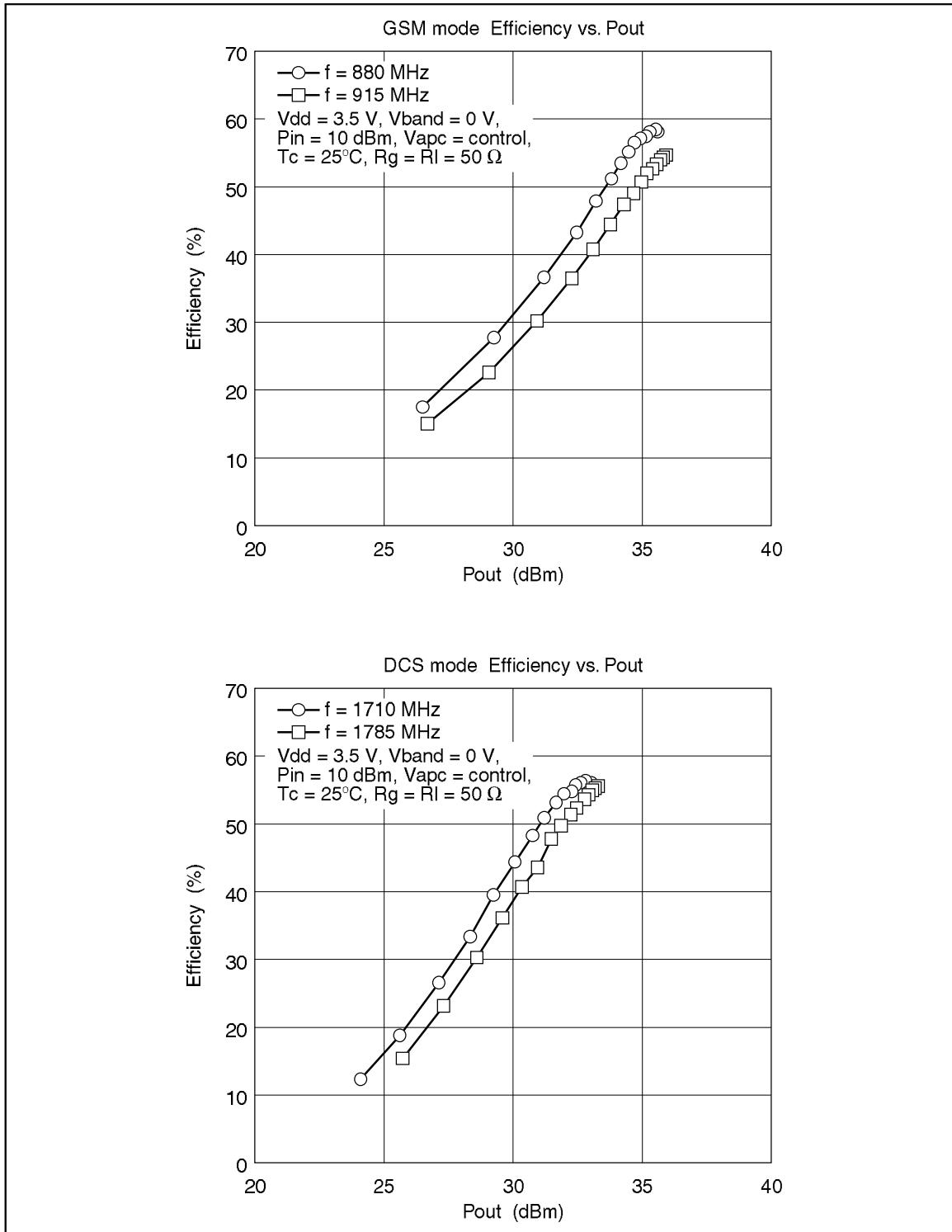


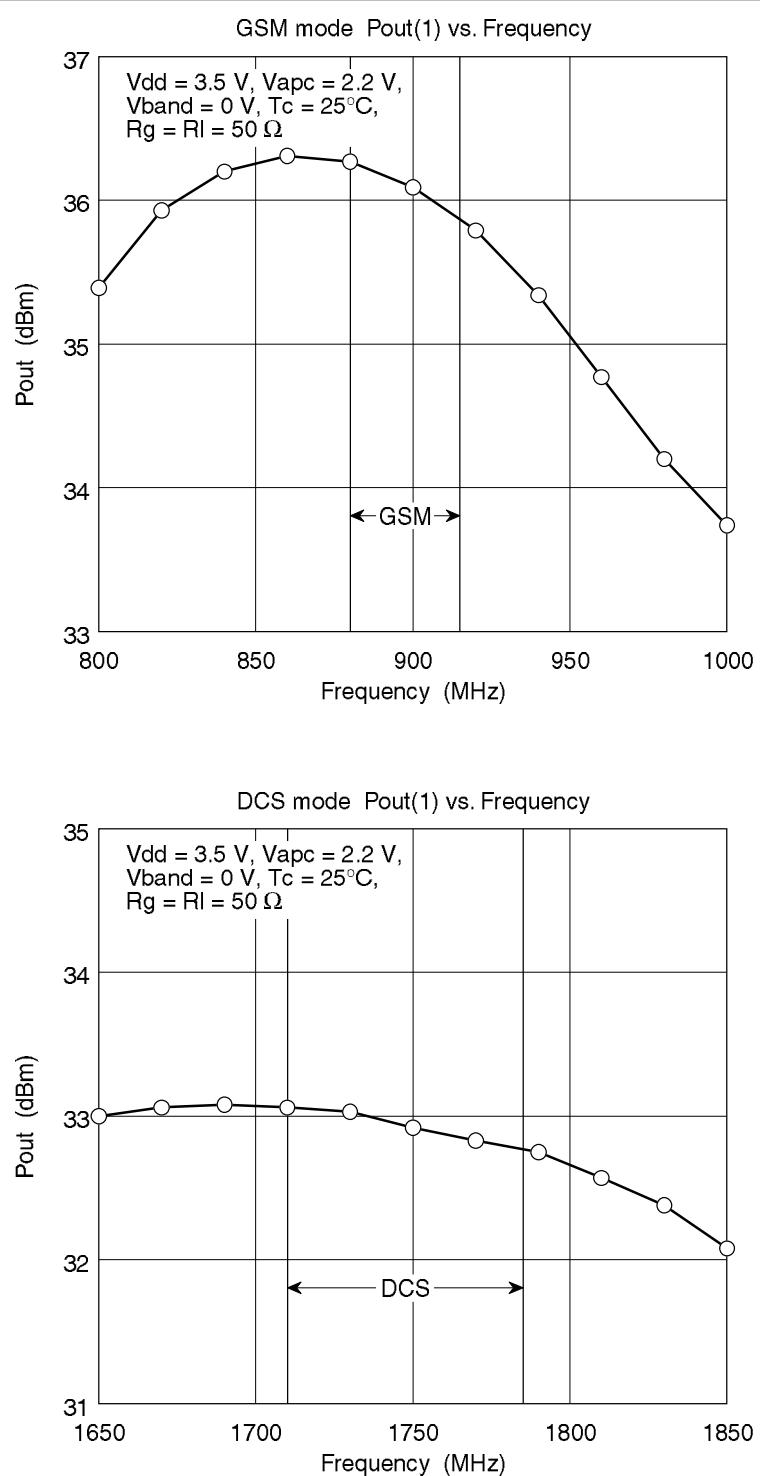


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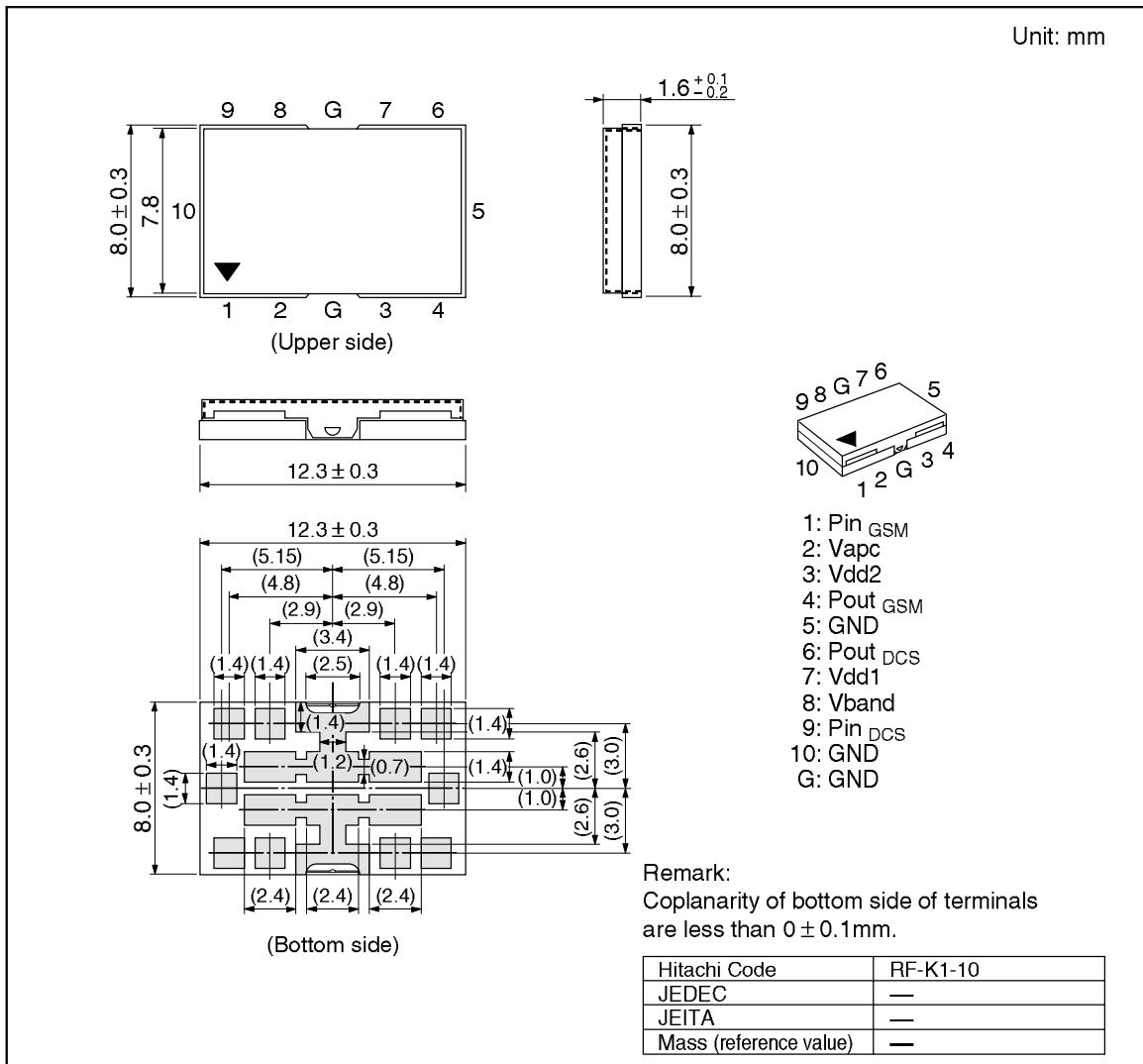
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### Package Dimensions



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