

November 2013

# FQPF7N60

# N-Channel QFET $^{\mathbb{R}}$ MOSFET 600 V, 4.3 A, 1 $\Omega$

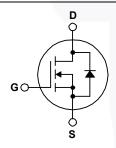
### **Description**

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

### **Features**

- 4.3 A, 600 V,  $R_{DS}(on)$  = 1.0  $\Omega$  (Max.) @  $V_{GS}$  = 10 V,  $I_D$  = 2.2 A
- · Low Gate Charge (Typ. 29 nC)
- Low C<sub>rss</sub> (Typ. 16 pF)
- · 100% Avalanche Tested





# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

	O v			
Symbol	Parameter		FQPF7N60	Unit
$V_{DSS}$	Drain-Source Voltage		600	V
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C	;)	4.3	Α
	- Continuous (T <sub>C</sub> = 100°	C)	2.7	Α
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	17.2	A
V <sub>GSS</sub>	Gate-Source Voltage		± 30	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	580	mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	4.3	A
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	4.8	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
$P_{D}$	Power Dissipation (T <sub>C</sub> = 25°C)		48	W
	- Derate above 25°C		0.38	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Rang	e	-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C

### **Thermal Characteristics**

Symbol	Parameter	FQPF7N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	2.60	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

# **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQPF7N60	FQPF7N60	TO-220F	Tube	N/A	N/A	50 units

### **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted.

Parameter	Test Conditions	Min	Тур	Max	Unit
racteristics					
Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	600			V
Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		0.67		V/°C
Zara Cata Valtaga Drain Current	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V			10	μΑ
Zero Gate voltage Drain Current	V <sub>DS</sub> = 480 V, T <sub>C</sub> = 125°C	-		100	μΑ
Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V	-		100	nA
Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V			-100	nA
	Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient  Zero Gate Voltage Drain Current Gate-Body Leakage Current, Forward	tracteristics         Drain-Source Breakdown Voltage $V_{GS} = 0 \text{ V}$ , $I_D = 250 \text{ μA}$ Breakdown Voltage Temperature Coefficient $I_D = 250 \text{ μA}$ , Referenced to 25°C         Zero Gate Voltage Drain Current $V_{DS} = 600 \text{ V}$ , $V_{GS} = 0 \text{ V}$ $V_{DS} = 480 \text{ V}$ , $V_{CS} = 125 \text{ C}$ Gate-Body Leakage Current, Forward $V_{GS} = 30 \text{ V}$ , $V_{DS} = 0 \text{ V}$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 2.2 \text{ A}$		0.8	1.0	Ω
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 50 \text{ V}, I_{D} = 2.2 \text{ A}$		6.4		S

## **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,	 1100	1430	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz	 135	175	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		 16	21	pF

# **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 300 V, I <sub>D</sub> = 7.4 A,		30	70	ns
t <sub>r</sub>	Turn-On Rise Time	$R_G = 25 \Omega$		80	170	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			65	140	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note	4)	60	130	ns
$Q_g$	Total Gate Charge	VDS 400 V, ID 7.47V,		29	38	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10 V	A	7		nC
Q <sub>gd</sub>	Gate-Drain Charge	(Note	4)	14.5	/	nC

### **Drain-Source Diode Characteristics and Maximum Ratings**

I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		 	4.3	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		 /	17.2	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V, } I_{S} = 4.3 \text{ A}$	 J/	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 7.4 A,	 320		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> / dt = 100 A/μs	 2.4		μC

- Notes: Notes: Notes: A Repetitive Rating: Pulse width limited by maximum junction temperature. 2. L = 57.6 mH,  $I_{AS}$  = 4.3 A,  $V_{DD}$  = 50 V,  $R_{G}$  = 25  $\Omega$ , starting  $T_{J}$  = 25°C. 3.  $I_{SD}$  ≤ 7.4 A, di/dt ≤ 200 A/µs,  $V_{DD}$  ≤ B  $V_{DSS}$ , starting  $T_{J}$  = 25°C. 4. Essentially independent of operating temperature.

# **Typical Characteristics**

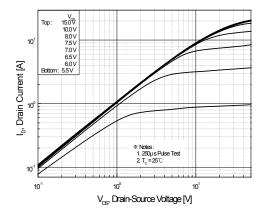


Figure 1. On-Region Characteristics

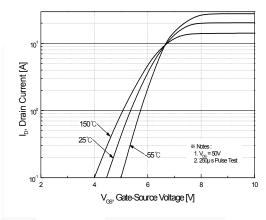


Figure 2. Transfer Characteristics

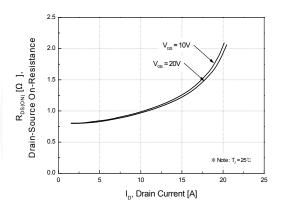


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

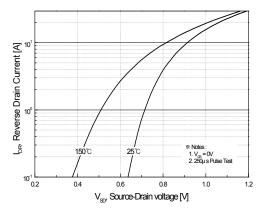


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

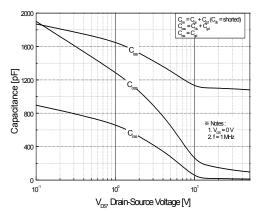


Figure 5. Capacitance Characteristics

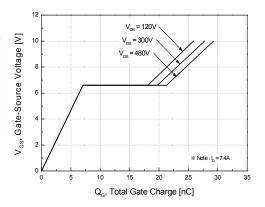


Figure 6. Gate Charge Characteristics

# **Typical Characteristics**

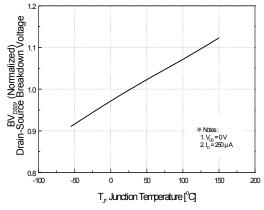
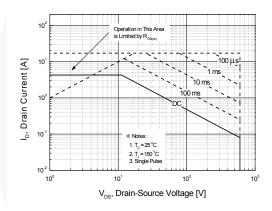


Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature



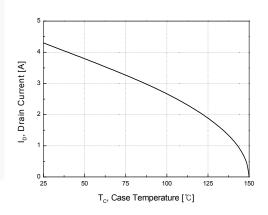


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

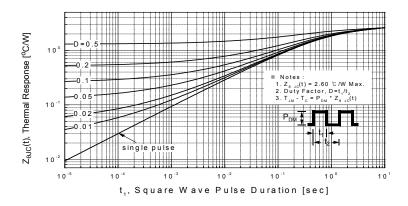


Figure 11. Transient Thermal Response Curve



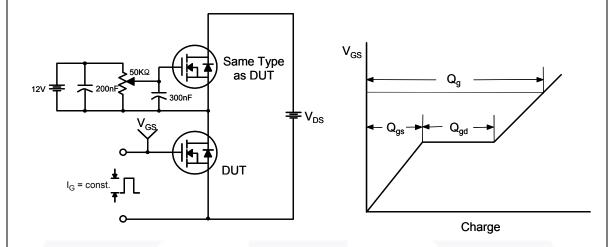


Figure 13. Resistive Switching Test Circuit & Waveforms

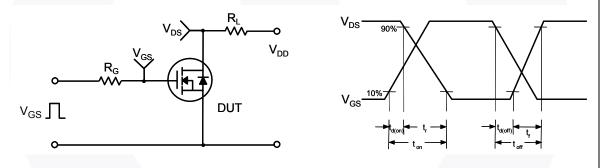
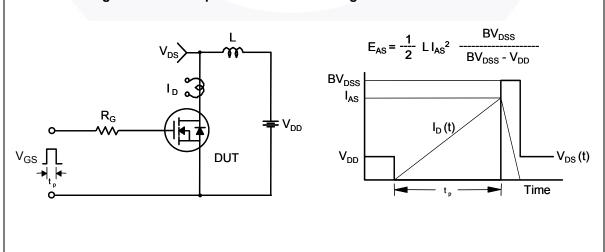
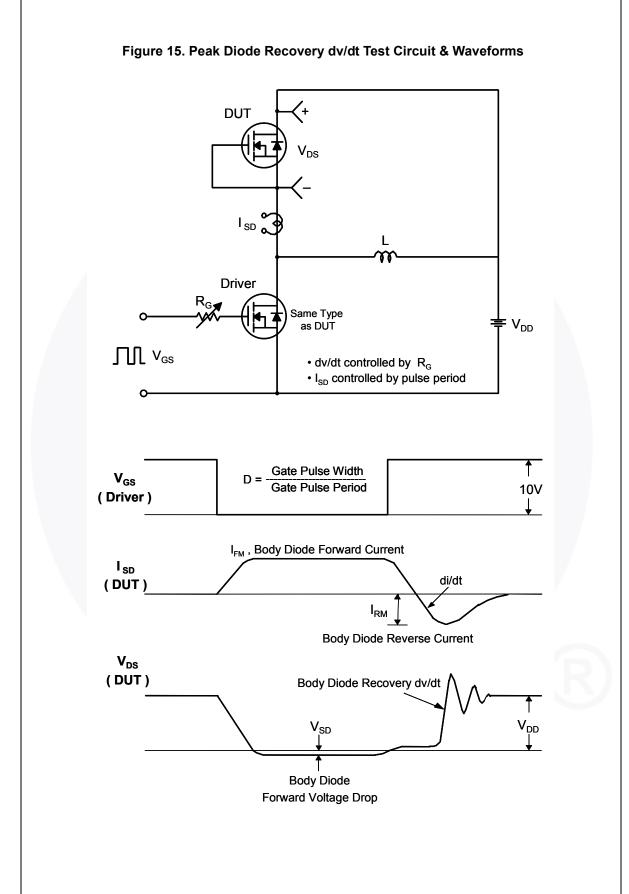


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms





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### **Mechanical Dimensions**

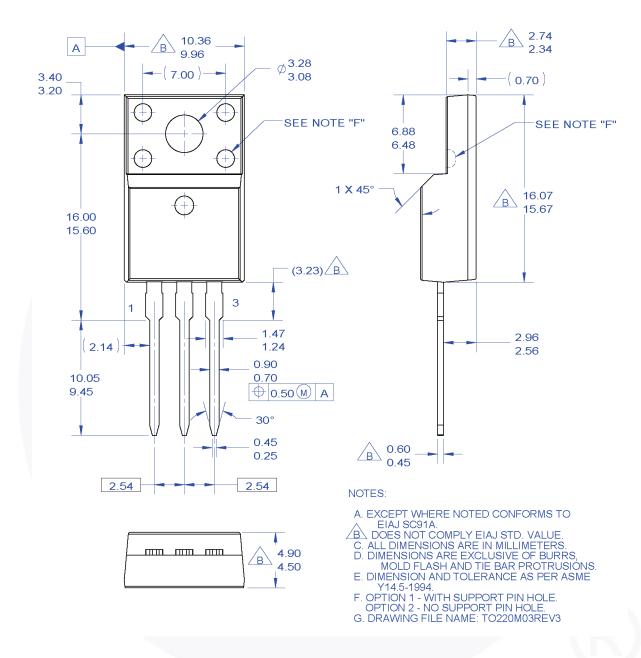


Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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