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CUSTOMER . CDE016

SAMPLE CODE (Ver.) PS320240WRF-DBAH01 (Ver.0)

MASS PRODUCTION CODE (Ver.) . PG320240WRFDBAHYZQ

DRAWING NO. (Ver.) . PG-03100-164 (Ver.0)

# **Customer Approved**

Date:

Approved	QC Confirmed	Designer
A. 第一7		林斯 乾~~

- Approval For Specifications Only.
  - \* This specification is subject to change without notice.

Please contact Powertip or it's representative before designing your product based on this specification.

Approval For Specifications and Sample.

## POWERTIP TECH. CORP.

#### Headquarters:

No.8, 6<sup>th</sup> Road, Taichung Industrial Park,

Taichung, Taiwan

台中市 407 工業區六路 8號

TEL: 886-4-2355-8168 FAX: 886-4-2355-8166 E-mail: <a href="mailto:sales@powertip.com.tw">sales@powertip.com.tw</a>

Http://www.powertip.com.tw

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# **RECORDS OF REVISION**

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Total: 22Page



#### **Contents**

### 1. SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
- 1.6 Backlight Characteristics

#### 2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics

#### 3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

### 4. RELIABILITY TEST

4.1 Reliability Test Condition

#### 5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty
- 6. PACKING SPECIFICATION

**Appendix: 1. LCM Drawing** 

2. Package

Note: For detailed information please refer to IC data sheet: RAIO RA8835P3N



## 1. SPECIFICATIONS

#### 1.1 Features

Item	Standard Value
Display Type	320 * 240 Dots
LCD Type	FSTN, Positive , Transflective
Driver Condition	LCD Module: 1/240 Duty, 1/16 Bias
Viewing Direction	6 O'clock
Backlight	LED B/L
Weight	280 g
Interface	8080 series and 8 bit parallel data input
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web side :
	http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

	.01.0			
Item	Item Standard Value			
Outline Dimension	153.54 (L) * 120.24 (w) * 21.0 (H)(Max)			
Viewing Area	120.14 (L) * 92.14 (w)	mm		
Active Area	115.17 (L) * 86.37 (w)	mm		
Dot Size	0.33 (L) * 0.33 (w)	mm		
Dot Pitch	0.36 (L) * 0.36 (w)	mm		

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	$V_{DD}$	_	-0.3	+7.0	V
LCD Driver Supply Voltage	V <sub>DD</sub> -V <sub>EE</sub>	-	-0.3	32	V
Input Voltage	V <sub>IN</sub>	_	-0.3	V <sub>DD</sub> +0.3	V
Operating Temperature	T <sub>OP</sub>	_	-20	70	°C
Storage Temperature.	T <sub>ST</sub>	_	-30	80	°C
Storage Humidity	H <sub>D</sub>	Ta<40 °C	20	90	%RH



## 1.4 DC Electrical Characteristics

 $V_{DD}$  = 5  $\pm 0.5 V$  ,  $V_{SS}$  = 0V , Ta = 25°C

					33 • .	
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	$V_{DD}$	_	4.5	5	5. 5	V
"H" Input Voltage	V <sub>IH</sub>	_	0.5 V <sub>DD</sub>	-	$V_{DD}$	V
"L" Input Voltage	V <sub>IL</sub>	_	V <sub>SS</sub>	-	0.2V <sub>DD</sub>	V
"H" Output Voltage	V <sub>OH</sub>	_	2.4	-	-	V
"L" Output Voltage	V <sub>OL</sub>	_	-	-	V <sub>SS</sub> +0.4	V
Supply current	I <sub>DD</sub>	V <sub>DD</sub> = 5.0 V	-	-	90	mA
	Vop	-20°C	-	-	-	
LCM driving voltage	( VOP+~VOP-)	25°C	22	22.2	22.4	V
	( VOF+~ VOP-)	70°C	-	_	-	

# 1.5 Optical Characteristics

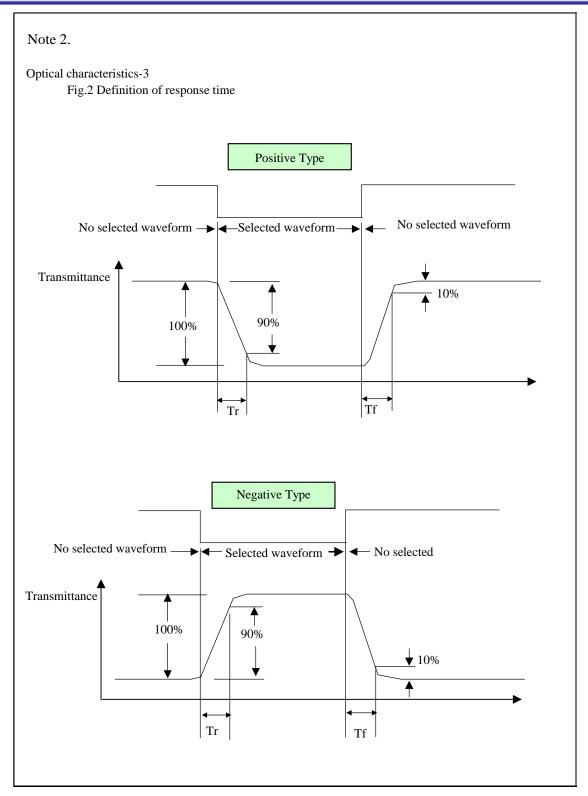
LCD Panel: 1/240 Duty, 1/15 Bias,  $V_{LCD} = 22.0$  V,  $Ta = 25^{\circ}C$ 

Item	Symbol	Conditions	Min.	Тур.	Max.	Reference	
View Angle	θ	C <u>&gt;</u> 2.0, ∅ = 270°	-40°	-	40°	Notes 1	
Contrast Ratio	С	θ =-5°, Ø = 270°	2	3	-	Note 3	
Response Time(rise)	tr	θ =-5°, Ø = 270°	-	120 ms	180 ms	Note 2	
Response Time(fall)	tf	θ =-5°, Ø = 270°	-	290 ms	435 ms	Note 2	



# Note 1. Optical characteristics-2 Viewing angle Rear (12H) Top ( $\theta$ =0°) $\varphi = 90^{\circ}$ $\theta L$ Left (9H) Right (3H) $\varphi = 180^{\circ}$ $\varphi = 0^{\circ}$ Front (6H) $\varphi$ =270° Viewing angle







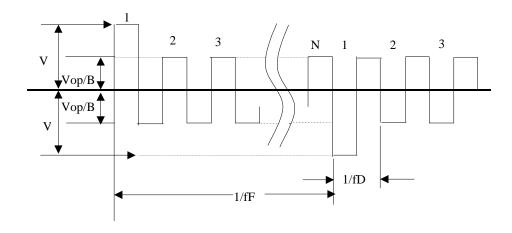
#### Electrical characteristics-2

※2 Drive waveform

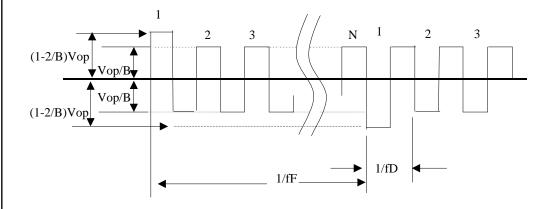
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

#### (1) Selected waveform



#### (2) Non-Selected waveform

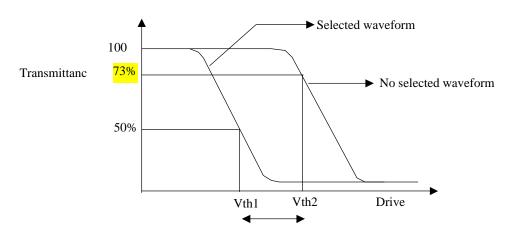


Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period







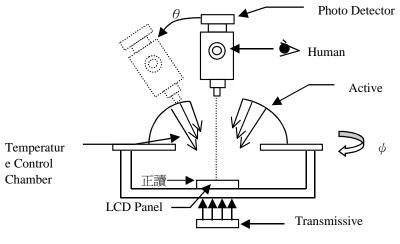
Active voltage range

	Vth1	Vth2
View direction	10°	40 °
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

#### **※**1 Contrast ratio

= (Brightness in OFF state) / (Brightness in ON state)

#### Outline of Electro-Optical Characteristics Measuring System





# 1.6 Backlight Characteristics

LCD Module with LED Backlight

## Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	I <sub>F</sub>	Ta =25°C	-	160	mA
Reverse Voltage	$V_R$	Ta =25°C	-	5	V
Power Dissipation	P <sub>D</sub>	Ta =25°C	-	0.67	W

## **Electrical / Optical Characteristics**

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	V <sub>F</sub>	IF= 160 mA	-	3.7	4.2	V
Reverse Current	I <sub>R</sub>	VR= 5 V	-	-	10	uA
Average Brightness (with LCD)	I <sub>V</sub>	IF=160mA	20	35	-	cd/m <sup>2</sup>
CIE Color Coordinate	X	IF= 160mA	0.25	0.31	0.37	-
(With LCD)	Y			0.34	0.40	-
Backlight Uniformity (with LCD)	∆В	IF= 160mA	70	-	-	%
Color			White			

Note: △B=(Min/Max)100%



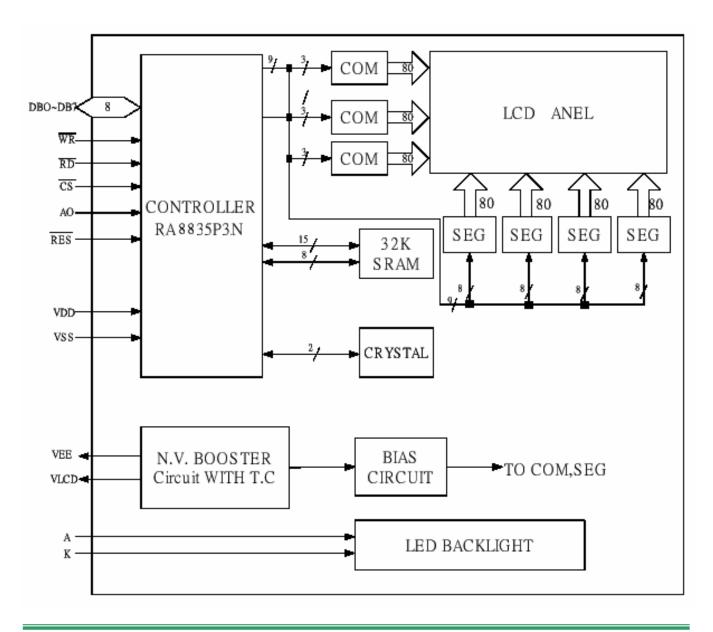
## 2. MODULE STRUCTURE

## 2.1 Counter Drawing

## 2.1.1 LCM Mechanical Diagram

\* See Appendix

## 2.1.2 Block Diagram





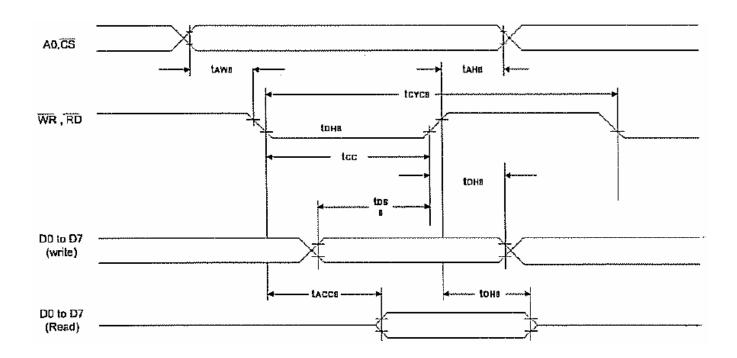
# 2.2 Interface Pin Description

Pin No.	Symbol	Function
1	V <sub>SS</sub>	Ground (V <sub>SS</sub> =0 V)
2	$V_{DD}$	Power Supply (V <sub>DD</sub> = 5.0 V)
3	$V_{LCD}$	Operating voltage for LCD.
4	/RD	Data read (read data from the module at "L")
5	WR	Data write (write data to the module at "L")
6	A0	Command / Data read or write select (H : command L : data)
7	DB0	Data bus bit 0
8	DB1	Data bus bit 1
9	DB2	Data bus bit 2
10	DB3	Data bus bit 3
11	DB4	Data bus bit 4
12	DB5	Data bus bit 5
13	DB6	Data bus bit 6
14	DB7	Data bus bit 7
15	/CS	Chip select, active "L"
16	/RES	Reset input , active "L"
17	$V_{EE}$	Negative voltage out .
18	FG	Frame ground (connected to metal bezel)
19	NC	Not connection
20	NC	Not connection
-	А	Power supply for LED backlight Anode input
-	K	Power supply for LED backlight cathode input

Built in negative voltage generator circuit and temperature compensation circuit. Built in Timing mode for 8080 family.



## 2.3 Timing Characteristics



 $Ta = -20 \text{ to } 75^{\circ}C$ 

Signal	Symbol	Parameter	V <sub>DD</sub> = 4.5	to 5.5V	$V_{DD} = 2.7$	to 4.5V	Unit	Condition	
Signal		raidifietei	Min.	Max.	Min.	Max.	- Onne	Condition	
	t <sub>AHB</sub>	Address hold time	10	-	10	–	ΠŞ		
A0, CS	t <sub>AW8</sub>	Address setup time	0	-	0		ns		
WR.	t <sub>CYC8</sub>	System cycle time	note.		note.		ns		
RD	tcc	Strobe pulse width	120		150		ns	CL=	
	t <sub>DSB</sub>	Data setup time	120		120		ns	100pF	
D0.1- D7	t <sub>DH8</sub>	Data hold time	5	<u> </u>	5	_	ns		
D0 to D7	t <sub>ACC8</sub>	RD access time	<u> </u>	50	_	80	ns		
	t <sub>OH8</sub>	Output disable time	10	50	10	55	ns		

Note: For memory control and system control commands:

 $t_{CYC8} = 2t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$ 

For all other commands:

 $t_{\rm CYC8} = 4t_{\rm C} + t_{\rm CC} + 30$ 



## 2.4 Display Command

Class	Command	Code									Hex	Command description		
Class		RD	WR	A0	D7	D6	D5	D4	D3	D2	D1	D0	пех	Command description
System	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	40	Initialize device and display
control	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	53	Enter standby mode
	DISP ON/OFF	1	0	1	0	1	0	1	1	0	0	D	58. 59	Enable and disable display and display flashing
	SCROLL	1	0	1	0	1	0	0	0	1	0	0	44	Set display start address and display regions
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	5D	Set cursor type
	CGRAM ADR	1	0	1	0	1	0	1	1	1	0	0	5C	Set start address of character generator RAM
Display control	CSRDIR	1	0	1	0	1	0	0	1	1	CD 1	CD 0	4C to 4F	Set direction of cursor movement
	HDOT SCR	1	0	1	0	1	0	1	1	0	1	0	5A	Set horizontal scroll position
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	5B	Set display overlay format
	GRAY	1 0			•		4	•	•	•	_		40	Out on any or all a Manday and de
	SCALE			1	0	1	1	0	0	0	0	0	40	Setup grayscale display mode
Drawing	CSRW	1	0	1	0	1	0	0	0	1	1	0	46	Set cursor address
control	CSRR	1	0	1	0	1	0	0	0	1	1	1	47	Read cursor address
Memory	MWRITE	1	0	1	0	1	0	0	0	0	1	0	42	Write to display memory
control	MRAD	1	0	1	0	1	0	0	0	0	1	1	43	Read from display memory

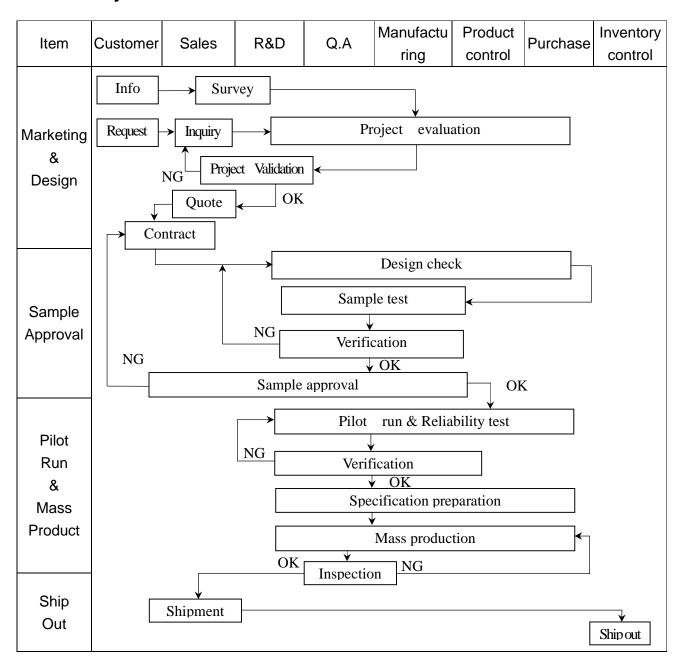
#### Notes

- 1. In general, the internal registers of the SED 13700 series are modified as each command parameter is input. However, the microprocessor does not have to set all the parameters of a command and may send a new input will have been changed but the remaining parameter registers are unchanged.
  - 2-byte parameters (where two bytes are treated as 1 data item) are handled as follows:
  - a. CSRW, CSRR: Each byte is processed individually. The microprocessor may read or write just the low byte of the cursor address.
  - b. SYSTEM SET, SCROLL, CGRAM ADR: Both parameter bytes are processed together. If the command is changed after half of the parameter has been input, the single byte is ignored.
- 2. APL and APH are 2-byte parameters, but are treated as two 1-byte parameters.

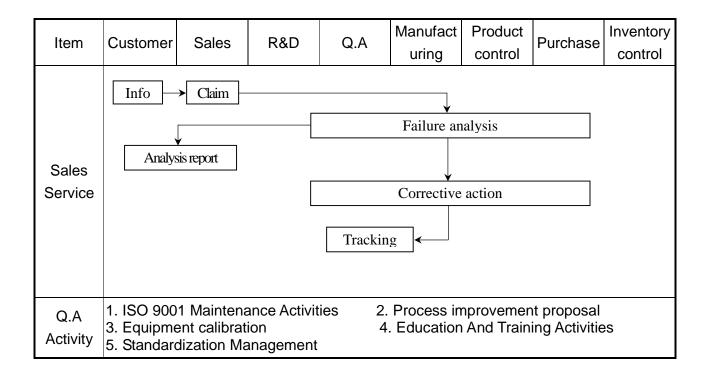


## 3. QUALITY ASSURANCE SYSTEM

## 3.1 Quality Assurance Flow Chart









## 3.2 Inspection Specification

Equipment: Gauge \( \text{MIL-STD} \) \text{Powertip Tester} \( \text{Sample} \) \( \text{IQC Defect Level} : \text{Major Defect AQL 0.4; Minor Defect AQL 1.5} \)

FQC Defect Level: 100% Inspection • OUT Going Defect Level: Sampling •

Specification:

NO	Item	Specification	Judge	Level		
1	Part Number	The part number is inconsistent with work order of production	N.G.	Major		
2	Quantity	production				
	Electronic	The display lacks of some patterns.	N.G.	Major		
	characteristics of	Missing line.	N.G.	Major		
3		LCM The size of missing dot, A is > 1/2 Dot size		Major		
	A=( L + W )÷2	There is no function.	N.G.	Major		
		Output data is error	N.G.	Major		
		Material is different with work order of production	N.G.	Major		
		LCD is assembled in inverse direction	N.G.	Major		
		Bezel is assembled in inverse direction	N.G.	Major		
		Shadow is within LCD viewing area + 0.5 mm	N.G.	Major		
	Appearance of LCD A=( L + W )÷2	The diameter of dirty particle, A is>0.4 mm	N.G.	Minor		
		Dirty particle length is $>$ 3.0mm, and 0.01mm $<$ width $\leq$ 0.05mm		Minor		
4	Dirty particle (Including scratch · bubble)	Display is without protective film	N.G.	Minor		
		Conductive rubber is over bezel 1mm	N.G.	Minor		
		Polarizer exceeds over viewing area of LCD	N.G.	Minor		
		Area of bubble in polarizer, A>1.0mm, the number	N.G.	Minor		
		of bubble is >1 piece.				
		0.4mm $<$ Area of bubble in polarizer, A $<$ 1.0mm, the number of bubble is $>$ 4 pieces.	N.G.	Minor		
		Burned area or wrong part number is on PCB	N.G.	Major		
	Appearance of PCB A=( L + W )÷2	The symbol, character, and mark of PCB are unidentifiable.	N.G	Minor		
		The stripped solder mask, A is>1.0mm	N.G.	Minor		
		0.3mm < stripped solder mask or visible circuit. A <				
5		1.0mm, and the number is ≥4 pieces	N.G.	Minor		
		There is particle between the circuits in solder mask	N.G	Minor		
		The circuit is peeled off or cracked	N.G	Minor		
		There is any circuits risen or exposed.	N.G	Minor		
		0.2mm < Area of solder ball, A is $\leq$ 0.4mm The number of solder ball is $\geq$ 3 pieces	N.G	Minor		
		The magnitude of solder ball, A is >0.4mm.	N.G	Minor		



NO	Item	Specification	Judge	Level
6		The shape of modeling is deformed by touching.	N.G.	Major
	Appearance of	Insufficient epoxy: Circuit or pad of IC is visible	N.G.	Minor
	molding A=( L + W )÷2	Excessive epoxy: Diameter of modeling is >20mm or height is >2.5mm		Minor
		The diameter of pinhole in modeling, A is >0.2mm.	N.G.	Minor
	Appearance of frame	The folding angle of frame must be $>45^{\circ}$ +10°	N.G.	Minor
		The area of stripped electroplate in top-view of frame, A is >1.0mm.	N.G.	Minor
7	A=( L + W )÷2	Rust or crack is (Top view only)	N.G.	Minor
	Λ-( L + VV )· Z	The scratched width of frame is $>$ 0.06mm. (Top view only)	N.G.	Minor
	Electrical characteristic of backlight A=( L + W )÷2	The color of backlight is nonconforming	N.G.	Major
		Backlight can't work normally.	N.G.	Major
8		The LED lamp can't work normally	N.G.	Major
0		The unsoldering area of pin for backlight, A is >1/2 solder joint area.	N.G.	Minor
		The height of solder pin for backlight is >2.0mm	N.G.	Minor
	Assembly parts A=( L + W )÷2	The mark or polarity of component is unidentifiable.	N.G.	Minor
		The height between bottom of component and surface of the PCB is floating > 0.7mm	N.G.	Minor
10		D>1/4W  W D D D' Pad	N.G.	Minor
		End solder joint width, D' is >50% width of component termination or width of pad	N.G.	Minor
		Side overhang, D is $>$ 25% width of component termination.		Minor
		Component is cracked, deformed, and burned, etc.	N.G.	Minor
		The polarity of component is placed in inverse direction.		Minor
		Maximum fillet height of solder extends onto the component body or minimum fillet height is <0.5mm.	N.G.	Minor



# 4. RELIABILITY TEST

# 4.1 Reliability Test Condition

NO	Item	Test Co	ondition				
1	High Temperature Storage	Storage at 80 ±2°C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs					
2	Low Temperature Storage	Storage at -30 ±2°C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs					
3	High Temperature /Humidity Storage	1.Storage 96~100 hrs 60±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer).  or 2.Storage 96~100 hrs 40±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4 hrs.					
4	Temperature Cycling	$-20^{\circ}\mathbb{C} \rightarrow 25^{\circ}\mathbb{C} \rightarrow 70^{\circ}\mathbb{C} \rightarrow 25^{\circ}\mathbb{C}$ $(30\text{mins}) (5\text{mins}) (30\text{mins}) (5\text{mins})$ $10 \text{ Cycle}$					
5	Vibration	10~55Hz ( 1 minute ) 1.5mm X,Y and Z direction * (each 2hrs)					
6	ESD Test	Air Discharge: Apply 6 KV with 5 times discharge for each polarity +/- Testing location: Around the face of LCD	Contact Discharge: Apply 250V with 5 times discharge for each polarity +/- Testing location: 1.Apply to bezel. 2.Apply to Vdd, Vss.				
7	Drop Test	Packing Weight (Kg)  0 ~ 45.4  45.4 ~ 90.8  90.8 ~ 454  Over 454	Drop Height (cm)  122  76  61  46				



## 5. PRECAUTION RELATING PRODUCT HANDLING

#### **5.1 SAFETY**

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

#### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}$ C  $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

#### **5.4 TERMS OF WARRANTY**

- 5.4.1 Applicable warrant period
  - The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
  - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



# 6. PACKING SPECIFICATION

