



SPECIFICATIONS

CUSTOMER : Geneva Lab Information Consulting

SAMPLE CODE : SH128160T-066-L01Q

MASS PRODUCTION CODE : PH128160T-066-L01Q

SAMPLE VERSION : 01

SPECIFICATIONS EDITION : 002

DRAWING NO. (Ver.) : JLMD-PH128160T-066-L01Q _001

PACKAGING NO. (Ver.) : JPKG-PH128160T-066-L01Q _001

Customer Approved

Date:



Approved	Checked	Designer
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- Preliminary specification for design input
- Specification for sample approval

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History of Version

Date	Ver.	Edi.	Description	Page	Design by
2/15/2011	01	001	New Drawing	-	Violin Huang
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1. SPECIFICATIONS

1.1 Features

Main LCD Panel

Item	Standard Value
Display Type	128 *3(R、G、B) * 160 Dots
LCD Type	a-Si TFT , Normally White TN mode , Transmissive
Screen size(inch)	1.77 (Diagonal)
Viewing Direction	12 O'clock
Color configuration	R.G.B. vertical stripe
Backlight	White LED
Interface	4-line SPI interface
Other(controller / driver IC)	ST7735R(Support 65K or 262K Colors)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web side : http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	34.0 (W) * 47.0 (L) * 2.4 (H)(MAX)	mm

TFT LCD Panel

Item	Standard Value	Unit
Viewing Area (LCD)	29.032 (W) * 36.04 (L)	mm
Active Area (LCD)	28.032 (W) * 35.04 (L)	mm

Note: For detailed information please refer to LCM drawing.

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDD	-	-0.3	+4.6	V
	/VGH-VGL/	-	-0.3	30.0	V
Input Voltage	VIN	-	-0.3	VDD+0.3	V
Operating Temperature	T _{OP}	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C
Storage Humidity	H _D	Ta ≦ 60 °C	20	90	%RH

1.4 DC Electrical Characteristics

Module

GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Interface operation voltage	VDD	I/O supply voltage	-	2.8	3.0	V
Input High Voltage	V _{IH}	-	0.7*VDD	-	VDD	V
Input Low Voltage	V _{IL}	-	GND	-	0.3*VDD	V
Output High Voltage	V _{OH}	IOH=-0.1mA	0.8*VDD	-	VDD	V
Output Low Voltage	V _{OL}	IOL=0.1mA	GND	-	0.2*VDD	V
Supply Current	IDD	VDD= 2.8V, Pattern= Black*1	-	1.0	1.5	mA

Note1 : Maximum current display.

1.5 Optical Characteristics

TFT LCD Panel

VDD = 2.8V, Ta=25°C

Item	Symbol	Condition	Min.	Typ.	Max.	unit		
Response time	Tr + Tf	Ta = 25°C θX, θY = 0°	-	40	60	ms	Note2	
Viewing angle	Top	θY+	CR ≥ 10	-	30	-	Deg.	Note4
	Bottom	θY-		-	15	-		
	Left	θX-		-	45	-		
	Right	θX+		-	45	-		
Contrast ratio	CR		150	200	-	-	Note3	
Color of CIE Coordinate (With B/L)	White	X	Ta = 25°C θX, θY = 0°	0.25	0.30	0.35	-	Note1
		Y		0.27	0.32	0.37		
	Red	X		0.59	0.64	0.69		
		Y		0.29	0.34	0.39		
	Green	X		0.29	0.34	0.39		
		Y		0.55	0.60	0.65		
	Blue	X		0.09	0.14	0.19		
		Y		0.03	0.08	0.13		
Average Brightness Pattern=white display (With B/L)	IV	IF= 30mA	250	300	-	cd/m ²	Note1	

Note1:

1 : $\Delta B = B(\min) / B(\max) \times 100\%$.

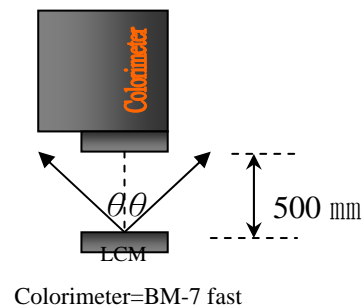
2 : Measurement Condition for Optical Characteristics:

a : Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500 ± 50 mm, (θ= 0°).

c : Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

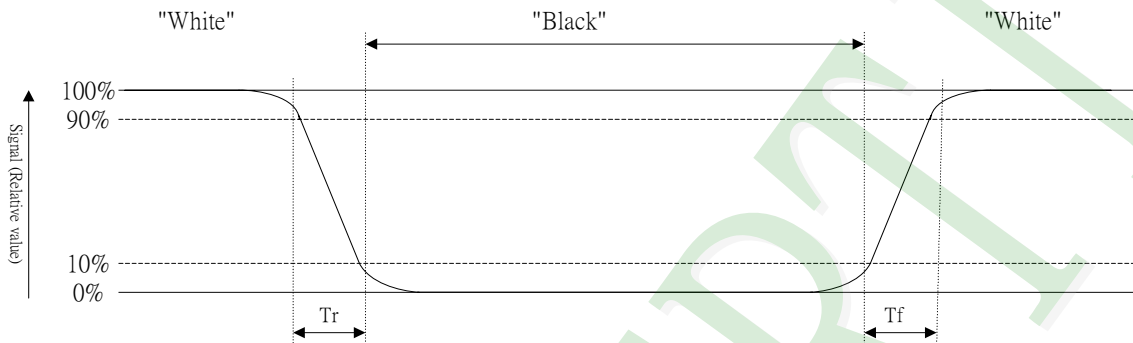
d : The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%.



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



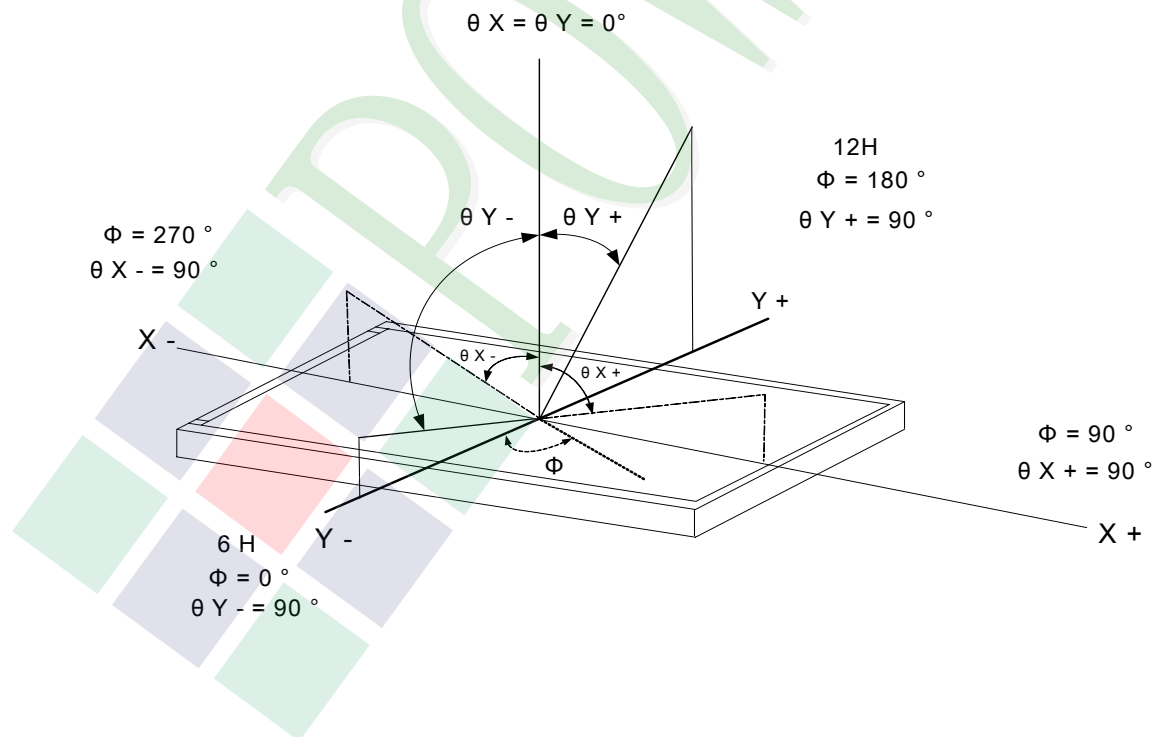
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



1.6 Backlight Characteristics

Maximum Ratings With one LED

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	35	mA
Reverse Voltage	VR	Ta =25°C	-	5	V
Forward Voltage	VF	Ta =25°C	-	3.5	V
Power Dissipation	PD	Ta =25°C	-	123	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF=30mA	-	3.2	3.5	V
Average Brightness (without LCD)	IV	IF=30mA	3200	3500	-	cd/m ²
Color of CIE Coordinate (without LCD)	X		-	0.30	-	-
	Y	-	0.29	-		
Color	White					

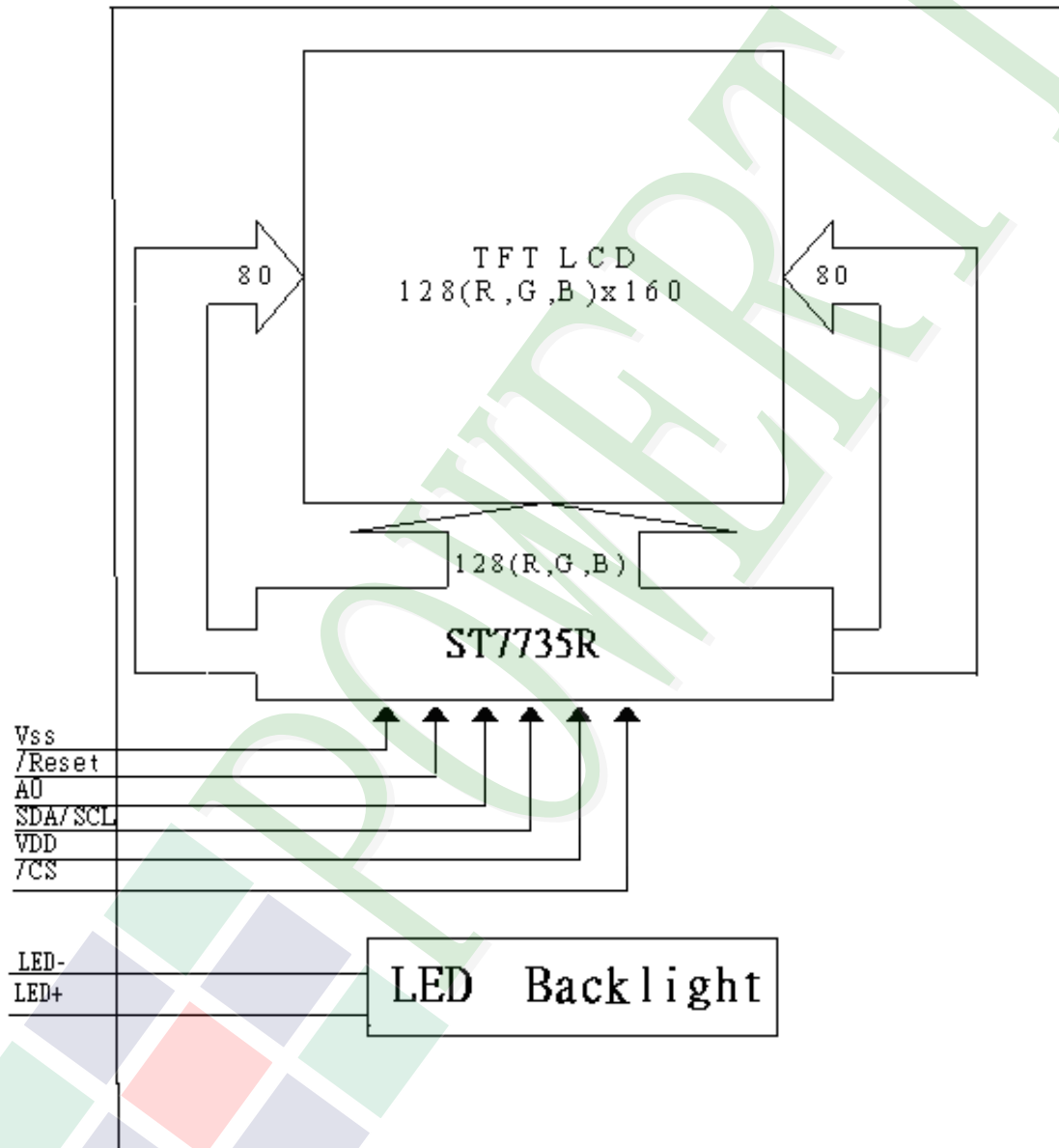
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram

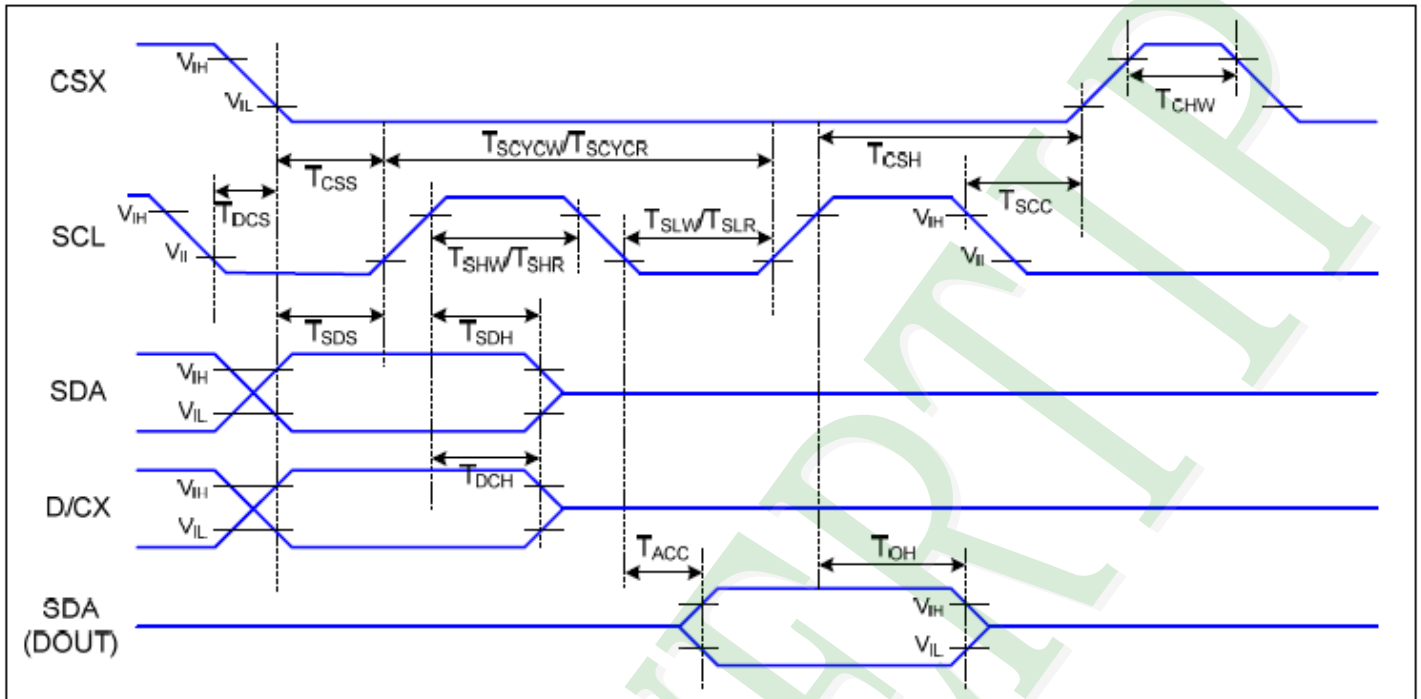


2.2 Interface Pin Description

Pin No.	Symbol	Function
1	NC	No Connection
2	VSS	Ground
3	LED-	LED Cathode
4	LED+	LED Anode
5	VSS	ground
6	/RESET	System Reset Pin
7	A0	Register select signal pin. L :Command; H:Display Data
8	SDA	Serial data input
9	SCL	Serial clock input
10	VDD	Power Supply
11	VDDI	Power Supply
12	/CS	Chip select input pin
13	VSS	Ground
14	NC	No Connection

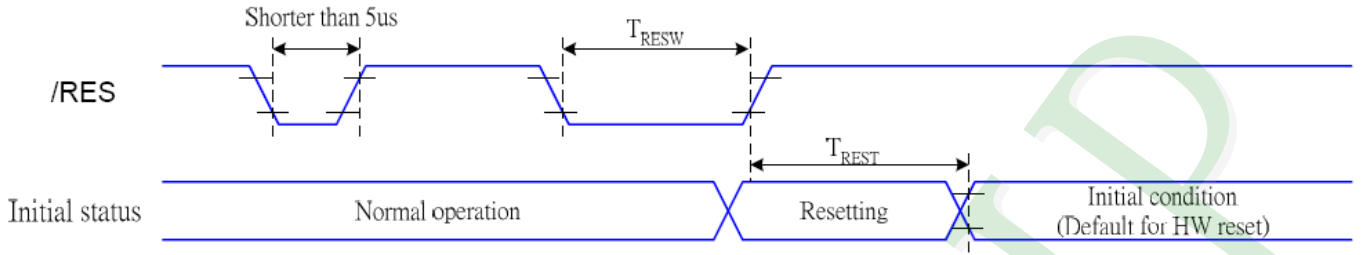
2.3 Timing Characteristics

Serial interface character (4-line serial)



T_a=25 °C, V_{DDI}=1.65~3.7V, V_{DD}=2.3~4.8V

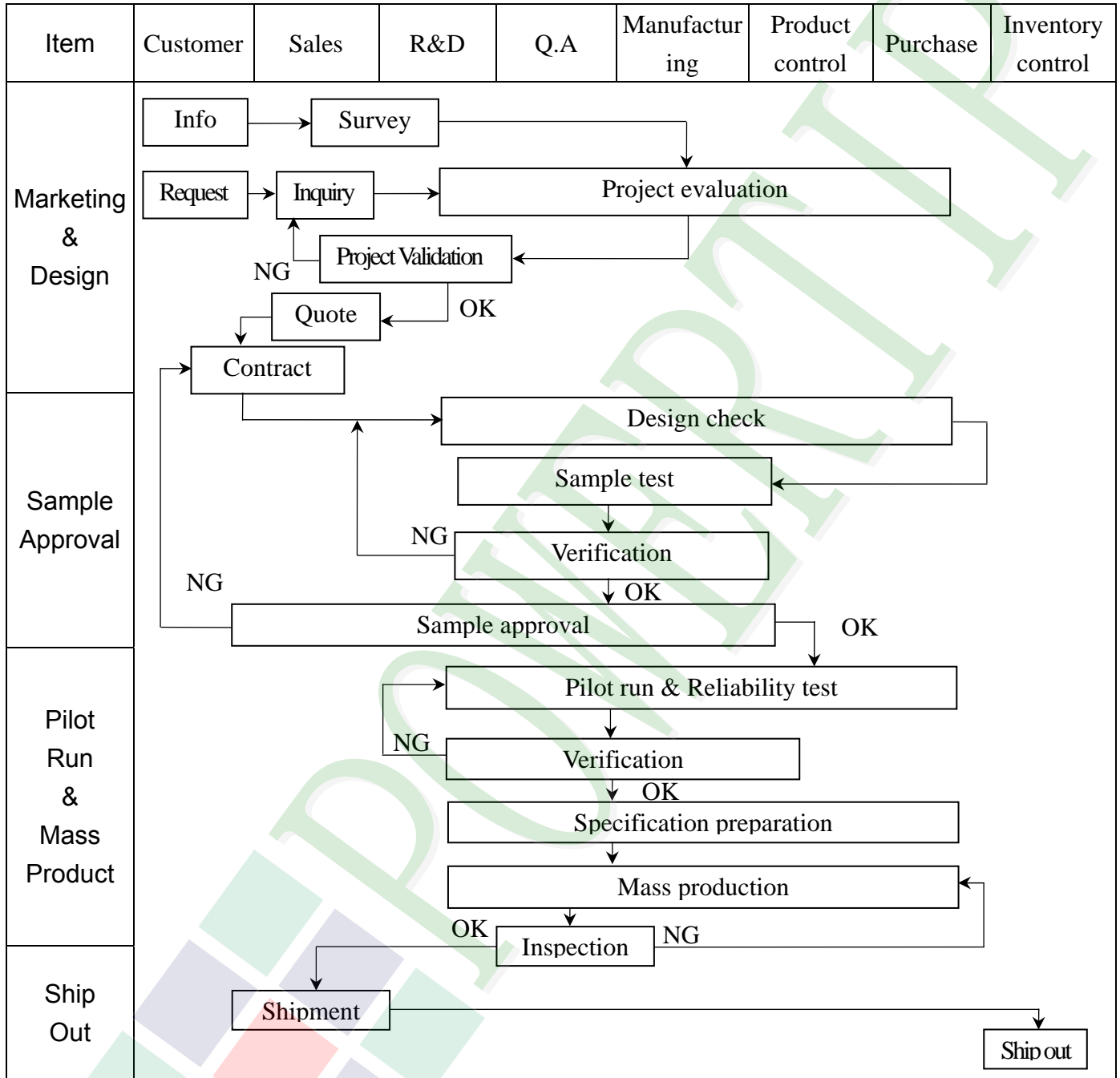
Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T _{CSS}	Chip select setup time (write)	45		ns	
	T _{CSH}	Chip select hold time (write)	45		ns	
	T _{CSS}	Chip select setup time (read)	60		ns	
	T _{SCC}	Chip select hold time (read)	65		ns	
	T _{CHW}	Chip select "H" pulse width	40		ns	
SCL	T _{SCYCW}	Serial clock cycle (Write)	66		ns	-write command & data ram
	T _{SHW}	SCL "H" pulse width (Write)	15		ns	
	T _{SLW}	SCL "L" pulse width (Write)	15		ns	
	T _{SCYCR}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T _{SHR}	SCL "H" pulse width (Read)	60		ns	
	T _{SLR}	SCL "L" pulse width (Read)	60		ns	
D/CX	T _{DCS}	D/CX setup time	10		ns	
	T _{DCH}	D/CX hold time	10		ns	
SDA (DIN) (DOUT)	T _{SDS}	Data setup time	10		ns	For maximum CL=30pF For minimum CL=8pF
	T _{SDH}	Data hold time	10		ns	
	T _{ACC}	Access time	10	50	ns	
	T _{OH}	Output disable time	15	50	ns	

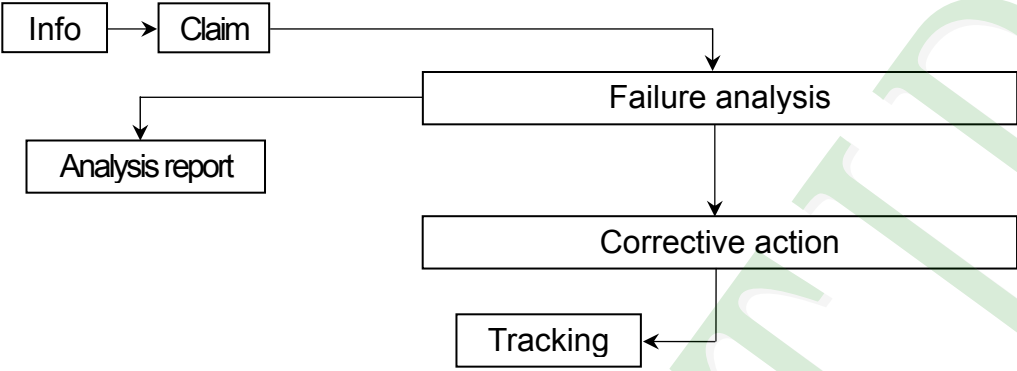
Reset Timing:


Related Pins	Symbol	Parameter	MIN	MAX	Unit
/RES	tRESW	Reset pulse duration	10	-	µs
	tREST	Reset cancel	-	5	ms
				120	ms

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



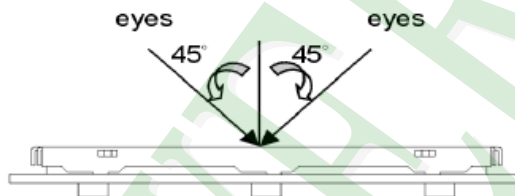
Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> FA[Failure analysis] Claim --> AR[Analysis report] FA --> CA[Corrective action] CA --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2. Inspection Specification

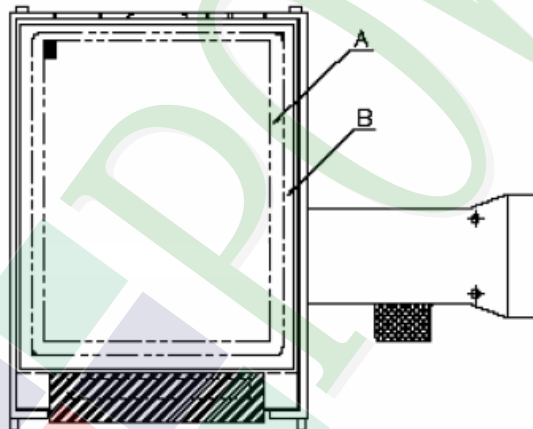
- ◆ Scope : The document shall be applied to TFT-LCD Module for less than 3.5" (Ver.B01).
- ◆ Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II .
- ◆ Equipment : Gauge 、 MIL-STD 、 Powertip Tester 、 Sample
- ◆ Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ◆ OUT Going Defect Level : Sampling.
- ◆ Standard of the product appearance test :

a. Manner of appearance test :

- (1). The test best be under 20W×2 fluorescent light , and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)

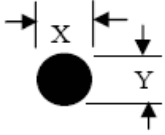
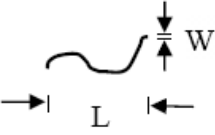
◆ Specification For TFT-LCD Module Less Than 3.5" :

(Ver.B01)

NO	Item	Criterion	Level												
01	Product condition	1. 1 The part number is inconsistent with work order of production.	Major												
		1. 2 Mixed product types.	Major												
		1. 3 Assembled in inverse direction.	Major												
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major												
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major												
04	Electrical Testing	4. 1 Missing line character and icon.	Major												
		4. 2 No function or no display.	Major												
		4. 3 Display malfunction.	Major												
		4. 4 LCD viewing angle defect.	Major												
		4. 5 Current consumption exceeds product specifications.	Major												
05	Dot defect (Bright dot 、 Dark dot) On -display	<table border="1"> <thead> <tr> <th></th> <th>Item</th> <th>Acceptance (Q'ty)</th> </tr> </thead> <tbody> <tr> <td rowspan="4">Dot Defect</td> <td>Bright Dot</td> <td>≤ 2</td> </tr> <tr> <td>Dark Dot</td> <td>≤ 3</td> </tr> <tr> <td>Joint Dot</td> <td>≤ 2</td> </tr> <tr> <td>Total</td> <td>≤ 3</td> </tr> </tbody> </table>		Item	Acceptance (Q'ty)	Dot Defect	Bright Dot	≤ 2	Dark Dot	≤ 3	Joint Dot	≤ 2	Total	≤ 3	Minor
			Item	Acceptance (Q'ty)											
Dot Defect	Bright Dot	≤ 2													
	Dark Dot	≤ 3													
	Joint Dot	≤ 2													
	Total	≤ 3													
5. 1 Inspection pattern : full white , full black , Red , Green and blue screens. 5. 2 It is defined as dot defect if defect area $> 1/2$ dot. 5. 3 The distance between two dot defect ≥ 5 mm.															

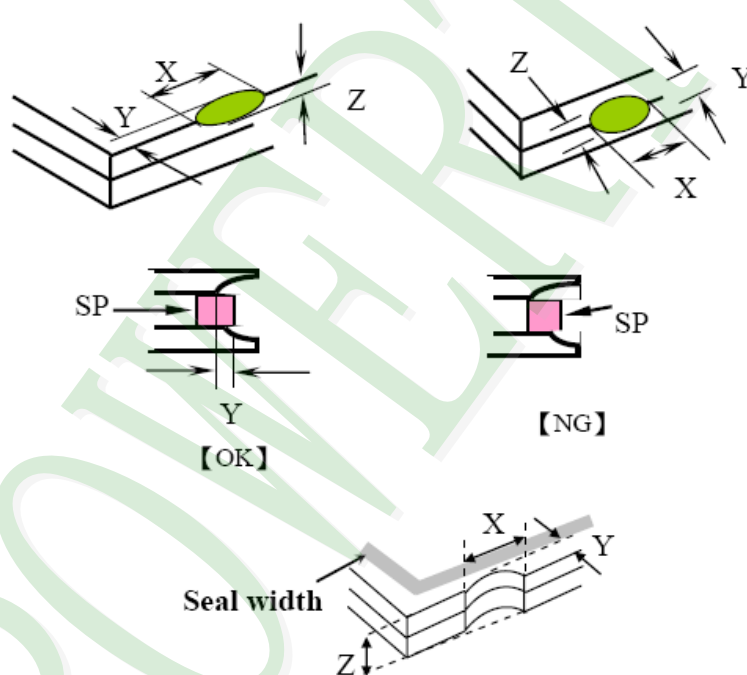
◆ Specification For TFT-LCD Module Less Than 3.5" :

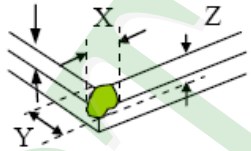
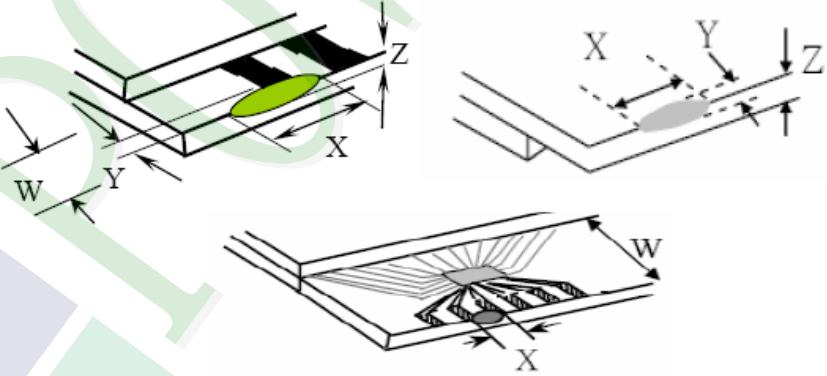
(Ver.B01)

NO	Item	Criterion	Level																																								
06	<p>Black or white dot、scratch、contamination</p> <p>Round type</p>  <p>$\Phi = (x + y) / 2$</p> <p>Line type</p> 	<p>6. 1 Round type (Non-display or display) :</p> <table border="1" data-bbox="552 439 1323 887"> <thead> <tr> <th rowspan="2">Dimension (diameter : Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.15$</td> <td colspan="2">Ignore</td> </tr> <tr> <td>$0.15 < \Phi \leq 0.20$</td> <td>2</td> <td rowspan="3">Ignore</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.30$</td> <td>2</td> </tr> <tr> <td>$\Phi > 0.30$</td> <td>0</td> </tr> <tr> <td>Total</td> <td colspan="2">3</td> </tr> </tbody> </table> <p>6. 2 Line type(Non-display or display) :</p> <table border="1" data-bbox="533 1003 1342 1417"> <thead> <tr> <th colspan="2">Dimension</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>Length (L)</th> <th>Width (W)</th> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.03$</td> <td>Ignore</td> <td rowspan="3">Ignore</td> </tr> <tr> <td>$L \leq 5.0$</td> <td>$0.03 < W \leq 0.05$</td> <td>3</td> </tr> <tr> <td>---</td> <td>$W > 0.05$</td> <td>As round type</td> </tr> <tr> <td colspan="2">Total</td> <td colspan="2">3</td> </tr> </tbody> </table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.15$	Ignore		$0.15 < \Phi \leq 0.20$	2	Ignore	$0.20 < \Phi \leq 0.30$	2	$\Phi > 0.30$	0	Total	3		Dimension		Acceptance (Q'ty)		Length (L)	Width (W)	A area	B area	---	$W \leq 0.03$	Ignore	Ignore	$L \leq 5.0$	$0.03 < W \leq 0.05$	3	---	$W > 0.05$	As round type	Total		3		Minor
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07	Polarizer Bubble	<table border="1" data-bbox="542 1467 1334 1872"> <thead> <tr> <th rowspan="2">Dimension (diameter : Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.20$</td> <td colspan="2">Ignore</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.50$</td> <td>3</td> <td rowspan="2">Ignore</td> </tr> <tr> <td>$\Phi > 0.50$</td> <td>0</td> </tr> <tr> <td>Total</td> <td colspan="2">3</td> </tr> </tbody> </table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.20$	Ignore		$0.20 < \Phi \leq 0.50$	3	Ignore	$\Phi > 0.50$	0	Total	3		Minor																								
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◆ Specification For TFT-LCD Module Less Than 3.5" :

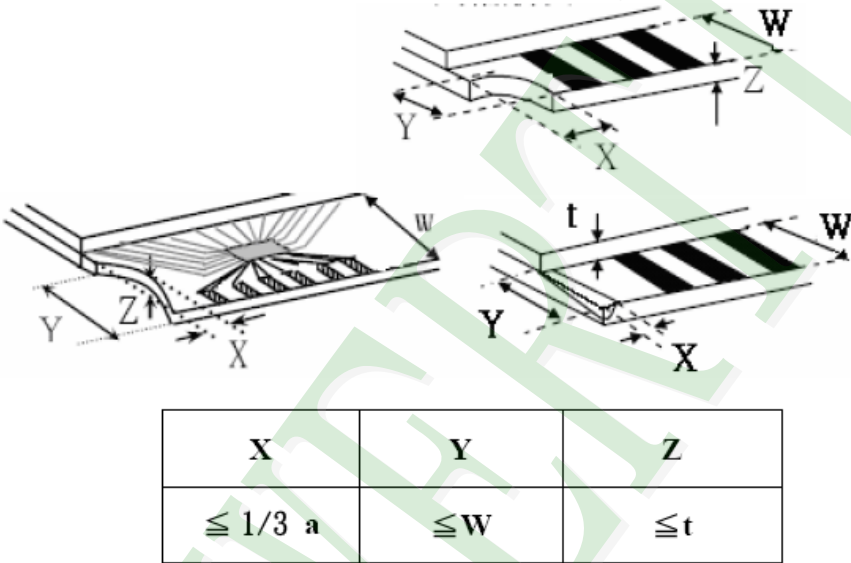
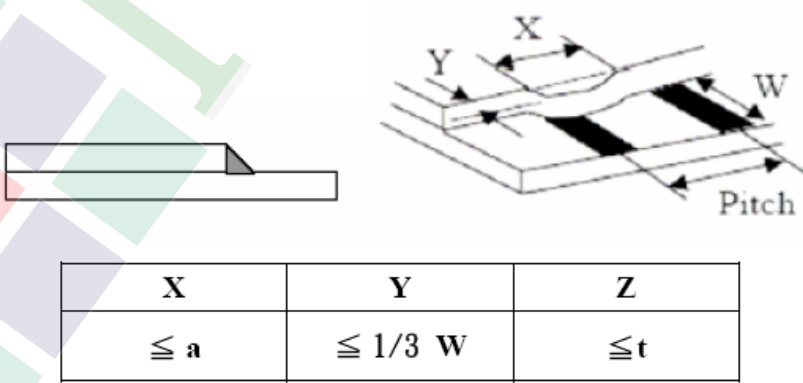
(Ver.B01)

NO	Item	Criterion	Level						
08	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Z : The thickness of crack t : The thickness of glass</p> <p>Y : The width of crack. W : terminal length a : LCD side length</p>	Minor						
		<p>8.1 General glass chip :</p> <p>8.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="542 1456 1340 1747"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq a$</td> <td>Crack can't enter viewing area</td> <td>$\leq 1/2 t$</td> </tr> <tr> <td>$\leq a$</td> <td>Crack can't exceed the half of SP width.</td> <td>$1/2 t < Z \leq 2 t$</td> </tr> </tbody> </table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
X	Y	Z							
$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$							
$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$							

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		<p>8.1.2 Corner crack :</p>  <table border="1" data-bbox="531 786 1337 1077"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq 1/5 a$</td> <td>Crack can't enter viewing area</td> <td>$Z \leq 1/2 t$</td> </tr> <tr> <td>$\leq 1/5 a$</td> <td>Crack can't exceed the half of SP width.</td> <td>$1/2 t < Z \leq 2 t$</td> </tr> </tbody> </table>		X	Y	Z	$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$	$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$			
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$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$													
		<p>8.2 Protrusion over terminal :</p> <p>8.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="568 1671 1345 1843"> <thead> <tr> <th></th> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>Front</td> <td>$\leq a$</td> <td>$\leq 1/2 W$</td> <td>$\leq t$</td> </tr> <tr> <td>Back</td> <td>$\leq a$</td> <td>$\leq W$</td> <td>$\leq 1/2 t$</td> </tr> </tbody> </table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	$\leq a$	$\leq W$	$\leq 1/2 t$	
	X	Y	Z												
Front	$\leq a$	$\leq 1/2 W$	$\leq t$												
Back	$\leq a$	$\leq W$	$\leq 1/2 t$												

◆ Specification For TFT-LCD Module Less Than 3.5" :

(Ver.B01)

NO	Item	Criterion	Level
08	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length</p>	Minor
		<p>8.2.2 Non-conductive portion :</p>  <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>8.2.3 Glass remain :</p> 	

◆ Specification For TFT-LCD Module Less Than 3.5" :

(Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General appearance	10. 1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION											
1	High Temperature Storage Test	Keep in $+80^{\circ}\text{C} \pm 2^{\circ}\text{C}$ 96 hrs Surrounding temperature, then storage at normal condition 4hrs.											
2	Low Temperature Storage Test	Keep in $-30^{\circ}\text{C} \pm 2^{\circ}\text{C}$ 96 hrs Surrounding temperature, then storage at normal condition 4hrs.											
3	High Temperature / High Humidity Storage Test	Keep in $+60^{\circ}\text{C} / 90\%$ R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)											
4	Temperature Cycling Storage Test	<p style="text-align: center;"> $-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ (30mins) (5mins) (30mins) (5mins) $\leftarrow \hspace{10em} \rightarrow$ 10 Cycle </p> <p>Surrounding temperature, then storage at normal condition 4hrs.</p>											
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/-	Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/-										
		1. Temperature ambience : $15^{\circ}\text{C} \sim 35^{\circ}\text{C}$ 2. Humidity relative : 30%~60% 3. Energy Storage Capacitance(Cs+Cd) : $150\text{pF} \pm 10\%$ 4. Discharge Resistance(Rd) : $330\Omega \pm 10\%$ 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : $\pm 5\%$)											
6	Vibration Test (Packaged)	1. Sine wave 10~55 Hz frequency (1 min/sweep) 2. The amplitude of vibration : 1.5 mm 3. Each direction (X、Y、Z) duration for 2 Hrs											
7	Drop Test (Packaged)	<table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Packing Weight (Kg)</th> <th>Drop Height (cm)</th> </tr> </thead> <tbody> <tr> <td>0 ~ 45.4</td> <td>122</td> </tr> <tr> <td>45.4 ~ 90.8</td> <td>76</td> </tr> <tr> <td>90.8 ~ 454</td> <td>61</td> </tr> <tr> <td>Over 454</td> <td>46</td> </tr> </tbody> </table>		Packing Weight (Kg)	Drop Height (cm)	0 ~ 45.4	122	45.4 ~ 90.8	76	90.8 ~ 454	61	Over 454	46
		Packing Weight (Kg)	Drop Height (cm)										
0 ~ 45.4	122												
45.4 ~ 90.8	76												
90.8 ~ 454	61												
Over 454	46												
		Drop Direction : ※1 corner / 3 edges / 6 sides each 1time											

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

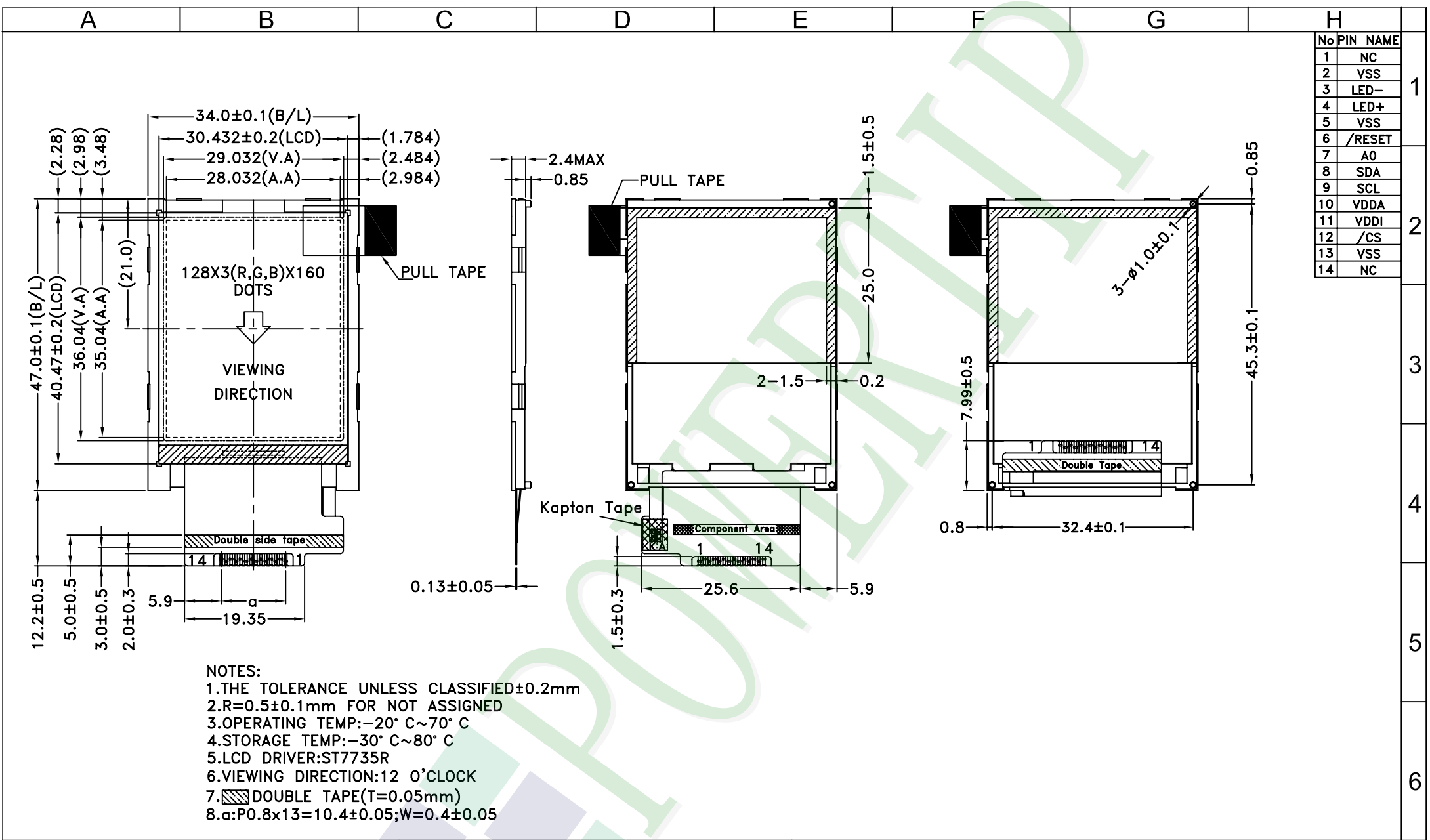
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



No	PIN	NAME
1	NC	
2	VSS	
3	LED-	
4	LED+	
5	VSS	
6	/RESET	
7	A0	
8	SDA	
9	SCL	
10	VDDA	
11	VDDI	
12	/CS	
13	VSS	
14	NC	

007			
006			
005			
004			
003			
002			
001	NEW DRAWING	Ye	2011/02/22
REV	REV BY	REVISER	DATE

PART NO:	PH128160T-066-L01Q
DRAWING NAME:	JLMD-PH128160T-066-L01Q
TITLE:	LCD MODULE DRAWING

江蘇久正光電有限公司
JIANGSU POWERTIP TECHNOLOGY CORP.LTD.

Design	Yeliangtao	(3) Surface Unit MM Scale 4:5 Page 1/1	Material	<table border="1"> <tr><th>公差</th><th>精度</th></tr> <tr><td>最小</td><td>等級</td></tr> <tr><td>1 ~ 4</td><td>-</td></tr> <tr><td>4 ~ 16</td><td>-</td></tr> <tr><td>16 ~ 63</td><td>-</td></tr> <tr><td>63 ~ 250</td><td>-</td></tr> <tr><td>250 ~ 1000</td><td>-</td></tr> </table>	公差	精度	最小	等級	1 ~ 4	-	4 ~ 16	-	16 ~ 63	-	63 ~ 250	-	250 ~ 1000	-
公差	精度																	
最小	等級																	
1 ~ 4	-																	
4 ~ 16	-																	
16 ~ 63	-																	
63 ~ 250	-																	
250 ~ 1000	-																	
Check	Zhou	Thickness																
Approve	Ryan	Quantity																

LCM包裝規格書

LCM Packaging Specifications

(For Tray)

Approve	Check	Contact
Ryan	Ryan	Ye

Documents NO. JPKG-PH128160T-066-L01Q

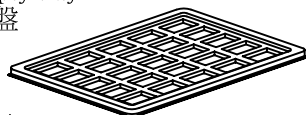
1. 包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH128160T-066-L01Q	34.0 X 47.0	6.0	864	5.18
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	—	6	—
3	TRAY 盤 (2)Tray	TYPH12816044BH	352 X 260 X 10.8	100	60	6.0
4	內盒(3)Product Box	BX36627063ABBA	393 X 274 X 68	0.2692	6	1.6152
5	保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.4208	1	1.4208
7	舒美墊(7)	OTFOAMPHD05ABA	310 X 225 X 1	0.0005	54	0.027
8						
9						

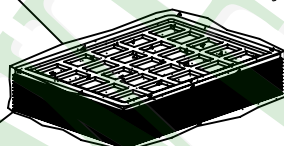
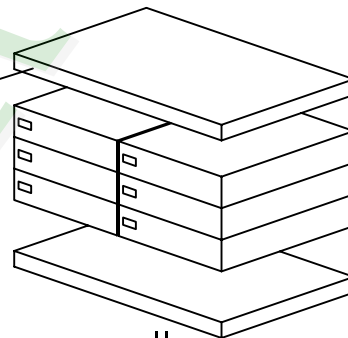
2. 一整箱總重量 (Total LCD Weight in carton) : 14.23 Kg \pm 10% 取小數2位

3. 單箱數量規格表 (Packaging Specifications and Quantity) :

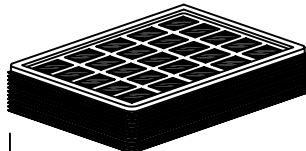
(1) LCM quantity per box : no per tray	16	x no of tray	9	=	144
(2) Total LCM quantity in carton : quantity per box	144	x no of boxes	6	=	864

Use empty tray
空盤

+

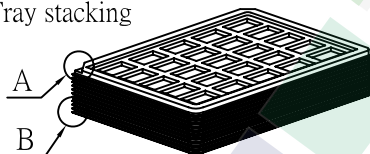
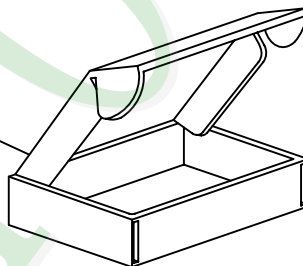
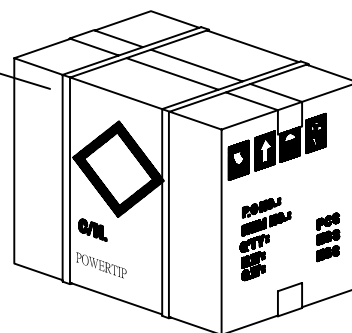
(1) 多層薄膜
POF(4) 保利龍板
Polylon board

Put products into the tray

(2) TRAY 盤
Tray

↓

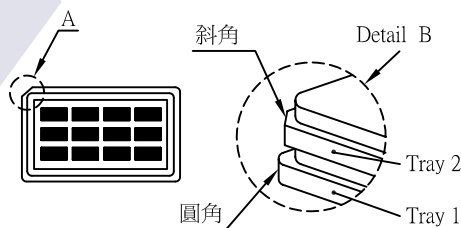
Tray stacking

(3) 內盒
Product Box(5) 外紙箱
Carton

特 記 事 項 (REMARK)

1. Label Specifications :

MODEL:
LOT NO:
QUANTITY:
CHECK:



2. TRAY盤相疊時, 需旋轉180度, 請詳見B視圖
Rotate tray 180 degrees and place on top of stack.
Check the tray stack using Fig. B.

3. 可適用於單品包裝
It's also suitable to Panel