

SPECIFICATIONS

CUSTOMER	
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MASS PRODUCTION CODE	PH128800T002-ZBC01
SAMPLE VERSION	02
SPECIFICATIONS EDITION	006
DRAWING NO. (Ver.)	LMD-PH128800T002-ZBC01 (Ver.003)
PACKAGING NO. (Ver.)	PKG-PH128800T002-ZBC01 (Ver.003)

Customer Approved

Date:

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		iminary specification cification for sample a	C I	POWERTIP 2018.09.06 TW RD APR
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History of Version

Date	Ver.	Edi.	Description	Page	Design by
03/14/2016	01	001	New Drawing.	-	Ackey
06/08/2016	01	002	New Sample.	-	Ackey
06/30/2016	01	003	Modify 1.1 Features & 3.2 Inspection Specification & 4.1 Reliability Test Condition content.	4, 20, 27	Ackey
02/09/2017	02	004	Second Sample. (Change CTP IC->Atmel IC) & Change Tray Package Number.	10, Appendix	Ackey
08/24/2017	02	005	Modify Drawing.	Appendix	Ackey
08/31/2018	02	006	Modify Packaging	Appendix	Ackey
				Tota	al: 28 Page



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1. SPECIFICATIONS

1.1 Features

ltem	Standard Value
Screen size(inch)	10.1(Diagonal)
Driver element	IPS
Resolution	1280* (R 、 G 、 B) * 800 Dots
Display mode	Normally Black, Transmissive
Touch panel	Projective Capacitive Touch Panel 5 Points touch
Surface treatment	Anti-Fingerprint Coating
Color arrangement	RGB-stripe
Weight	292.74(Typ.)
inversion	1+2line
Interface	LVDS
IC	HX8288*4 & HX8695*1
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : <u>http://www.powertip.com.tw/news_detail.php?Key=1&cID=1</u>

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	254.96(W) * 173.6 (L) * 7.4 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	216.96 (W) * 135.60 (L)	mm

Note : For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Digital Supply Voltage	VDD	-	-0.3	+3.9	V
TFT Gate on voltage	VGH	-	-0.3	+42	V
TFT Gate off voltage	VGL	-	-19	+0.3	V
TFT Gate voltage	VGH-VGL	-	+12	+40	
Analog power supply voltage	AVDD	-	-0.3	+14	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	Tst	-	-30	+80	°C
Storage Humidity	H _D	Ta<60 ℃	20	90	%RH

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.



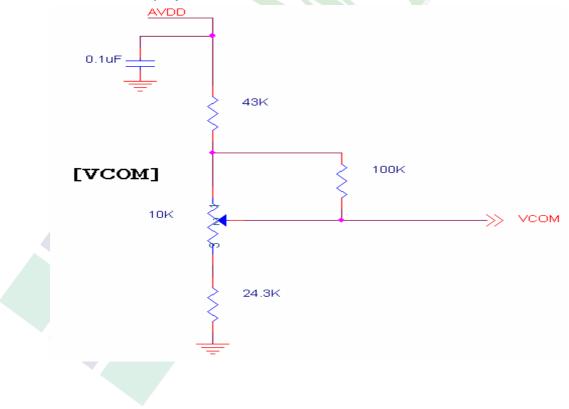
1.4 DC Electrical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Digital Supply Voltage	VDD	-	-	3.3	-	
Analog power supply voltage	AVDD	-	8.0	8.2	8.4	V
TFT Gate on voltage	VGH	-	21.7	22.0	22.3	V
TFT Gate off voltage	VGL	-	-7.3	-7.0	-6.7	V
TFT Common electrode voltage	VCOM	-	2.7	3.0	3.3	V
Input logic high voltage	VIH	-	0.8*VDD	-	VDD	V
Input logic low voltage	VIL	-	0	-	0.2*VDD	V
		VDD=3.3V				
Supply Current	IDD	Pattern=	-	100	150	mA
		Picture*4				

Note 1: Be sure to apply VDD and V_{GL} to the LCD first, and then apply V_{GH}.

Note 2: VDD setting should match the signals output voltage (VIH / VIL) of customer's system board.

- Note 3: Typical VCOM is only a reference value, it must be optimized according to each LCM. Be sure to use VR.
- Note 4: Maximum current display.





1.5 Optical Characteristics

TFT LCD Panel

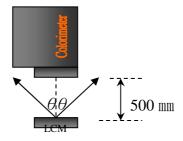
Ta=25℃

ltem		Symbol	Condition	Min.	Тур.	Max.	Unit	-
Response time		Tr	_	-	10	20	ms	Note2
	IC	Tf	-	-	15	30	1115	NOICE
	Тор	ΘY+		75	85	-		
Viewing angle	Bottom	ΘY-	CR ≥ 10	75	85	1	Deg.	Note4
viewing angle	Left	ΘX-	Ch 2 10	75	85	-	Deg.	NOIE4
	Right	ΘX+		75	85	-		
Contrast rati	0	CR		600	800	-	-	Note3
Color of CIE Coordinate	White	Х	IF=240mA	0.26	0.31	0.36	-	Note1
(With B/L)		Y		0.31	0.36	0.41		110101
Average Brighti Pattern=white di		IV	IF=240mA	450	500	-	cd/m2	Note1
Luminance unifc	ormity	YU	IF=240mA	75	80	-	%	Note1

Note1:

- $1 : \triangle B = B(min) / B(max) \times 100\%$
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: $25^{\circ}C \pm 5^{\circ}C / 60 \pm 20\%$ R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 ± 50 mm \rightarrow (0= 0 °)
 - c: Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d : The uncertainty of the C.I.E coordinate measurement ± 0.01 $^{\rm ,}$ Average Brightness \pm 4%





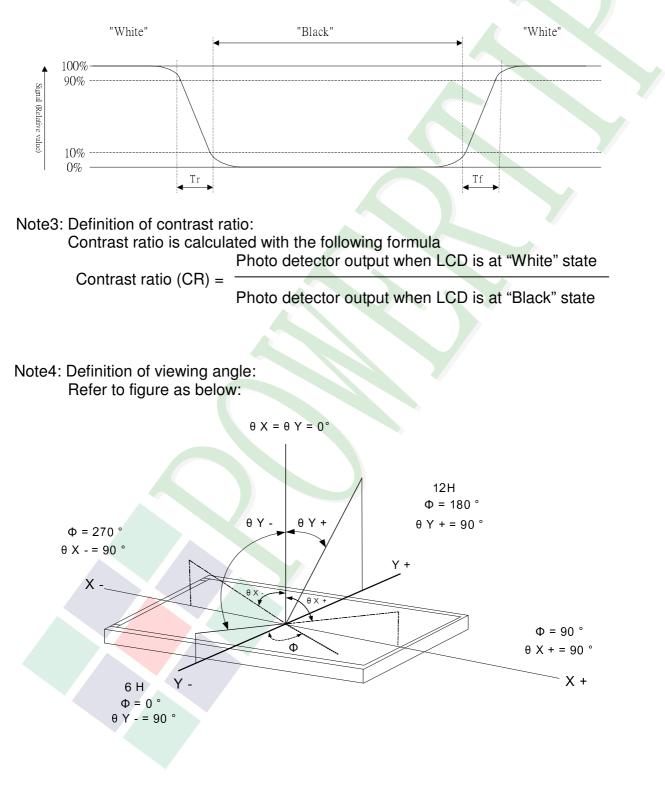
Colorimeter=BM-7 fast



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Power Dissipation	Pd	-	-	4680	-	mW
LED Forward Current	IF	1 LED	-	-	70	mA
LED Reverse Voltage	VR	1 LED	-	-	5	V

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Voltage for LED backlight	VF		16.8	(19.5)	21	V
Current for LED backlight	IF		200	240	280	mA
Color	White					

Other Description

Item	Conditions	Description
Life Time	Ta =25℃ IF= 200mA	20000 hrs

Note: The "LED life time" is defined as the module brightness decrease to 50% original brightness at

Ta=25°C and IF =200mA. The LED lifetime could be decreased if operating IF is larger than 200mA.



1.7 Touch Panel Characteristics

Features

Item	Standard Value				
Touch Panel Size	10.1				
Touch type	Projective capacitive touch panel				
Input Method	Finger / 5 Points touch				
Output Interface	USB				
Response Time	≦25ms				
Light Transparency	85% Min				
Surface Hardness	7H(Pencil)				

Mechanical Specifications

Item	Standard Value	Unit
Viewing Area	217.96 (W) * 136.6 (L)	mm

Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Supply voltage	TPVDD	-	-0.3	+6.0	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	Тѕт	-	-30	+80	°C

DC Electrical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage	TPVDD	-	-	5.0	-	V

Touch Panel IC Read/Write description & Register Mapping

Reference : Atmel Touch Driver Porting Reference Guide.



Interface Pin Description

CN2		
Pin No.	Symbol	Function
1	TPVDD	Digital I/O Power Can be Set as TPVDD.
2	USB-D-	USB Differential Signal D
3	USB-D+	USB Differential Signal D+.
4	NC	Not Connection.
5	GND	Ground.
6	NC	Not Connection.



2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

POWERTIP

2.2 Interface Pin Description

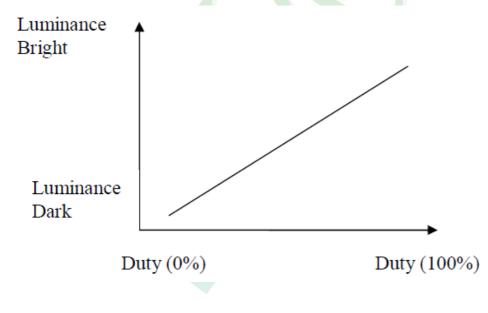
A 40pin connector is used for the module electronics interface. The recommended model is FH52-40S-0.5SH manufactured by Vigor Conn.

Pin No.	Symbol	Description
1	VCOM	Common voltage
2	VDD	Digital power
3	VDD	Digital power
4	NC	Not connect
5	NC	Not connect
6	NC	Not connect
7	GND	Ground
8	RXIN0-	Negative LVDS differential data inputs
9	RXIN0+	Positive LVDS differential data inputs
10	GND	Ground
11	RXIN1-	Negative LVDS differential data inputs
12	RXIN1+	Positive LVDS differential data inputs
13	GND	Ground
14	RXIN2-	Negative LVDS differential data inputs
15	RXIN2+	Positive LVDS differential data inputs
16	GND	Ground
17	RXCLKIN-	Negative LVDS differential clock inputs
18	RXCLKIN+	Positive LVDS differential clock inputs
19	GND	Ground
20	RXIN3-	Negative LVDS differential data inputs
21	RXIN3+	Positive LVDS differential data inputs
22	GND	Ground
23	NC	Not connect
24	NC	Not connect
25	GND	Ground



Pin No.	Symbol	Description					
26	NC	ot connect					
27	LED_PWM	CABC controller signal output for backlight					
28	NC	Not connect					
29	AVDD	Power for Analog Circuit					
30	GND	Ground					
31	LED-	LED Cathode					
32	LED-	LED Cathode					
33	NC	Not connect					
34	NC	Not connect					
35	VGL	Gate OFF Voltage					
36	GND	Ground					
37	CABC_EN	CABC Enable Input. High Voltage: Enable; Low Voltage or open: Disable					
38	VGH	Gate ON Voltage					
39	LED+	LED Anode					
40	LED+	LED Anode					

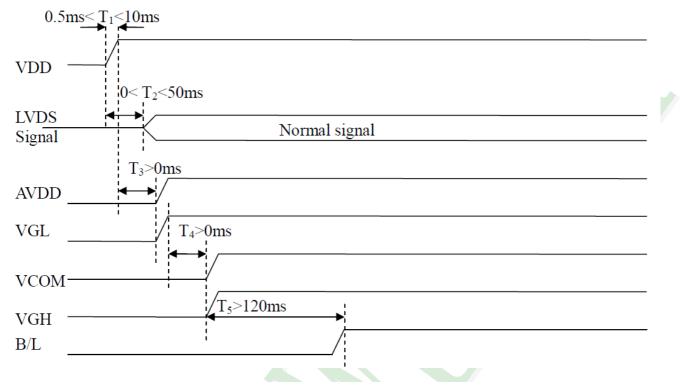
Note: LED_PWM is used to adjust backlight brightness



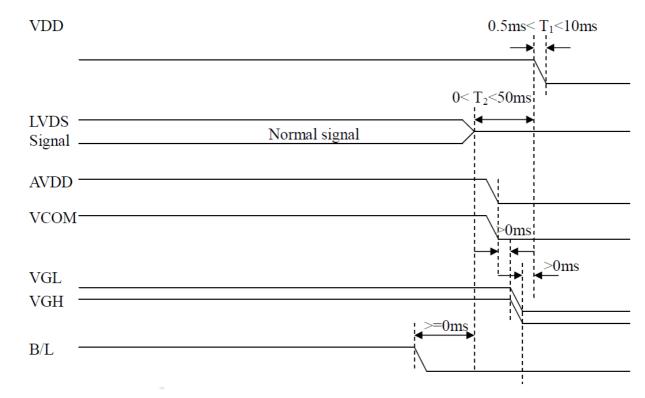


2.3 Timing Characteristics 2.3.1 POWER ON/OFF SEQUENCE

a. Power on:



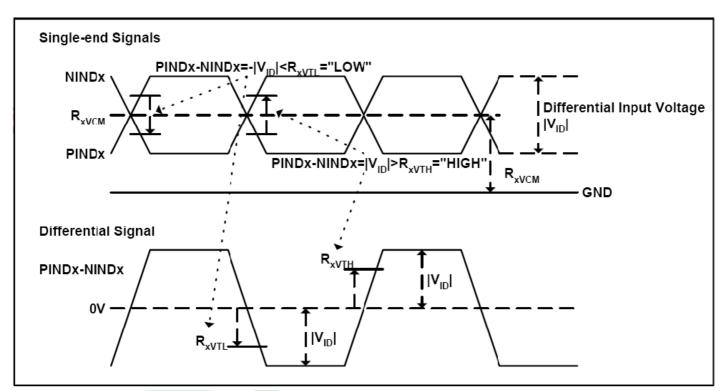
b. Power off:





2.3.2 LVDS Signal Timing Characteristics AC Electrical Characteristics

Parameter	Symbol		Values		Unit	Remark
	-,	Min.	Typ.	Max.		
LVDS Differential input high Threshold voltage	R _{xVTH}	-	-	+100	m∨	R _{XVCM} =1.2
LVDS Differential input low Threshold voltage	R _{xVTL}	-100	-	-	mV	V
LVDS Differential input common mode voltage	R _{xVCM}	0.7	-	1.6	V	
LVDS Differential voltage	V _{ID}	200	-	600	mV	



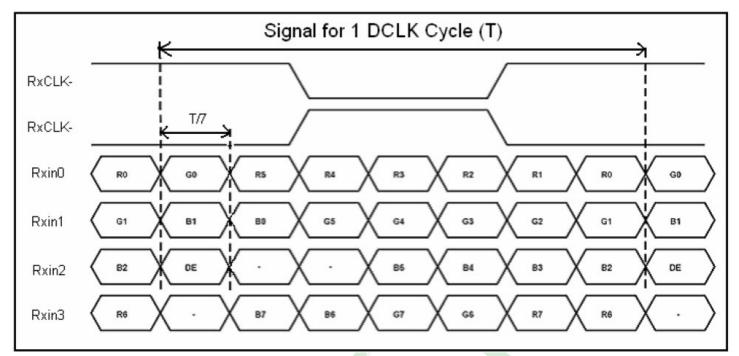


Timing Table

ltem	Symbol		Values		Unit	Remark
nem	Symbol	Min.	Тур.	Max.	Onit	Remark
Clock Frequency	1/Tc	68.9	71.1	73.4	MHz	Frame rate =60Hz
Horizontal display area	tнр		1280		Тс	
HS period time	tн	1410	1440	1470	Тс	
HS Width +Back Porch +Front Porch	thw+ thbp +thfp	130	160	190	Тс	
Vertical display area	tvd		800		tн	
VS period time	tv	815	823	833	tн	
VS Width +Back Porch +Front Porch	tvw+ tvbp +tvfp	15	23	33	tн	
Vsync		t _v				
DE			·····	1		
H sync t _{H E P} t _{HD} t _{H F P}						



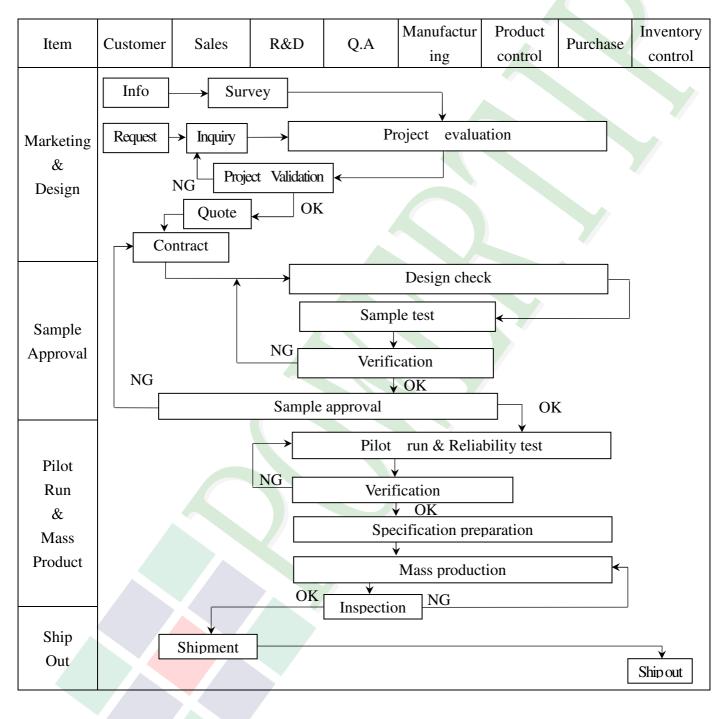
LVDS Data Input Format





3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



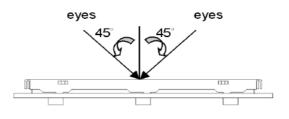


Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
Sales Service	Info Analys	→ Claim -	[Trackin	Failure an Corrective			
Q.A Activity	3. Equipment	Maintenand nt calibration ization Mana	n	4. E	ocess improv ducation An			

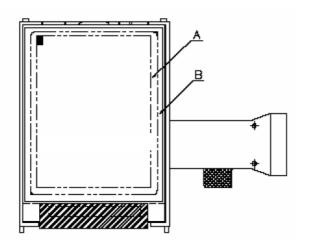
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3.2. Inspection Specification

- ◆Scope: The document shall be applied to TFT-LCD Module for 3. 5″ ~10″ (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ♦OUT Going Defect Level : Sampling.
- ◆Standard of the product appearance test ∶
 - a. Manner of appearance test :
 - (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



◆Specification For TFT-LCD Module 3. 5″~10″∶ (Ver.B01) NO Item Criterion 1. 1 The part number is inconsistent with work order of production. 01 **Product condition** 1. 2 Mixed product types. Major 1. 3 Assembled in inverse direction. Major 02 Quantity 2. 1 The quantity is inconsistent with work order of production. Major 3.1 Product dimension and structure must conform to structure 03 **Outline** dimension diagram. 4.1 Missing line character and icon. 4. 2 No function or no display. Major 04 **Electrical Testing** 4. 3 Display malfunction. 4.4 LCD viewing angle defect. 4.5 Current consumption exceeds product specifications. Item Acceptance (Q'ty) ≤ 4 **Bright Dot** Dot defect ≦ 5 **Dark Dot** Dot Defect (Bright dot \ ≤ 3 Joint Dot 05 Dark dot) Total ≤ 7 **On** -display 5.1 Inspection pattern : full white , full black , Red , Green and

blue screens.

5. 2 It is defined as dot defect if defect area >1/2 dot.

5. 3 The distance between two dot defect ≥ 5 mm.

Level

Major

Major

Major

Major

Major

Major

Minor



◆Specification For TFT-LCD Module 3. 5″~10″:								
NO	Item		Cr	iterion			Level	
	6. 1 Round type (Non-display or display) :							
		Dimensior	n (diameter ÷ Ф	Ac	ceptance ((Q'ty)		
	Black or white	Dimension			rea	B area		
	dot 、 scratch 、		$\Phi \leq 0.25$	Ign	ore			
	contamination	0.25 <	$< \Phi \leq 0.50$	5		T		
	Round type		$\Phi > 0.50$	0		Ignore		
			Total	5				
06	$\Phi = (x+y)/2$	6. 2 Line type(N	on-display or d	isplay) :			Minor	
	Line type $\downarrow W$ $\downarrow W$ $\downarrow L$			Accepta	ance (Q'ty)			
		Length (L)	L) Width (W)		A area	B area		
			W	≤ 0.03	Ignore			
		L ≦10.0	0.03 < W	≤ 0.05	4			
		L ≦5.0	0.05 < W	≤ 0.10	2	Ignore		
			W	>0.10	As roun type	d		
			Total		5			
		Dimension (diameter ÷ Φ)	Ac A ar	ceptance (rea	(Q'ty) B area		
			$\Phi \leq 0.25$	Igno	ore			
07	Polarizer	0.25 <	$0.25 < \Phi \le 0.50$ $0.50 < \Phi \le 0.80$				Minor	
	Bubble	0.50 <				Ignore		
			$\Phi > 0.80$	0				
		То	otal	5				



◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.)									
NO	Item		Criterion		Level				
08	The crack of glass	Z : The thi t : The thi 8.1 Genera	ckness of crack V		Minor				
		X	Y	Z					
		≦ a	Crack can't enter viewing area	$\leq 1/2 t$					
		≦ a	Crack can't exceed the half of SP width.	$1/2 t < Z \leq t$					



Specification For TFT-LCD Module 3. 5″~10″: (Ver.B01							
NO	Item	Criterion			Level		
		Symbols : X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length 8. 1. 2 Corner crack : X - Z					
		X	Y		Z		
		≦1/5 a	Crack can't en viewing area		$\leq 1/2 t$		
	The crack of glass	$\leq 1/5$ a	Crack can't excee half of SP widt	1/2 1	$< Z \leq 2 t$		
08		8.2 Protrusion over terminal : 8.2.1 Chip on electrode pad : X Y Z X Y ZV Y X X Y ZX Y $ZFront \leq a \leq 1/2 W \leq tBack \leq a \leq W \leq 1/2 t$				Minor	



◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.B01)									
NO	Item	Criterion							
08	The crack of glass	Symbols : X : The length of crack X : The width of crack. Z : The thickness of crack X : terminal length t : The thickness of glass a : LCD side length 8. 2. 2 Non-conductive portion : V V X X X X X X X X	Level						



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION			
1	High Temperature Storage Test	Keep in +80 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature Storage Test	Keep in −30 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)			
4	Temperature Cycling Storage Test	$-30^{\circ}C \rightarrow +25^{\circ}C \rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$ $(30 \text{mins}) (5 \text{mins})$			
5	ESD Test	Air Discharge:Contact Discharge:Apply 2 KV with 5 timesApply 250 V with 5 timesDischarge for each polarity +/-discharge for each polarity +/-1.Temperature ambiance : 15°C ~35°C2.Humidity relative : 30% ~60%3.Energy Storage Capacitance(Cs+Cd) :150pF±10%4.Discharge Resistance(Rd) : 330Ω±10%5.Discharge, mode of operation :Single Discharge (time between successive discharges at least 1 sec)(Tolerance if the output voltage indication : ±5%)			
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 			
7	Drop Test (Packaged)	Packing Weight (Kg) 0 ~ 45. 4 45. 4 ~ 90. 8 90. 8 ~ 454 Over 454 Drop direction :%1 corner / 3 edg	Drop Height (cm) 122 76 61 46 es / 6 sides each 1times		



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

