POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	:	**PTC**
SAMPLE CODE (Ver.)	:	**PS480272T-001-I-02 (Ver.0)**
MASS PRODUCTION CODE (Ver.)	:	**PH480272T-001-IA0Q (Ver.0)**
DRAWING NO. (Ver.)	:	**PH-07008-002 (Ver.0)**

Customer Approved

Date:

Approved	QC Confirmed	Designer
研發 2006-3-20. 黃秋蓮	Q.A. DEPT. MAR 14 2007 POWERTIP TECH CORP. 薛惠如 3/14.07	研發 2007-3-13. 彭文彥 / 研發 2007-3-14. 林昭俊

■ Approval For Specifications Only.

* This specification is subject to change without notice.

Please contact Powertip or it's representative before designing your product based on this specification.

☐ Approval For Specifications and Sample.

POWERTIP TECH. CORP.

Headquarters:

No.8, 6th Road, Taichung Industrial Park,
Taichung, Taiwan
台中市 407 工業區六路 8 號

TEL: 886-4-2355-8168
FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw
Http://www.powertip.com.tw

History of Version

Date	Ver.	Description	Page	Design by
2007/03/12	0	**MASS PRODUCTION**	-	Peter

Total : 31 Page

Contents

Note : For detailed information please refer to IC data sheet :

Primacy(TFT LCD) :Himax: HX8227-A01+ HX8655-A

(Or comparable IC)

1. SPECIFICATIONS

1.1 Features

LCM

Item	Standard Value
Display Type	480(R、G、B) * 272 Dots
LCD Type	Normally white , Transmissive type
Screen size(inch)	4.3 inch
Viewing Direction	6 O'clock
Color configuration	RGB-Strip
Backlight Type	LED B/L
Interface	Digital 24-bits RGB
Driver IC	HX8227-A01 + HX8655-A (Or comparable IC)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web side : http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5 (W) * 67.2 (L) * 3.95 (H)	mm

LCM

Item	Standard Value	Unit
Active Area	95.04 (W) * 53.856 (L)	mm
Pixel size	0.198 (W) * 0.198 (L)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	AVDD	GND=0	-0.3	6.0	V
	VCC	GND=0	-0.3	6.0	
	VGH-VGL	GND=0	-0.3	45.0	
Input Voltage	Vi	-	-0.3	VCC+0.3	V
Operating Temperature	T_{OP}	-	-20	70	°C
Storage Temperature	T_{ST}	-	-30	80	°C

1.4 DC Electrical Characteristics

Module Gnd = 0V , Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Digital Supply Voltage	VCC	-	2.3	2.5	3.6	V
Digital Operation Current	ICC	VCC= 2.5V Pattern=full display*1	-	12.1	20	mA
Analog Supply Voltage	VDD	-	4.8	5.0	5.2	V
Analog Operation Current	IDD	VDD= 5.0V Pattern=full display*1	-	14.5	25	mA
Gamma Voltage	Vn	-	0.2	-	VDD-0.2	V
Input H/L Level Voltage	VIH	-	0.7VCC	-	VCC	V
	VIL	-	0	-	0.3VCC	
Clock frequency	FCLK	-	-	9	15	MHz

*1:Maximum current display

1.5 Optical Characteristics

TFT LCD panel

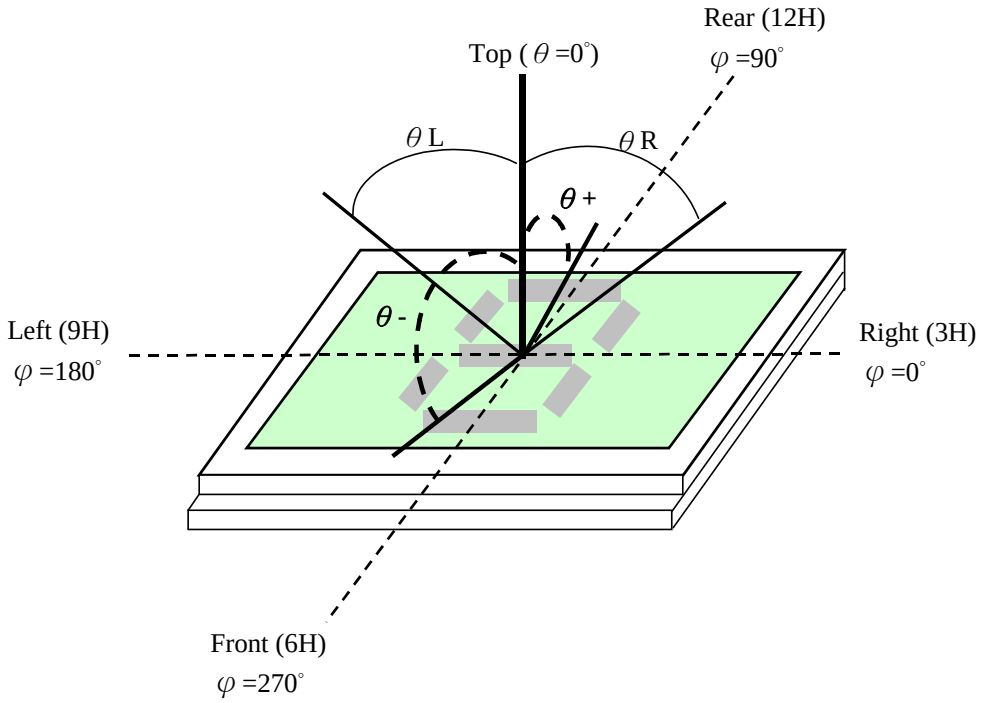
Item		Symbol	Condition	Min.	Typ.	Max.	unit	
Response time	Rise	Tr	Ta = 25°C θX, θY = 0°	-	15	20	ms	Note2
	Fall	Tf		-	35	50		
Color of CIE Coordinate*1	White	X		0.263	0.313	0.363	-	-
		Y		0.281	0.331	0.381		
	Red	X		0.551	0.601	0.651		
		Y		0.310	0.360	0.410		
	Green	X		0.299	0.349	0.399		
		Y		0.530	0.580	0.630		
	Blue	X		0.098	0.148	0.198		
		Y		0.056	0.106	0.156		
Viewing angle	Top	θY+	CR ≥ 10	-	45	-	deg.	Note1
	Bottom	θY-		-	50	-		
	Left	θX-		-	50	-		
	Right	θX+		-	50	-		
Contrast ratio		CR	Ta = 25°C	200	250	-	-	Note3
Average Brightness Pattern=white display (With LCD)		IV	IF= 20mA	240	280	-	cd/m^2	
Uniformity (With LCD)*1		△B	IF= 20mA	80	-	-	%	

*1：△B=B(min) / B(max).

Note 1.

Optical characteristics-2

Viewing angle

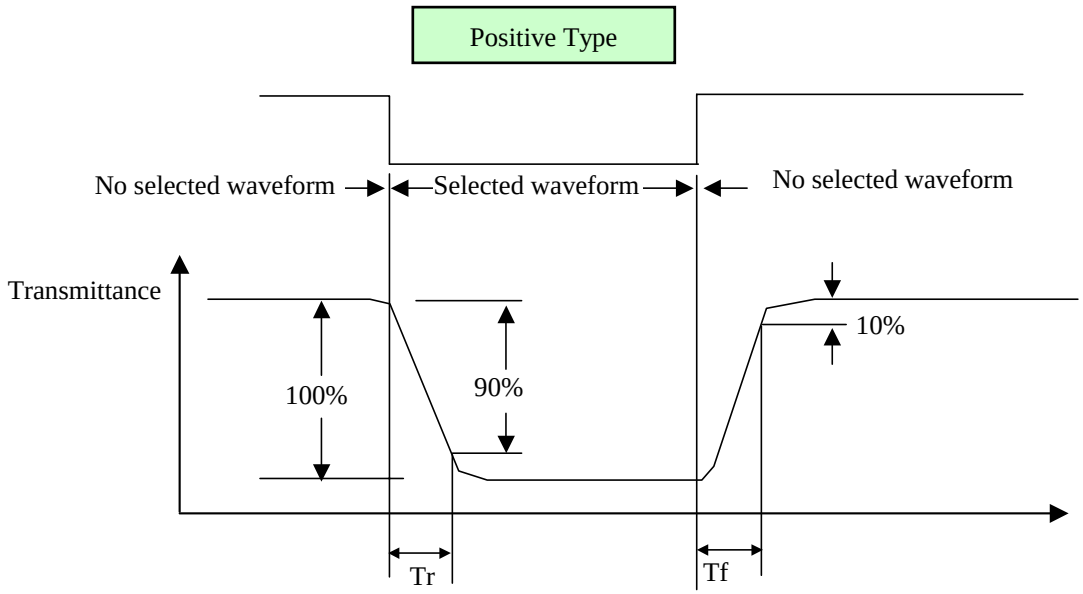


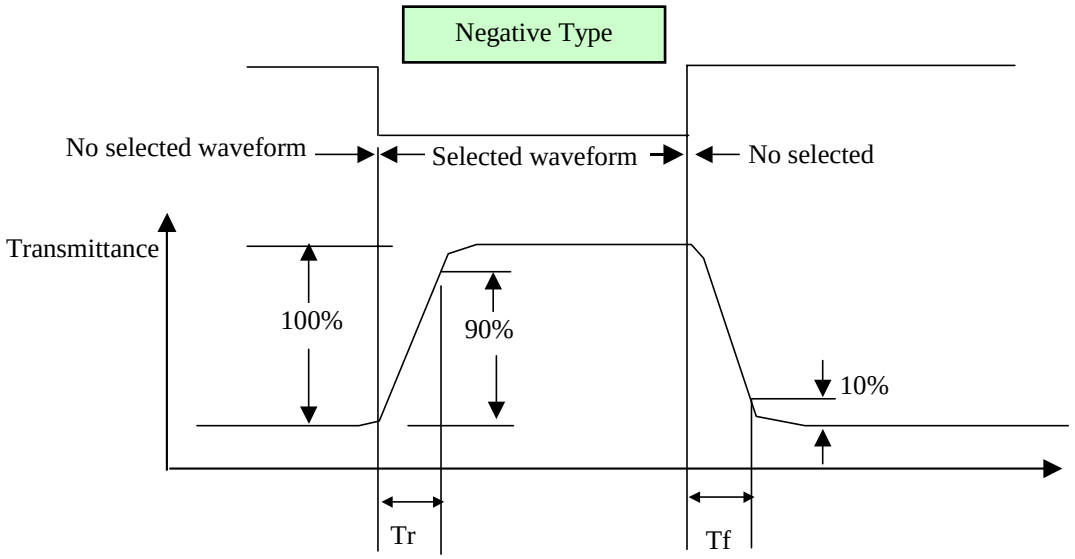
Viewing angle

Note 2.

Optical characteristics-3

Fig.2 Definition of response time





Electrical characteristics-2

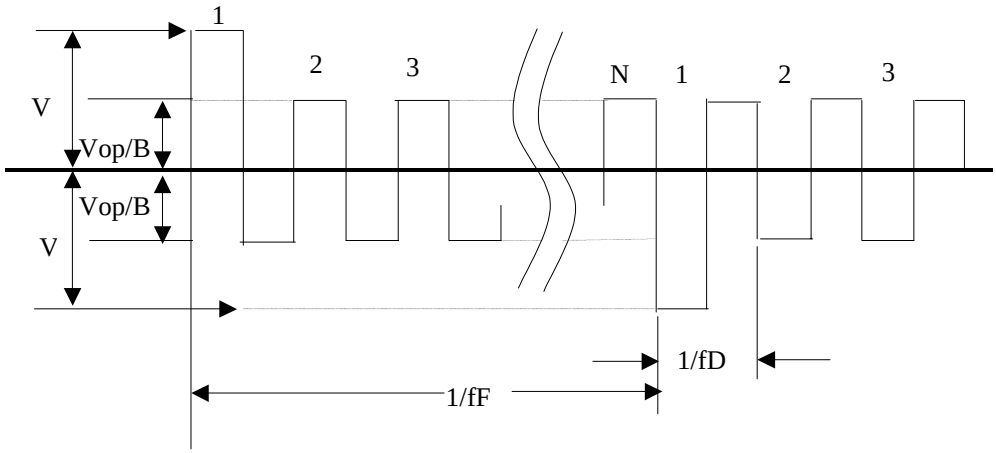
※2 Drive waveform

Vop: Drive voltage fF: Frame frequency

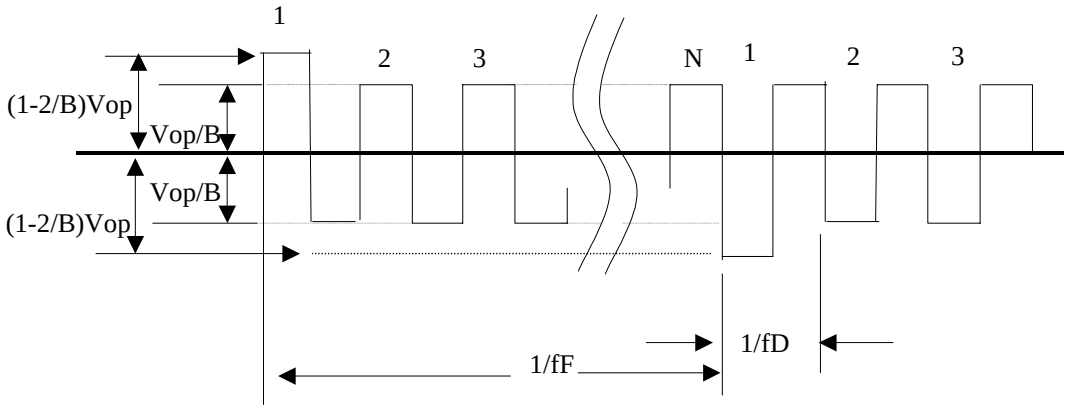
1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



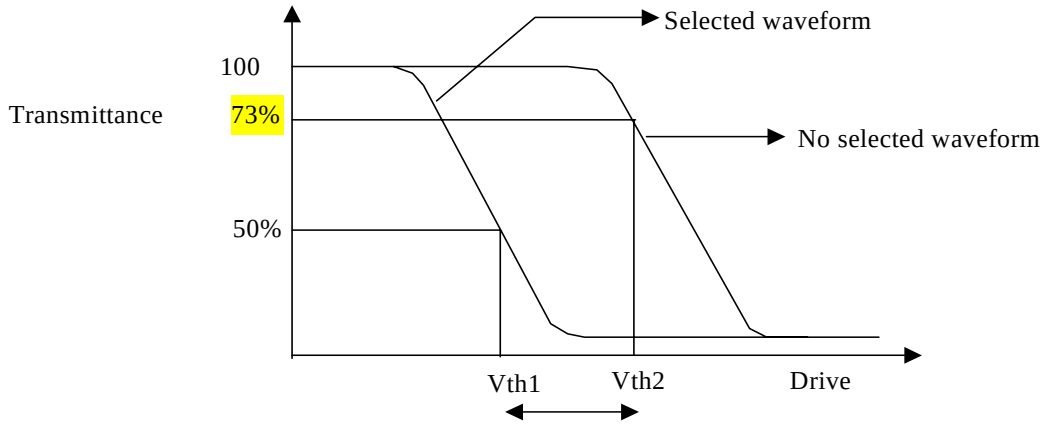
(2) Non- Selected waveform



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period

Note 3. : Definition of Vth

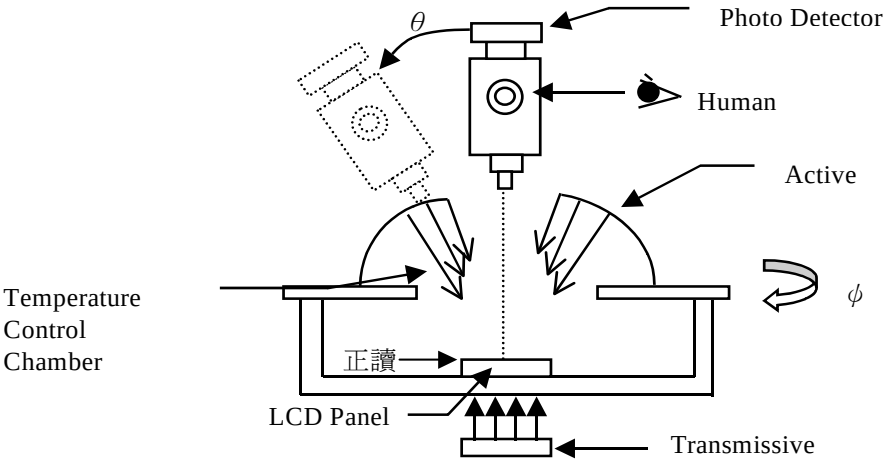


Active voltage range

	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio
= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System



Measuring System: Autronic DMS-803

1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Typ	Max.	Unit
Forward Current	I_F	Ta =25°C	-	20	-	mA
Forward Voltage	V_F	Ta =25°C	21.0	22.8	24.5	V
Power Dissipation	P_D	Ta =25°C	-	456	-	mW

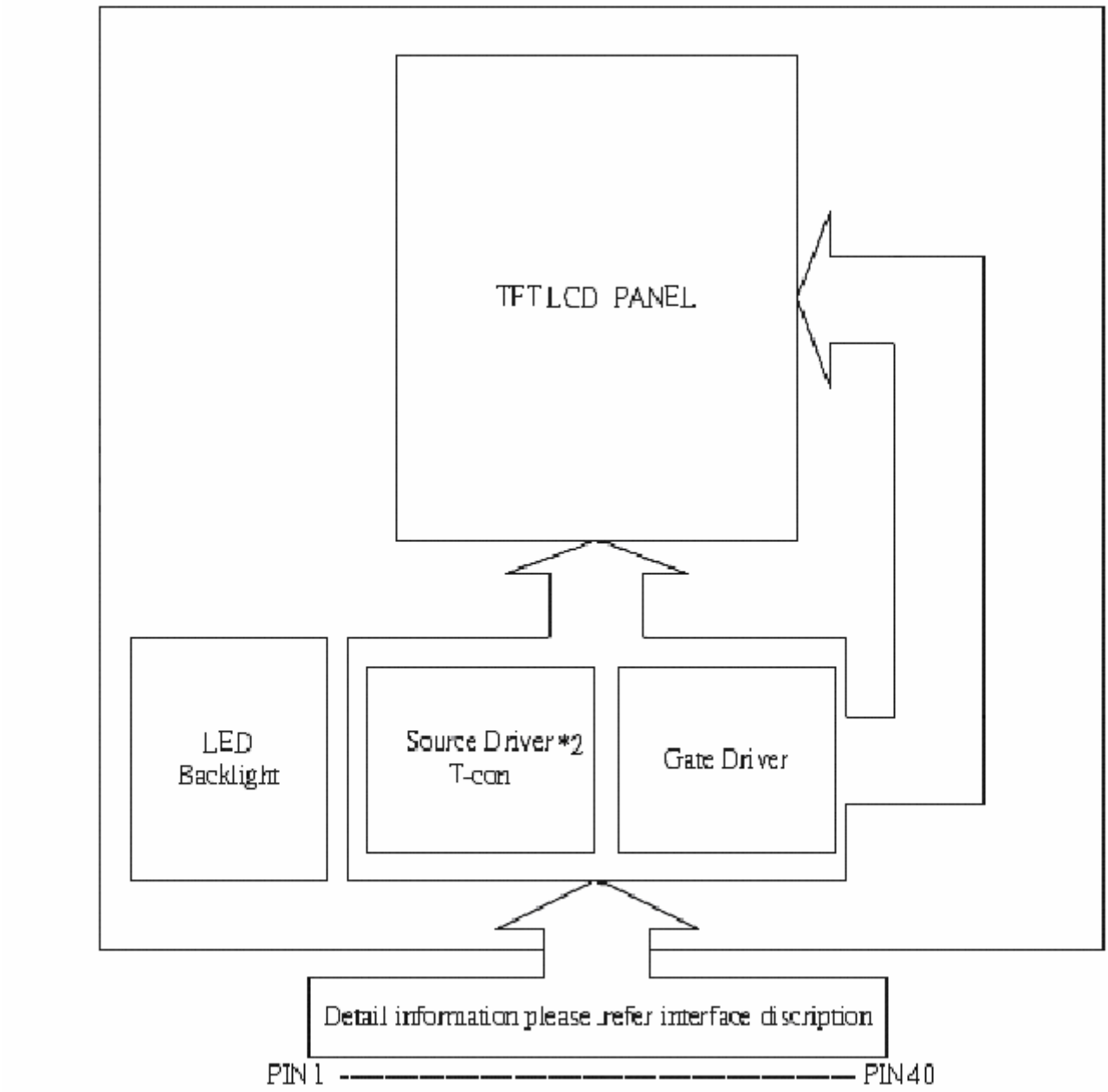
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

Pin No.	Symbol	Function
1	GND	Ground
2	GND	Ground
3	VCC	Digital power
4	VCC	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7

Pin No.	Symbol	Function
21	B0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	B3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7
29	GND	Ground
30	PCLK	Dot data clock
31	DISP	Display on/off mode control.
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data enable control
35	VDD	Analog power
36	VDD	Analog power
37	NC	Not used , Must be open
38	NC	Not used , Must be open
39	A	Power supply for LED Backlight anode input
40	K	Power supply for LED Backlight cathode input

2.3 Timing Characteristics

Timing Parameters 1

(480RGBx272, T_A =25oC, VCC=2.3 to 3.6V, GND= 0V)

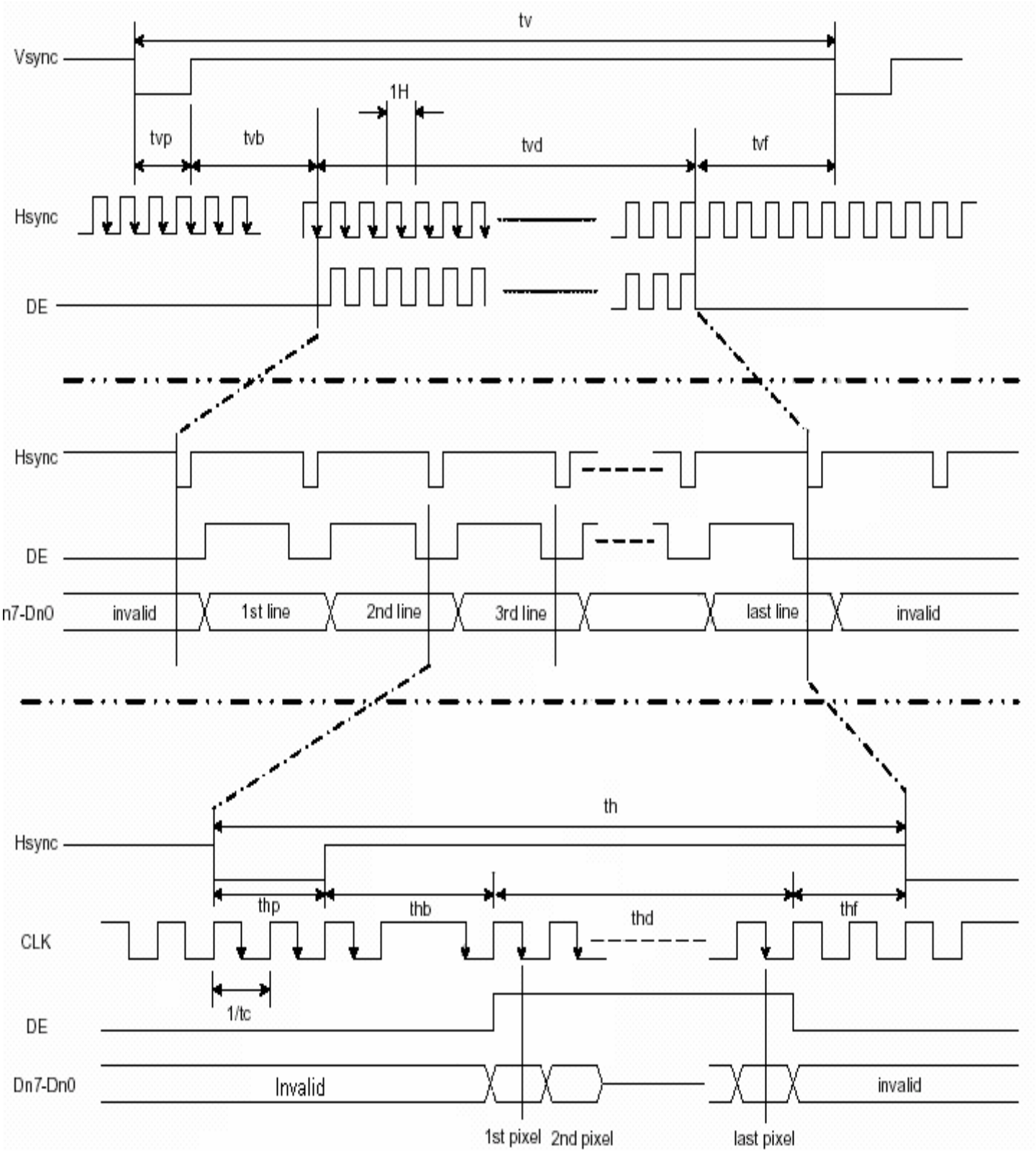
PARAMETER	Symbol	Min.	Typ.	Max.	Unit
Clock cycle	f_{CLK} (1)	-	9	15	MHz
Hsync cycle	1/th	-	17.14	-	KHz
Vsync cycle	1/tv	-	59.94	-	Hz
Horizontal Signal					
Horizontal cycle	th (2)	-	525	-	CLK
Horizontal display period	thd	-	480	-	CLK
Horizontal front porch	thf	2	-	-	CLK
Horizontal pulse width	thp	2	41	-	CLK
Horizontal back porch	thb	2	2	-	CLK
Vertical Signal					
Vertical cycle	tv	-	286	-	H
Vertical display period	tvd	-	272	-	H
Vertical front porch	tvf	1	2	-	H
Vertical pulse width	tvp	1	10	-	H
Vertical back porch	tvb	1	2	-	H

Note: (1) The table is parallel interface. Clock frequency and horizontal signal parameters are tripled in serial interface.

The maximum clock frequency of serial interface is 33MHz

(2) thd=480CLK, thf=2CLK, thp=41CLK, thb=2CLK, thf + thp + thb > 44CLK. (CLK=1/ fCLK ,H=th)

Timing Chart 1



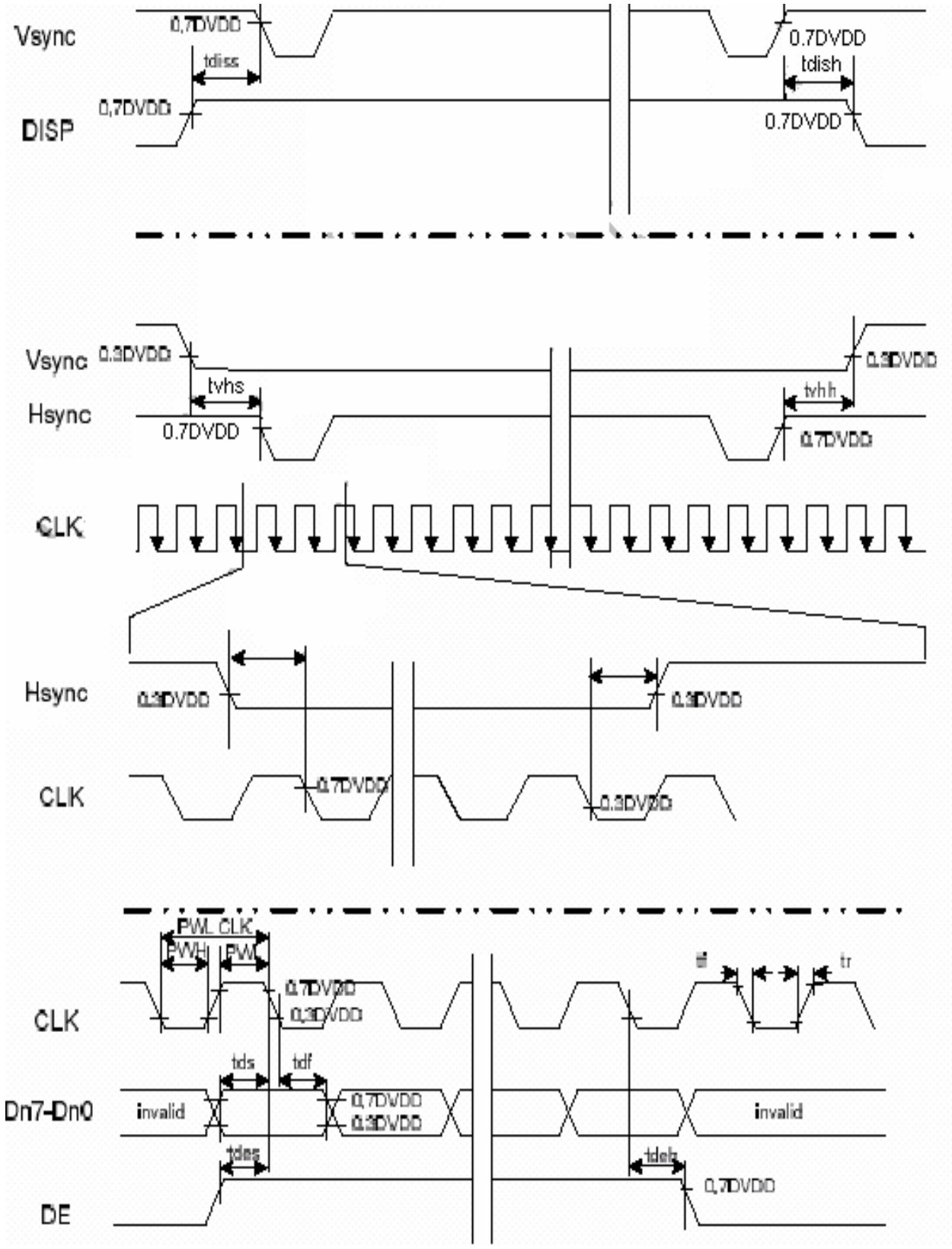
Timing Parameters 2

(T_A=25°C, VCC=2.3V to 3.6V , GND = 0V, tr [2]=tf [2]=2ns)

PARAMETER	Symbol	Min.	Typ.	Max.	Unit
DISP setup time	t_{diss}	10	-	-	ns
DISP hold time	t_{dish}	10	-	-	ns
Clock period	PW_{CLK} [1]	66.7	-	-	ns
Clock pulse high period	PWH [1]	26.7	-	-	ns
Clock pulse low period	PWL [1]	26.7	-	-	ns
Hsync setup time	t_{hs}	10	-	-	ns
Hsync hold time	t_{hh}	10	-	-	ns
Data setup time	t_{ds}	10	-	-	ns
Data hold time	t_{dh}	10	-	-	ns
DE setup time	t_{des}	10	-	-	ns
DE hold time	t_{deh}	10	-	-	ns
Vsync setup time	t_{vhs}	10	-	-	ns
Vsync hold time	t_{vhh}	10		-	ns

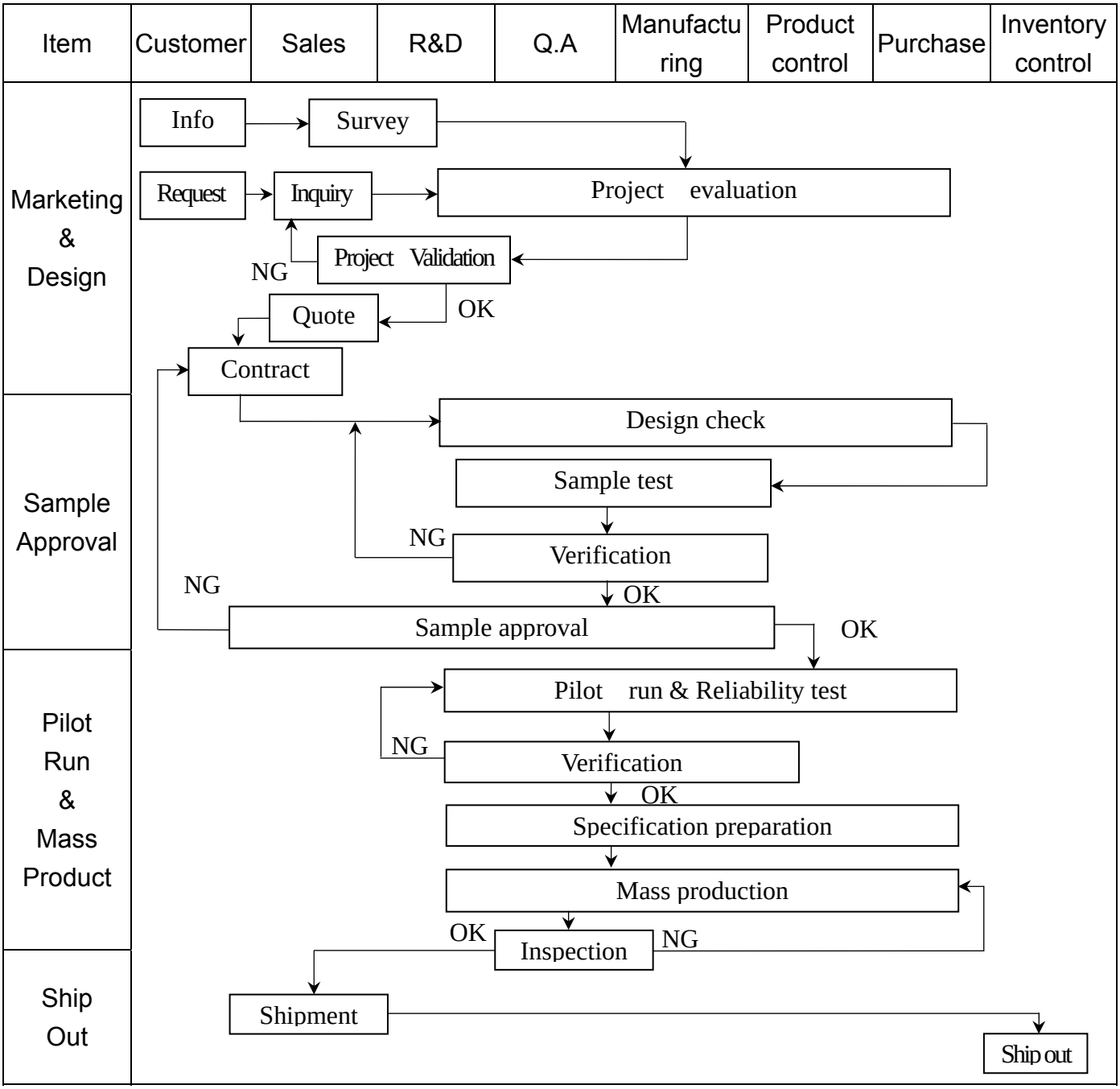
Note: (1) For parallel interface, maximum clock frequency is 15MHz.
(2) tr, tf is defined 10% to 90% of signal amplitude.

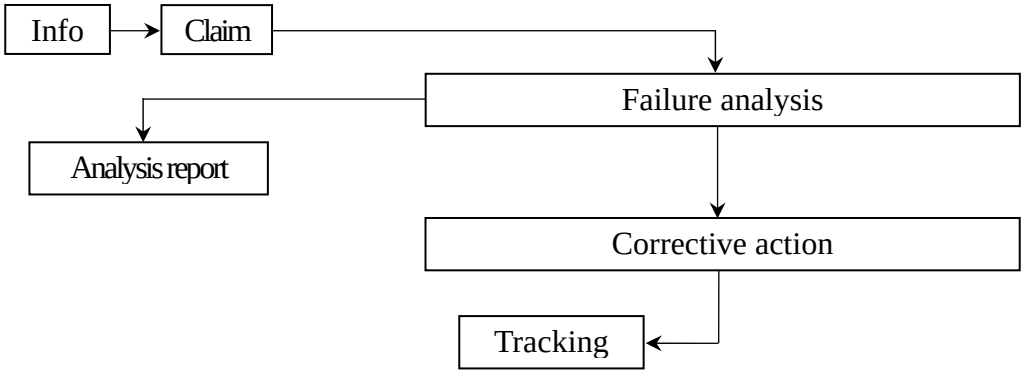
Timing Chart 2



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2 Inspection Specification

1. Inspection Specification

◆Scope ：The document shall be applied to TFT-LCD Module for 3.5″ ~10″ (Ver.02).

◆Inspection Standard：MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment：Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level：Major Defect AQL： 0.4 ； Minor Defect AQL： 1.5

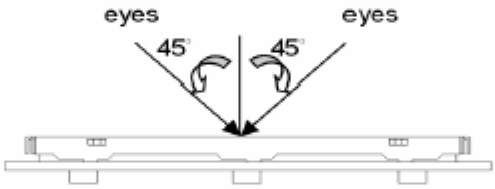
◆OUT Going Defect Level：Sampling.

◆Standard of the product appearance test：

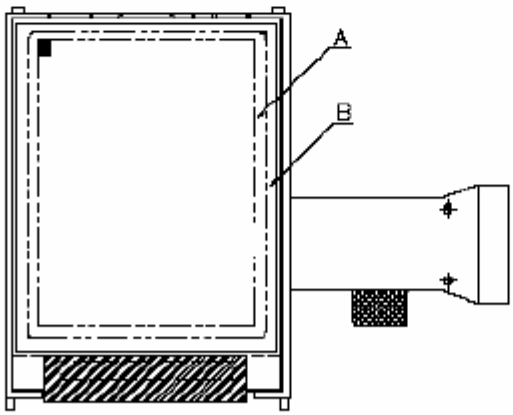
a. Manner of appearance test：

(1). The test best be under 20W×2 fluorescent light，and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection： (Unit：mm)

◆Specification For TFT-LCD Module 3.5″ ~10″ : (Ver.02)

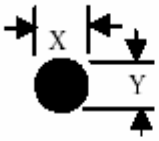
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="3">01</td><td rowspan="3">Product condition</td><td>1.1The part number is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>1.2 Mixed product types.</td><td>Major</td></tr>
<tr><td>1.3 Assembled in inverse direction.</td><td>Major</td></tr>
<tr><td>02</td><td>Quantity</td><td>2.1The quantity is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>03</td><td>Outline dimension</td><td>3.1 Product dimension and structure must conform to structure diagram.</td><td>Major</td></tr>
<tr><td rowspan="5">04</td><td rowspan="5">Electrical Testing</td><td>4.1 Missing line character and icon.</td><td>Major</td></tr>
<tr><td>4.2 No function or no display.</td><td>Major</td></tr>
<tr><td>4.3 Display malfunction.</td><td>Major</td></tr>
<tr><td>4.4 LCD viewing angle defect.</td><td>Major</td></tr>
<tr><td>4.5 Current consumption exceeds product specifications.</td><td>Major</td></tr>
<tr><td>05</td><td>Dot defect

(Bright dot、Dark dot)

On -display</td><td>
<table>
<tr><th colspan="2">Item</th><th>Acceptance (Q'ty)</th></tr>
<tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≦ 4</td></tr>
<tr><td>Dark Dot</td><td>≦ 5</td></tr>
<tr><td>Joint Dot</td><td>≦ 3</td></tr>
<tr><td>Total</td><td>≦ 7</td></tr>
</table>
5.1 Inspection pattern：full white , full black , Red , Green and blue screens.
5.2 It is defined as dot defect if defect area ＞1/2 dot.
5.3 The distance between two dot defect ≧5 mm.
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~10″ : (Ver.02)

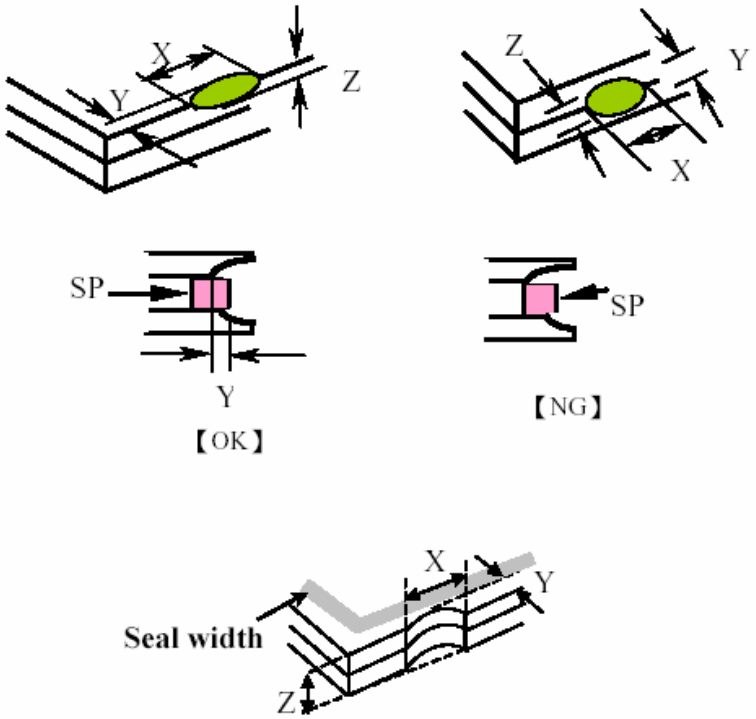
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>06</td><td>Black or white dot、scratch、contamination

Round type
 

Φ=(x+y) / 2

Line type
 
</td><td>6.1 Round type (Non-display or display) :
<table><tr><th>Dimension (diameter : Φ)</th><th>Acceptance (Q'ty)</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>5</td></tr><tr><td>Φ > 0.50</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table>
6.2 Line type(Non-display or display) :
<table><tr><th>Length (L)</th><th>Width (W)</th><th>Acceptance (Q'ty)</th></tr><tr><td>---</td><td>W ≦ 0.03</td><td>Ignore</td></tr><tr><td>L ≦10.0</td><td>0.03 < W ≦ 0.05</td><td>4</td></tr><tr><td>L ≦5.0</td><td>0.05 < W ≦ 0.10</td><td>2</td></tr><tr><td>---</td><td>W >0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
<tr><td>07</td><td>Polarizer Bubble</td><td><table><tr><th>Dimension (diameter : Φ)</th><th>Acceptance (Q'ty)</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>4</td></tr><tr><td>0.50 < Φ ≦ 0.80</td><td>1</td></tr><tr><td>Φ > 0.80</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
</table>

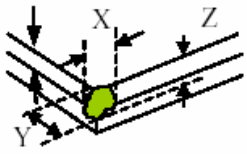
◆Specification For TFT-LCD Module 3.5″ ~10″ : (Ver. 02)

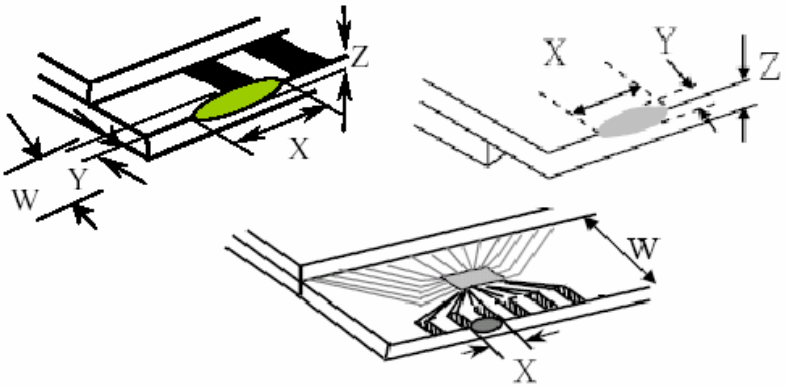
NO	Item	Criterion	Level
08	The crack of glass	Symbols : X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length 8.1 General glass chip : 8.1.1 Chip on panel surface and crack between panels:  X : ≦ a; Y : Crack can't enter viewing area; Z : ≦1/2 t X : ≦ a; Y : Crack can't exceed the half of SP width.; Z : 1/2 t < Z ≦2 t	Minor

◆Specification For TFT-LCD Module 3.5″ ~10″ : (Ver.02)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="2">08</td><td rowspan="2">The crack of glass</td><td>
Symbols :

X : The length of crack　　Y : The width of crack.
Z : The thickness of crack　　W : terminal length
t : The thickness of glass　　a : LCD side length

8.1.2 Corner crack :


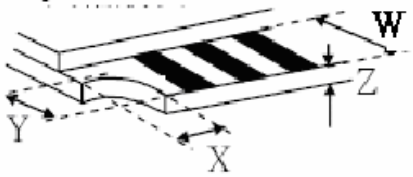
<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5 a</td><td>Crack can't enter viewing area</td><td>Z ≦ 1/2 t</td></tr>
<tr><td>≦1/5 a</td><td>Crack can't exceed the half of SP width.</td><td>1/2 t < Z ≦ 2 t</td></tr>
</table>
</td><td rowspan="2">Minor</td></tr>
<tr><td>
8.2 Protrusion over terminal :
8.2.1 Chip on electrode pad :


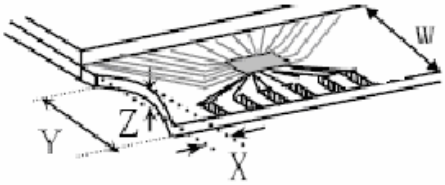
<table>
<tr><th></th><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>Front</td><td>≦ a</td><td>≦ 1/2 W</td><td>≦ t</td></tr>
<tr><td>Back</td><td>≦ a</td><td>≦ W</td><td>≦ 1/2 t</td></tr>
</table>
</td></tr>
</table>

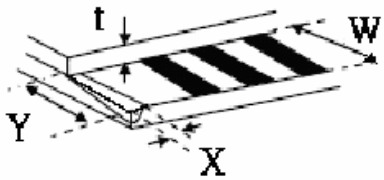
◆Specification For TFT-LCD Module 3.5″ ~10″ : (Ver.02)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>08</td><td>The crack of glass</td><td>
Symbols :

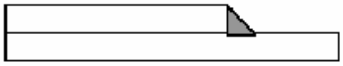
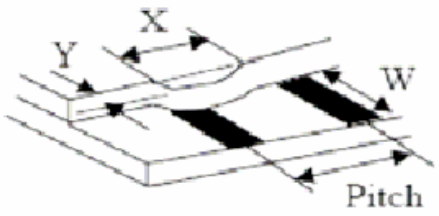
X : The length of crack　　Y : The width of crack.
Z : The thickness of crack　　W : terminal length
t : The thickness of glass　　a : LCD side length

8.2.2 Non-conductive portion :






<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ 1/3 a</td><td>≦ W</td><td>≦t</td></tr>
</table>
⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.

8.2.3 Glass remain :



<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ a</td><td>≦ 1/3 W</td><td>≦t</td></tr>
</table>
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~10″ : (Ver.02)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type、quantity、dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC .	Major
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≦1.5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition

Ver.02

<table>
<tr><th>NO.</th><th>TEST ITEM</th><th>TEST CONDITION</th></tr>
<tr><td>1</td><td>High Temperature
Storage Test</td><td>Keep in +80 ±2℃ 96 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>2</td><td>Low Temperature
Storage Test</td><td>Keep in -30 ±2℃ 96 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>3</td><td>High Temperature /
High Humidity
Storage Test</td><td>Keep in +60℃ / 90% R.H duration for 96 hrs
Surrounding temperature, then storage at normal condition 4hrs.
(Excluding the polarizer)</td></tr>
<tr><td rowspan="2">4</td><td rowspan="2">ESD Test</td><td>Air Discharge:
Apply 2 KV with 5 times
Discharge for each polarity +/-</td><td>Contact Discharge:
Apply 250 V with 5 times
discharge for each polarity +/-</td></tr>
<tr><td colspan="2">1. Temperature ambiance : 15℃～35℃
2. Humidity relative : 30%～60%
3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%
4. Discharge Resistance(Rd) : 330Ω±10%
5. Discharge, mode of operation :
Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)</td></tr>
<tr><td>5</td><td>Temperature Cycling
Storage Test</td><td>-20℃ → +25℃ → +70℃ → +25℃
(30mins) (5mins) (30mins) (5mins)
10 Cycle
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>6</td><td>Vibration Test
(Packaged)</td><td>1. Sine wave 10～55 Hz frequency (1 min)
2. The amplitude of vibration :1.5 mm
3. Each direction (X、Y、Z) duration for 2 Hrs</td></tr>
<tr><td>7</td><td>Drop Test
(Packaged)</td><td><table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table>Drop direction :※1 corner / 3 edges / 6 sides each 1times</td></tr>
</table>

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands , this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 280 ± 10°C and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.

5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush , shake , or jolt the module.

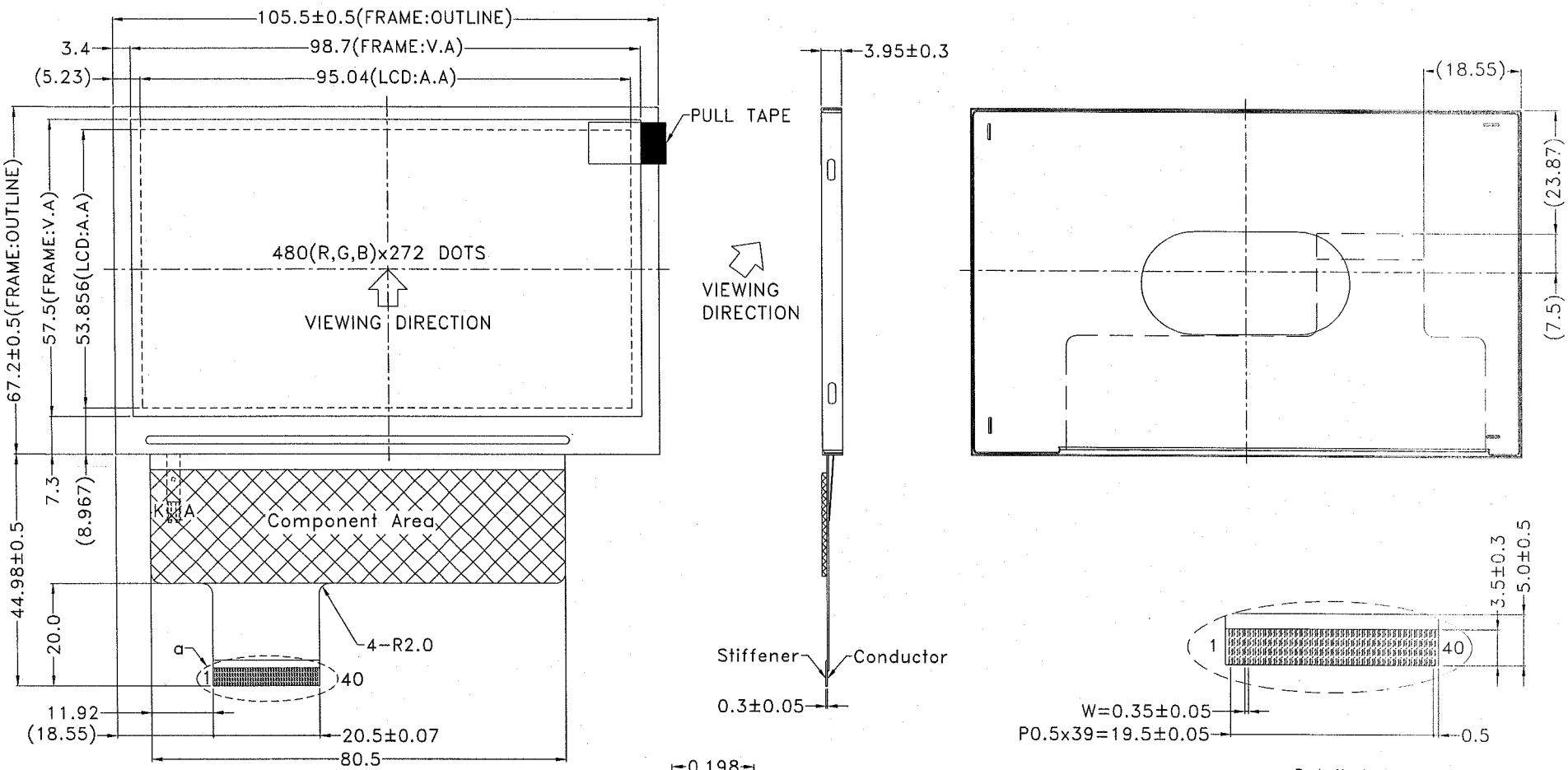
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company’s specification as a part for use in your company’s general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



NOTES:
1.LCD TYPE: a-Si TFT
2.LCD DISPLAY:POSITIVE/TRANSMISSIVE
3.VIEW DIRECTION: 6 O'CLOCK
4.Top: -20~70°C Tst:-30~80°C
5.The tolerance unless classified ±0.2mm
6.IC NO.: Source:HX8227-A01(2ea)
Gate:HX8655-A(1ea)
7. FPC Component Area(H=1.3Max)

REV	DESCRIPTION	DATE

久正光電股份有限公司
POWERTIP TECHNOLOGY CORPORATION

				APPROVED	CHECKER	DRAWN
SCALE:1/1.2	UNIT:mm	PAGE:1/1		研發 2006-3-20. 黃秋蓮	研發 2007-3-9. 李美倫	研發 2007-3-9. 賴銘佶
圖面名稱	PH480272T-001-IA0Q					
圖面編號	PH-07008-002	EDI	0			

LCM Model	PH480272T-001-IA0Q
Ver.0	

LCM Packaging Specifications

(For Tray)

Approve	Check	Contact
研發 2006-3-20. 吉敏達	研發 2007-3-9. 李美倫	研發 2007-3-9. 賴銘信

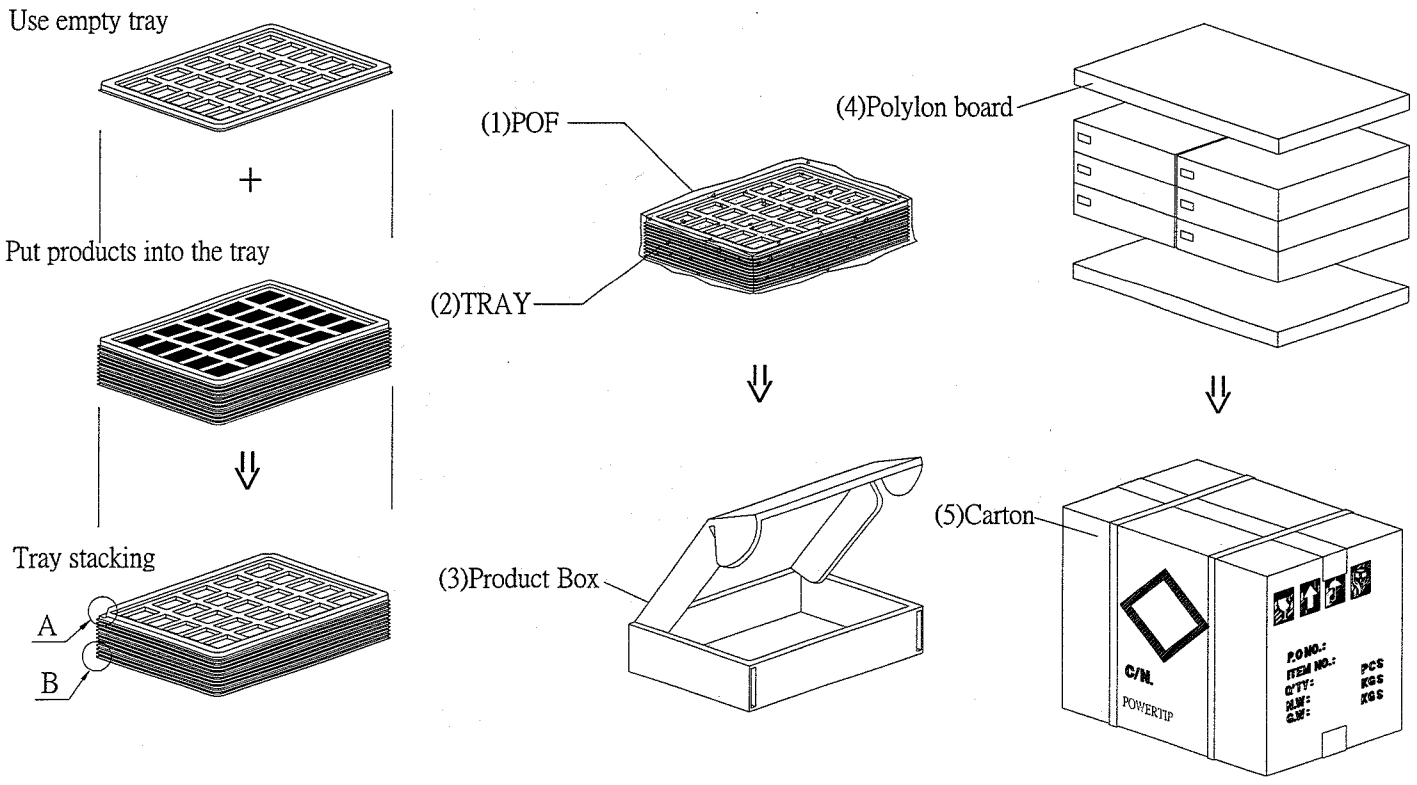
1.Packaging Material : per carton

No.	Item	Model	Dimensions (mm)	Quantity
1	LCM	PH480272T-001-IA0Q	105.5 X 67.2	144
2	(1)POF	OTFILM0BA03ABA	19"X350X0.015	6
3	(2)TRAY	TYPH48027201BA	352 X 260 X 12.8	42
4	(3)Product Box	BX36627063ABBA	393 X 274 X 68	6
5	(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	2
6	(5)Carton	BX57041027CCBA	570 X 410 X 265	1
7				
8				
9				

2.Packaging Specifications and Quantity :

(1)LCM quantity per box : no per tray 4 x no of tray 6 = 24

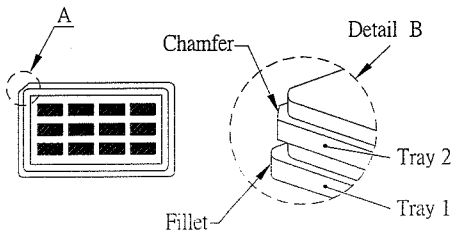
(2)Total LCM quantity in carton : quantity per box 24 x no of boxes 6 = 144



R E M A R K

1. Label Specifications :

MODEL:
LOT NO:
QUANTITY:
CHECK:



2.Rotate tray 180 degrees and place on top of stack. Check the tray stack using Fig. B.

3.It's also suitable to Panel

4.TRAY Number : PH480272T-001