

26 February 2025

Product data sheet

1. General description

NPN/PNP general-purpose double transistors in a very small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Simplified circuit design
- Reduced component count
- Reduced pick and place costs
- AEC-Q101 qualified

3. Applications

General-purpose switching and amplification

4. Quick reference data

Table 1. Quick	reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transistor; for the PNP transistor with negative polarity							
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
I _C	collector current			-	-	150	mA

5. Pinning information

Table 2. F	inning infor	nation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	C2	collector TR2		B2 B1 E1
2	E2	emitter TR2		
3	C1	collector TR1		
4	E1	emitter TR1		TR2
5	B1	base TR1	TSOP6 (SOT457)	C2 E2 C1
6	B2	base TR2		sym082



6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PIMZ2	TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	<u>SOT457</u>		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PIMZ2	Мб

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or; for the PNP transistor wit	h negative polarity				
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	7	V
I _C	collector current			-	150	mA
I _{CM}	peak collector current			-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB).

9. Thermal characteristics

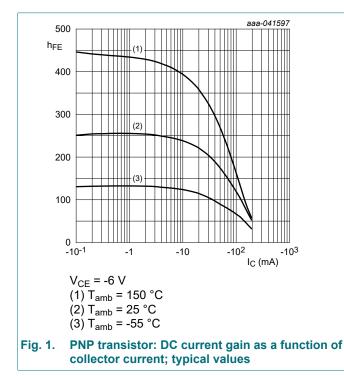
Table 6. Th	ermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transis	stor						
R _{th(j-a)}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C	[1]	-	-	625	K/W
Per device	!						
R _{th(j-a)}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C	[1]	-	-	417	K/W

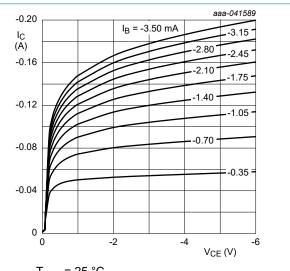
[1] Device mounted on an FR4 printed-circuit board.

PIMZ2

10. Characteristics

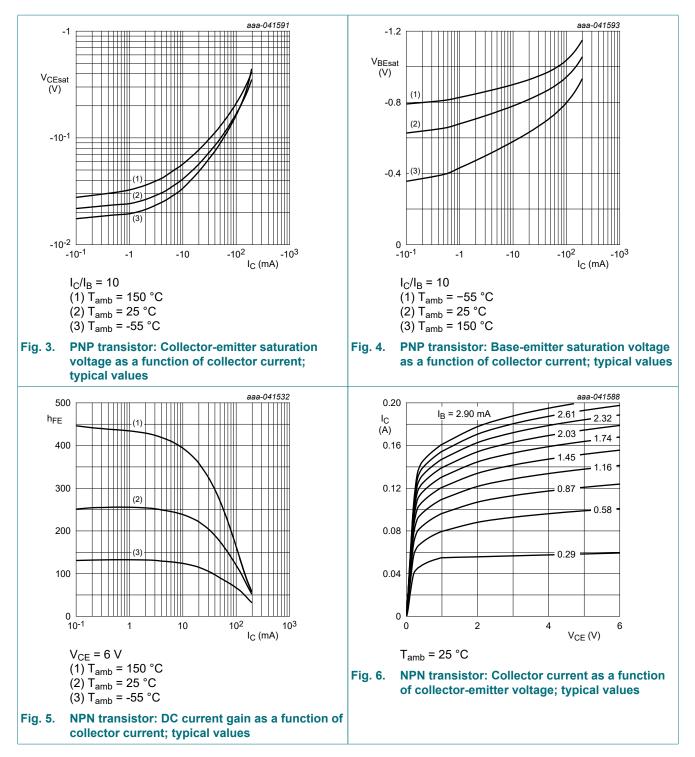
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or; for the PNP transistor	with negative polarity; unless otherwise	specif	ied			
I _{CBO}	collector-base cut-off	V_{CB} = 60 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
	current	V _{CB} = 60 V; I _E = 0 A; T _j = 150 °C; T _{amb} = 25 °C		-	-	50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 7 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 6 V; I _C = 1 mA; T _{amb} = 25 °C		120	250	560	
TR1 (PNP)	·						
V _{CEsat}	collector-emitter saturation voltage	I_{C} = -50 mA; I_{B} = -5 mA; T_{amb} = 25 °C		-	-	-500	mV
C _c	collector capacitance	V _{CB} = -12 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	2.3	5	pF
f _T	transition frequency	V_{CE} = -12 V; f = 100 MHz; T_{amb} = 25 °C; I_E = -2 mA		-	190	-	MHz
TR2 (NPN)							
V _{CEsat}	collector-emitter saturation voltage	I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C		-	-	250	mV
C _c	collector capacitance	$V_{CB} = 12 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$		-	-	3	pF
f _T	transition frequency	V_{CE} = 12 V; f = 100 MHz; T_{amb} = 25 °C; I _E = 2 mA		100	-	-	MHz

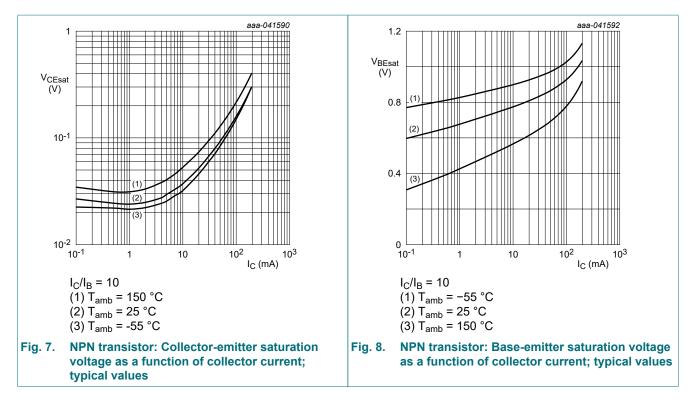










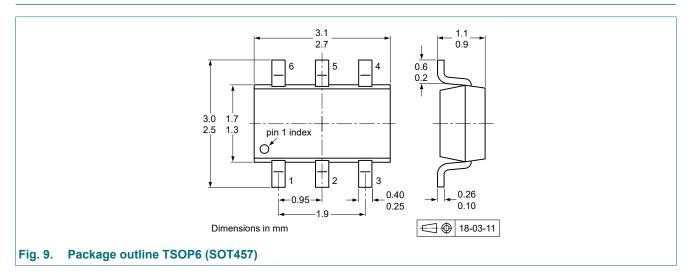


11. Test information

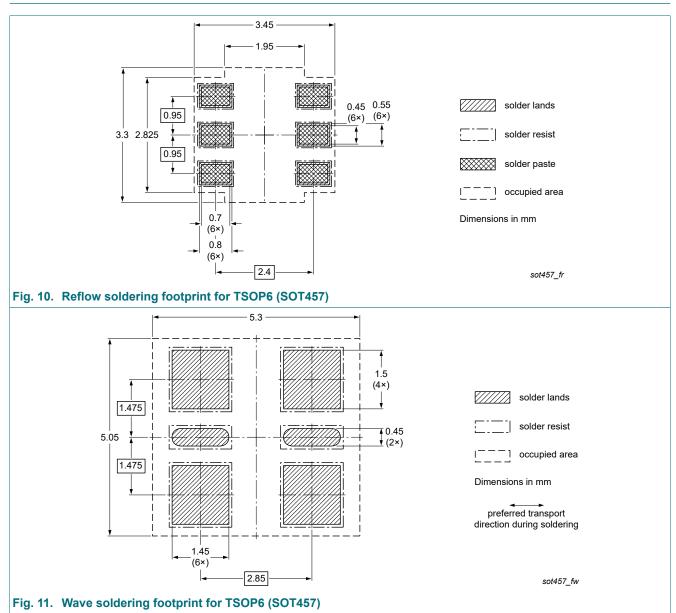
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PIMZ2 v.7	20250226	Product data sheet	-	PIMZ2_PUMZ2_6
Modification:	 The format of t Nexperia. Legal texts have 	eet reduced to single type data his data sheet has been redes ve been adapted to the new co s: Figures 1 - 8 added	igned to comply with	
PIMZ2_PUMZ2_6	20091117	Product data sheet	-	PIMZ2_PUMZ2_5
PIMZ2_PUMZ2_5	20041124	Product data sheet	-	PIMZ2_PUMZ2_4
PIMZ2_PUMZ2_4	20031217	Product specification	-	PIMZ2_2
PIMZ2_2	20030714	Product specification	-	PIMZ2_1
PIMZ2 1	20030602	Objective specification	-	_

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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NPN/PNP general-purpose double transistor

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Contents

1.	General description	1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	1
5.	Pinning information	1
6.	Ordering information	2
7.	Marking	. 2
8.	Limiting values	. 2
	Thermal characteristics	
10.	Characteristics	3
11.	Test information	. 5
12.	Package outline	. 5
	Soldering	
	Revision history	
	Legal information	
	-	

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