PAN	ĴΤ
	SEMI CONDUCTOR

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PJA3400



Current

4.9A

Features

Voltage

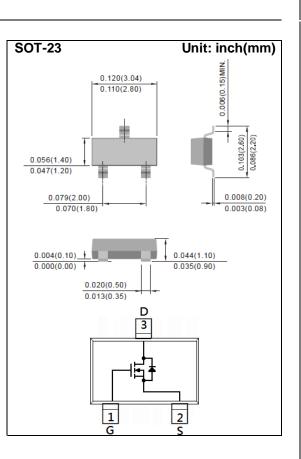
- Rds(ON), Vgs@10V, Id@4.9A<38mΩ
- RDS(ON) , VGS@4.5V, ID@3.5A<44mΩA

30 V

- Rds(ON) , Vgs@2.5V, Id@2.7A<60mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A00



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	4.9	А
Pulsed Drain Current		I _{DM}	19.6	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _{eja}	100	°C/W



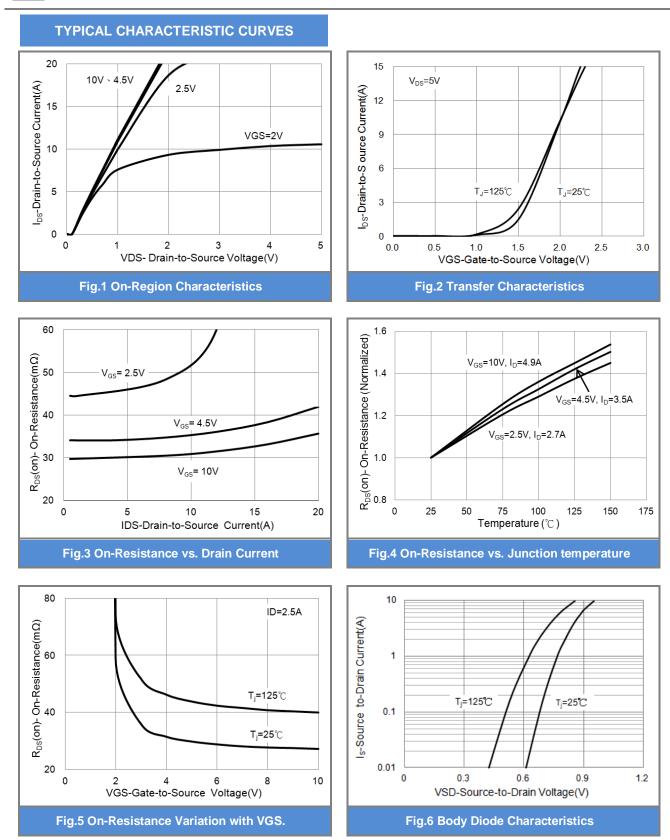
Electrical Characteristics (T_A=25°C unless otherwise noted)

		TEST CONDITION	RAINI	TVD		
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V
•				0.84	- 1.3	V
Gate Threshold Voltage Drain-Source On-State Resistance	V _{GS(th)} R _{DS(on)}	$V_{DS}=V_{GS}$, $I_D=250uA$	0.5			mΩ
		V _{GS} =10V, I _D =4.9A	-	28	38	
		V _{GS} =4.5V, I _D =3.5A	-	32	44	
		V _{GS} =2.5V, I _D =2.7A	-	45	60	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	0.01	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	V _{DS} =15V, I _D =4.9A, V _{GS} =10V ^(Note 1,2)	-	5.7	-	nC
Gate-Source Charge	Q_{gs}		-	1.1	-	
Gate-Drain Charge	Q_gd		-	1.5	-	
Input Capacitance	Ciss	V _{DS} =15V, V _{GS} =0V,	-	490	-	pF
Output Capacitance	Coss		-	44	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	32	-	
Switching						
Turn-On Delay Time	td _(on)		-	2	-	
Turn-On Rise Time	tr	V _{DD} =15V, I _D =4.9A, V _{GS} =10V,	-	57	-	ns
Turn-Off Delay Time	td _(off)		-	78	-	
Turn-Off Fall Time	tf	$R_G=3\Omega^{(Note 1,2)}$	-	79	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	1				4 5	^
Diode Forward Current	I _S		-	-	1.5	A
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	0.77	1.2	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{®JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited







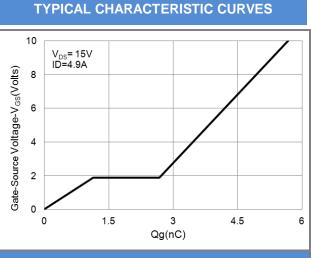


Fig.7 Gate-Charge Characteristics

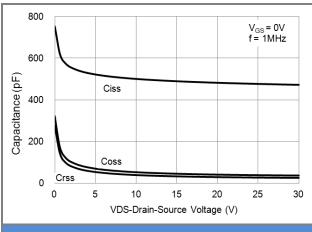


Fig.9 Capacitance vs. Drain-Source Voltage.

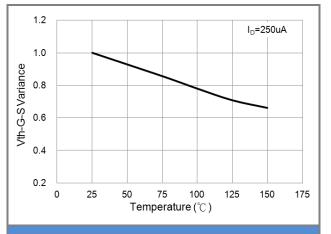


Fig.8 Threshold Voltage Variation with Temperature

July 1,2015-REV.01



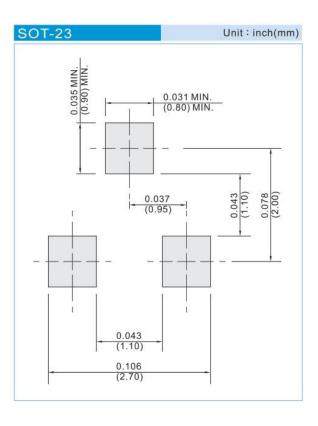




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJA3400_R1_00001	SOT-23	3K pcs / 7" reel	A00	Halogen free
PJA3400 _R2_00001	SOT-23	12K pcs / 13" reel	A00	Halogen free

MOUNTING PAD LAYOUT







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