



PJA65P03

30V P-CHANNEL ENHANCEMENT MODE MOSFET

VOLTAGE 30 Volts **CURRENT** 4 Amperes

SOT-23 Unit : inch(mm)

FEATURES

- $R_{DS(ON)}$, $V_{GS}@-2.5V, I_D@-1A < 9\text{ m}\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V, I_D@-4A < 65\text{ m}\Omega$
- $R_{DS(ON)}$, $V_{GS}@-10V, I_D@-4.2A < 55\text{ m}\Omega$
- Advanced Trench Process Technology
- High Density Cell Design For Ultra Low On-Resistance
- Specially Designed for DC/DC Converters
- Low Gate Charge
- Lead free in comply with EU RoHS 2002/95/EC directives.
- Green molding compound as per IEC61249 Std. . (Halogen Free)

MECHANICAL DATA

- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Apporx. Weight : 0.0003 ounces, 0.0084grams
- Marking : 65

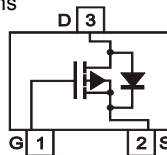
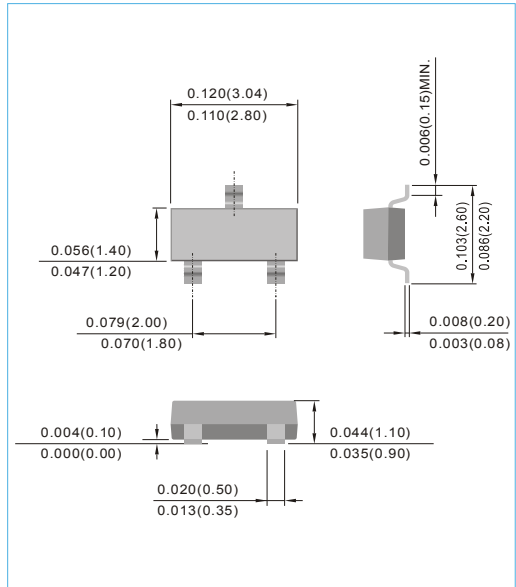


Fig.80 (TOP VIEW)



MAXIMUM RATINGS AND THERMAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current (Notes 1)	I_D	-4	A
Pulsed Drain Current	I_{DM}	-30	A
Power Dissipation (Notes 1)	P_D	1.36	W
Typical Thermal Resistance (Notes 1)	$R_{\theta JA}$	125	$^\circ\text{C/W}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to + 150	$^\circ\text{C}$

NOTES:

1. Mounted on 1 in² FR-4 PCB .



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ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-0.7	-1	-1.3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = -10V, I _D = -4.2A	-	46	55	mΩ
		V _{GS} = -4.5V, I _D = -4A	-	56	65	
		V _{GS} = -2.5V, I _D = -1A	-	79	9J	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -24V, V _{GS} =0V	-	-	-1	μA
Gate -Source Leakage Current	I _{GSS}	V _{GS} = ±12V, V _{DS} =0V	-	-	±100	nA
Diode Forward Voltage	V _{SD}	I _S = -1A, V _{GS} =0V	-	-0.8	-1	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} = -15V, I _D = -4A V _{GS} = -4.5V	-	8	-	nC
Gate-Source Charge	Q _{gs}		-	2	-	
Gate-Drain Charge	Q _{gd}		-	2	-	
Turn-On Delay Time	td _{on}	V _{DS} = -15V, V _{GS} = -10V, R _G = 6.8Ω, R _L = 3.7Ω	-	1.2	-	ns
Turn-Off Delay Time	td _{off}		-	50.5	-	
Turn-On Rise Time	t _r		-	23.4	-	
Turn-Off Fall Time	t _f		-	14.2	-	
Input Capacitance	C _{iss}	V _{DS} = -15V, V _{GS} =0V f=1.0MHz	-	776	-	pF
Output Capacitance	C _{oss}		-	70	-	
Reverse Transfer Capacitance	C _{rss}		-	61	-	



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RATING AND CHARACTERISTIC CURVES

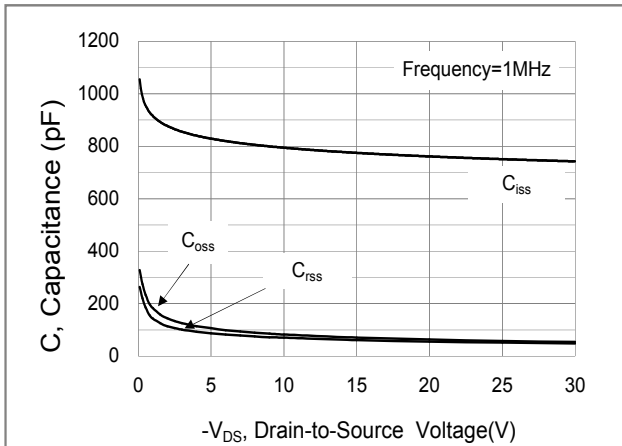


Fig.1 Capacitance Characteristic

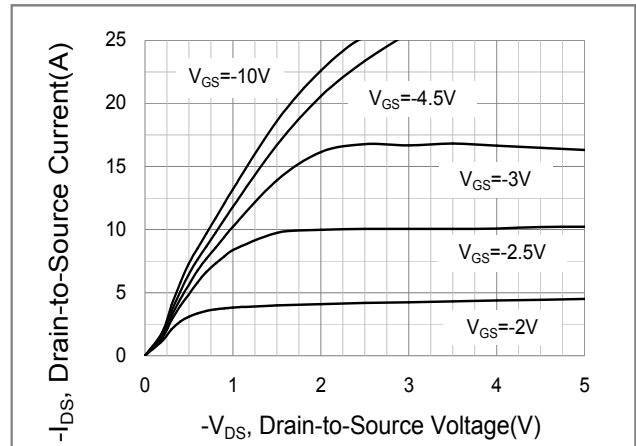


Fig.2 Output Characteristic

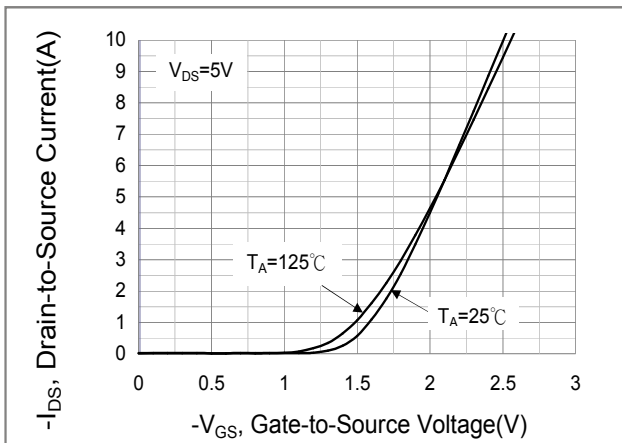


Fig.3 Transfer Characteristic

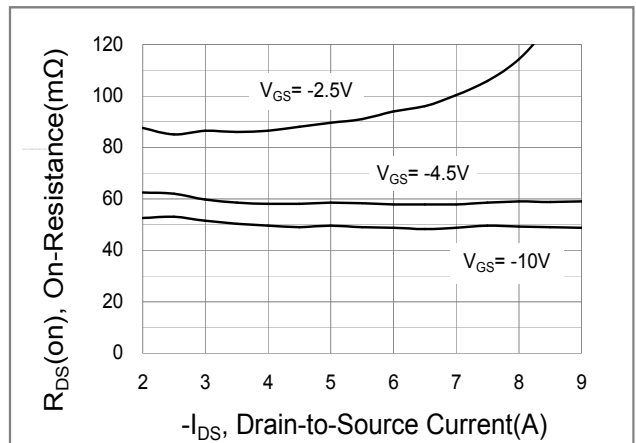


Fig.4 On-Resistance vs. Drain Current

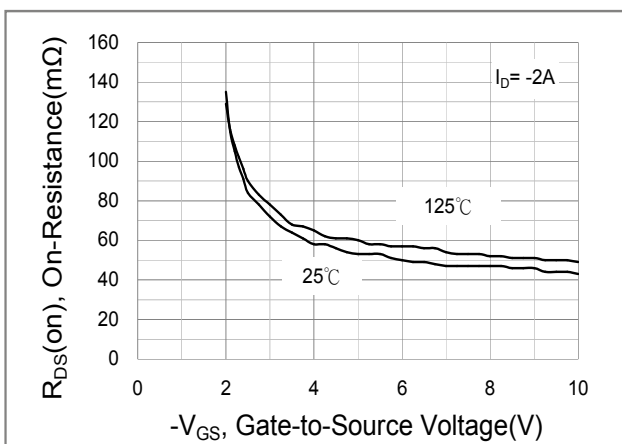


Fig.5 On-Resistance vs. Gate-to-Source Voltage

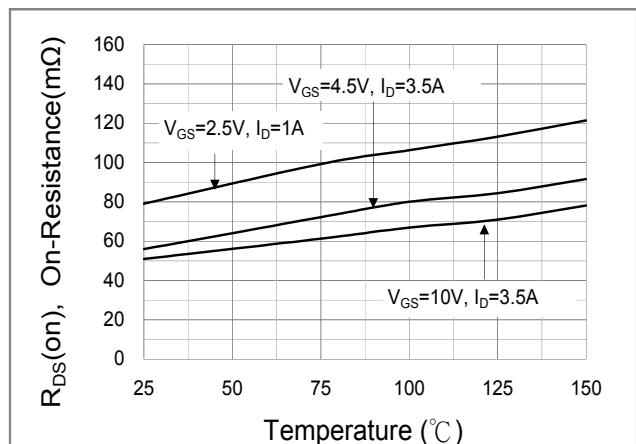


Fig.6 On-Resistance vs. Junction Temperature



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RATING AND CHARACTERISTIC CURVES

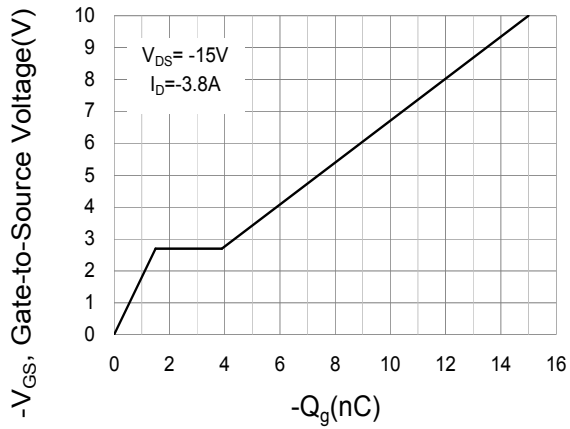


Fig.7 Gate-Charge Characteristic

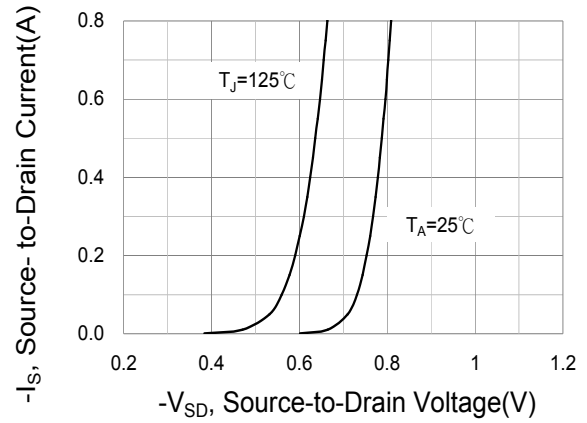
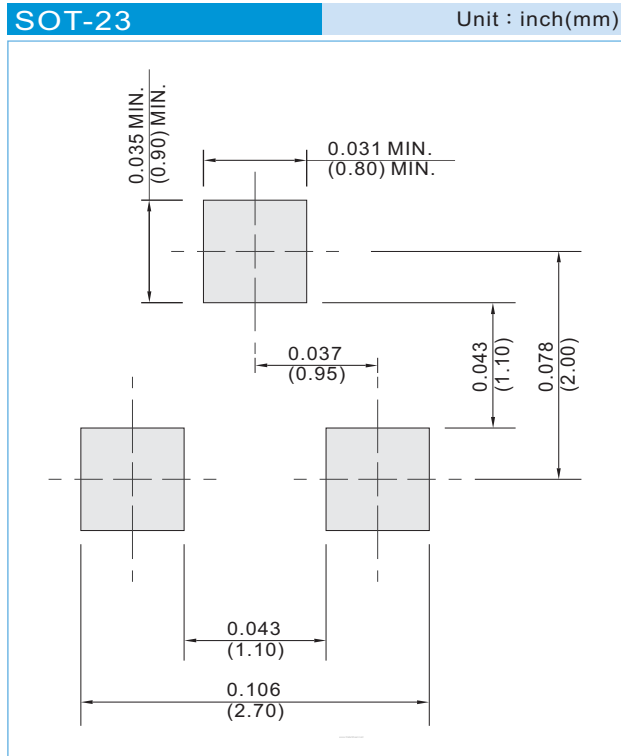


Fig.8 Body Diode Characteristic



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MOUNTING PAD LAYOUT



ORDER INFORMATION

- Packing information
 - T/R - 12K per 13" plastic Reel
 - T/R - 3K per 7" plastic Reel



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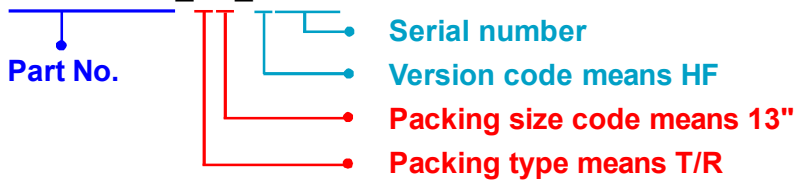
Part No_packing code_Version

PJA65P03_R1_00001

PJA65P03_R2_00001

For example :

RB500V-40_R2_00001



Packing Code XX				Version Code XXXXX		
Packing type	1 st Code	Packing size code	2 nd Code	HF or RoHS	1 st Code	2 nd ~5 th Code
Tape and Ammunition Box (T/B)	A	N/A	0	HF	0	serial number
Tape and Reel (T/R)	R	7"	1	RoHS	1	serial number
Bulk Packing (B/P)	B	13"	2			
Tube Packing (T/P)	T	26mm	X			
Tape and Reel (Right Oriented) (TRR)	S	52mm	Y			
Tape and Reel (Left Oriented) (TRL)	L	PANASERT T/B CATHODE UP (PBCU)	U			
FORMING	F	PANASERT T/B CATHODE DOWN (PBCD)	D			



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