



PJD10P10A

100V P-Channel Enhancement Mode MOSFET

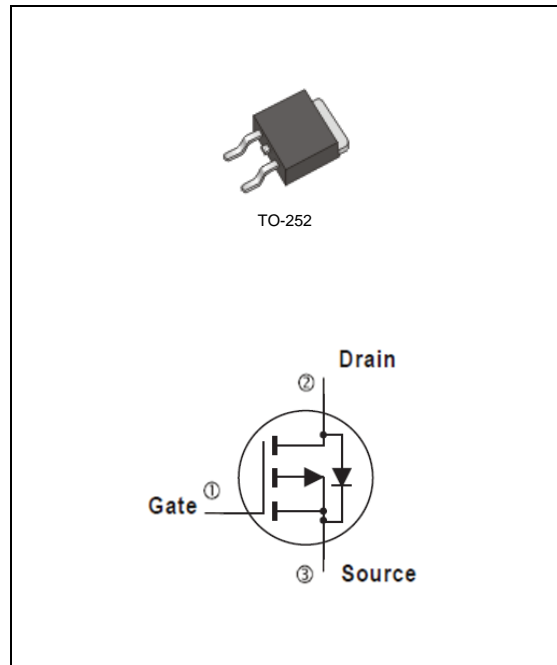
Voltage -100 V **Current** -10 A

Features

- $R_{DS(ON)}, V_{GS}@-10V, I_D@-5A < 210m\Omega$
- $R_{DS(ON)}, V_{GS}@-4.5V, I_D@-3A < 230m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: TO-252 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0104 ounces, 0.297grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-100	V
Gate-Source Voltage		V_{GS}	+20	V
Continuous Drain Current	$T_C=25^\circ\text{C}$	I_D	-10	A
	$T_C=100^\circ\text{C}$		-6	
Pulsed Drain Current ^(Note 1)		I_{DM}	-40	
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	54	W
	$T_C=100^\circ\text{C}$		22	
Continuous Drain Current	$T_A=25^\circ\text{C}$	I_D	-2.0	A
	$T_A=70^\circ\text{C}$		-1.6	A
Power Dissipation		P_D	2.0	W
Power Dissipation			$T_A=70^\circ\text{C}$	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal resistance ^(Note 4,5)	Junction to Case	$R_{\theta JC}$	2.3	$^\circ\text{C/W}$
	Junction to Ambient	$R_{\theta JA}$	62.5	

- Limited only By Maximum Junction Temperature



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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	-1.9	-3.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-5A$	-	170	210	m Ω
		$V_{GS}=-4.5V, I_D=-3A$	-	190	230	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-100V, V_{GS}=0V$	-	-	-1.0	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	$V_{DS}=-80V, I_D=-5A,$ $V_{GS}=-10V$ (Note 1,2)	-	20	-	nC
Gate-Source Charge	Q_{gs}		-	3.5	-	
Gate-Drain Charge	Q_{gd}		-	4.6	-	
Input Capacitance	C_{iss}	$V_{DS}=-25V, V_{GS}=0V,$ $f=1.0\text{MHz}$	-	1419	-	pF
Output Capacitance	C_{oss}		-	89	-	
Reverse Transfer Capacitance	C_{rss}		-	45	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-50V, I_D=-5A,$ $V_{GS}=-10V, R_G=25\Omega$ (Note 1,2)	-	18	-	ns
Turn-On Rise Time	t_r		-	8	-	
Turn-Off Delay Time	$t_{d(off)}$		-	100	-	
Turn-Off Fall Time	t_f		-	30	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	-10	A
Reverse Recovery Time	V_{SD}	$I_S=-1A, V_{GS}=0V$	-	-0.74	-1.2	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature $T_J(\text{MAX})=150^\circ\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ\text{C}$.
4. The maximum current rating is package limited
5. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
6. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

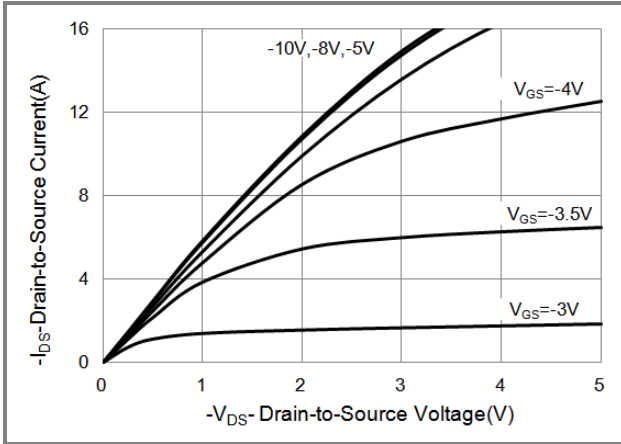


Fig.1 On-Region Characteristics

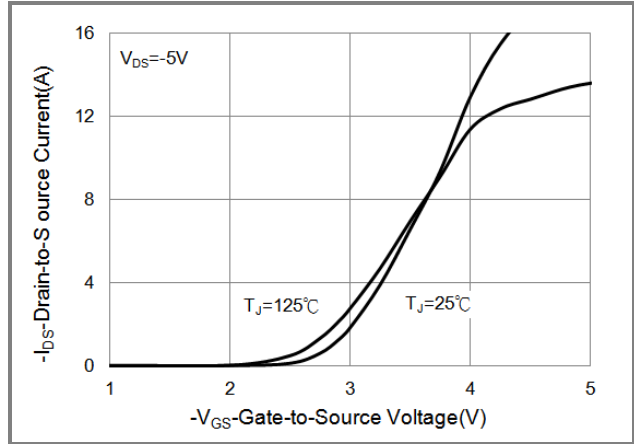


Fig.2 Transfer Characteristics

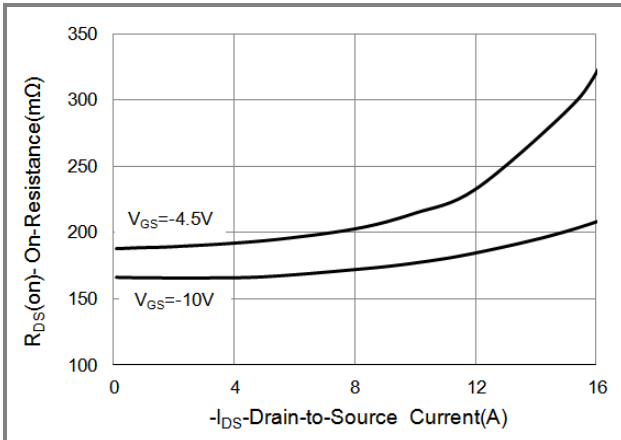


Fig.3 On-Resistance vs. Drain Current

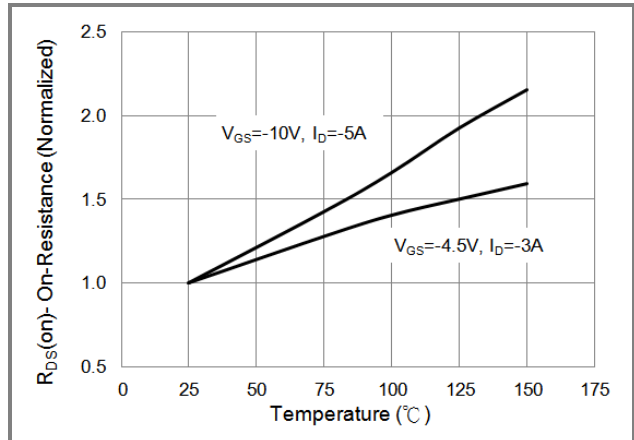


Fig.4 On-Resistance vs. Junction temperature

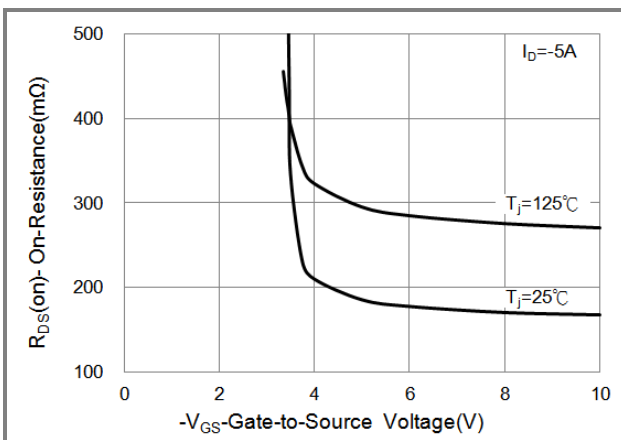


Fig.5 On-Resistance Variation with VGS.

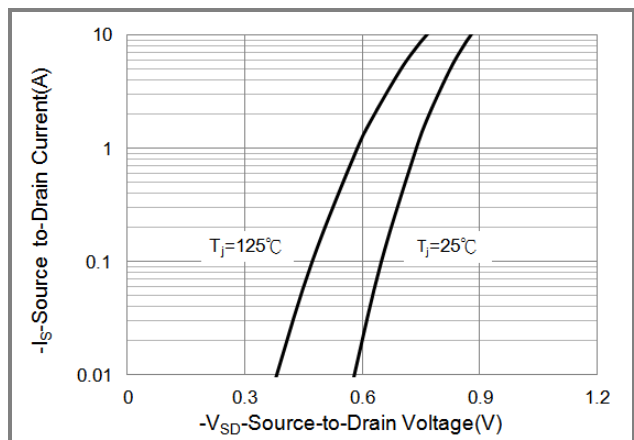


Fig.6 Body Diode Characteristics



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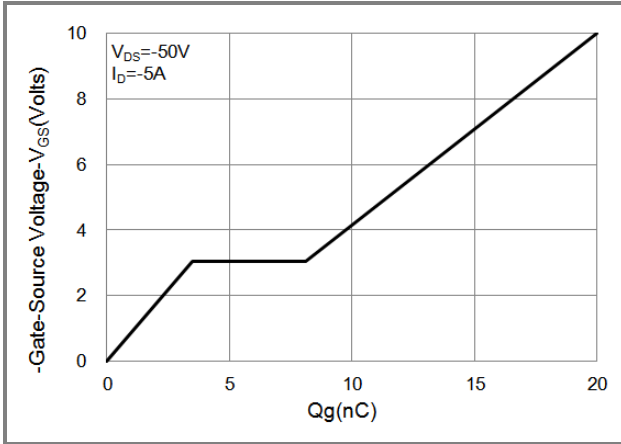


Fig.7 Gate-Charge Characteristics

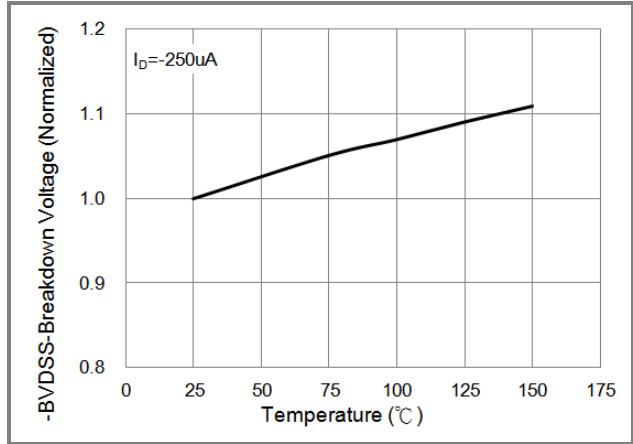


Fig.8 Breakdown Voltage Variation vs. Temperature

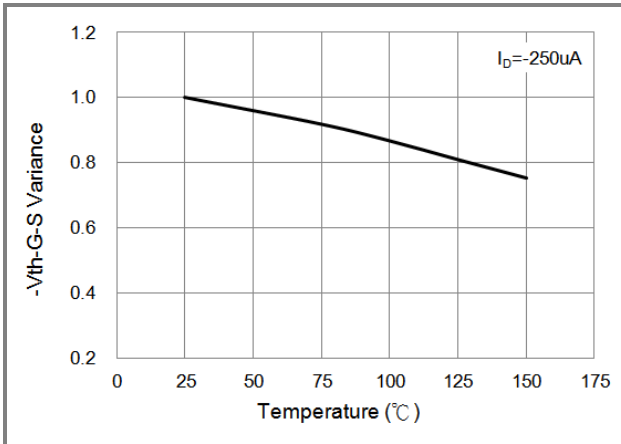


Fig.9 Threshold Voltage Variation with Temperature.

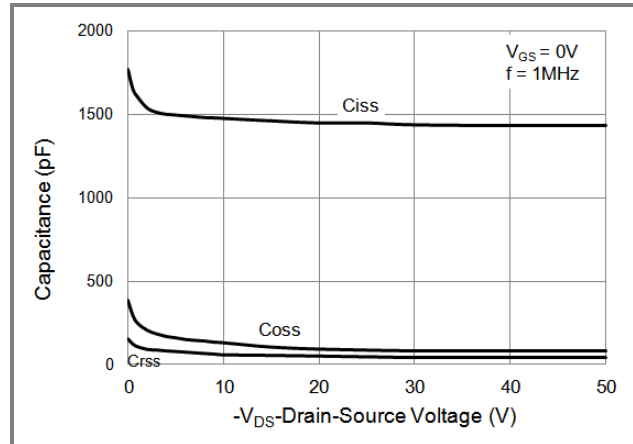


Fig.10 Capacitance vs. Drain-Source Voltage.

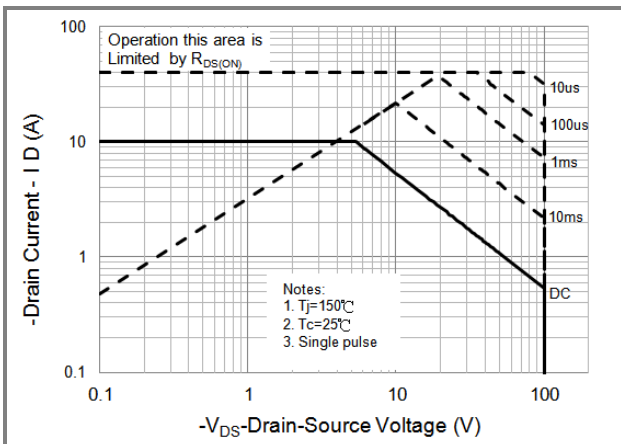


Fig.11 Maximum Safe Operating Area



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TYPICAL CHARACTERISTIC CURVES

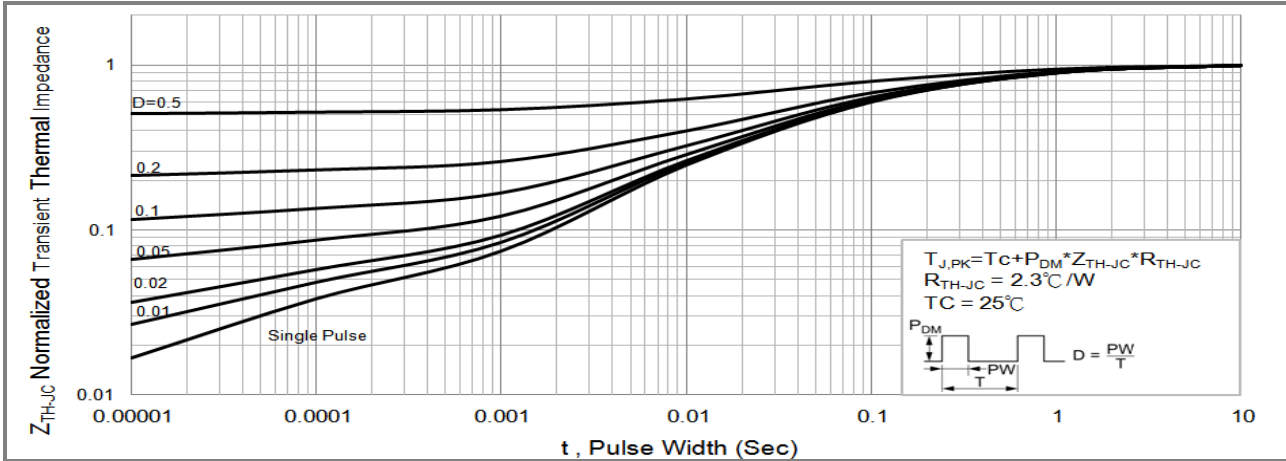
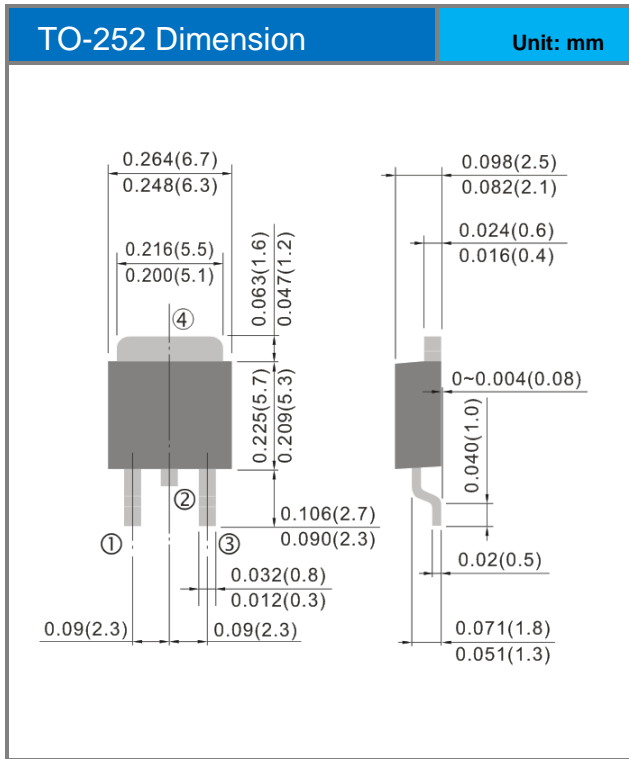


Fig.12 Normalized Thermal Transient Impedance



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Packaging Information



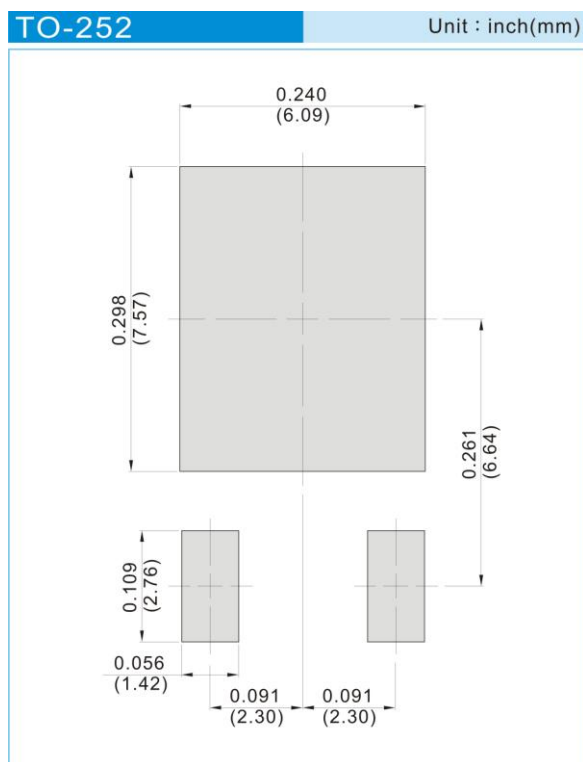


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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJD10P10A_L2_00001	TO-252	3,000pcs / 13" reel	D10P10A	Halogen free

MOUNTING PAD LAYOUT





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