

# PJD60N06

## 60V N-Channel Enhancement Mode MOSFET

**Voltage**

**60 V**

**Current**

**60 A**

### Features

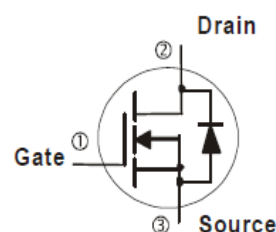
- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@30A<10m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

### Mechanical Data

- Case : TO-252 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0104 ounces, 0.297grams



TO-252



### Maximum Ratings and Thermal Characteristics ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C=25^{\circ}C$	$I_D$	60	A
	$T_C=100^{\circ}C$		38	
Pulsed Drain Current (Note 1)	$T_C=25^{\circ}C$	$I_{DM}$	240	
Power Dissipation	$T_C=25^{\circ}C$	$P_D$	72	W
	$T_C=100^{\circ}C$		29	
Continuous Drain Current	$T_A=25^{\circ}C$	$I_D$	10	A
	$T_A=70^{\circ}C$		8	A
Power Dissipation	$T_A=25^{\circ}C$	$P_D$	2.0	W
Power Dissipation	$T_A=70^{\circ}C$		1.3	
Single Pulse Avalanche Energy (Note 6)		$E_{AS}$	180	mJ
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	$^{\circ}C$
Typical Thermal resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	1.7	$^{\circ}C/W$
	Junction to Ambient	$R_{\theta JA}$	62.5	

- Limited only By Maximum Junction Temperature



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## Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

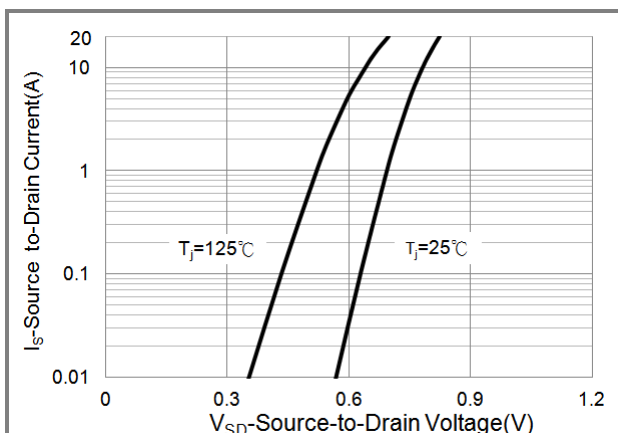
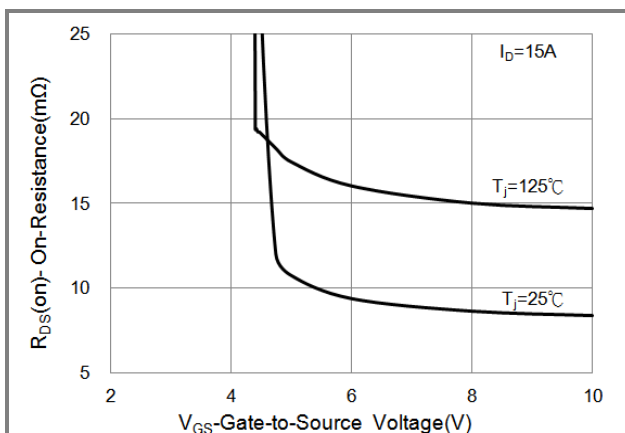
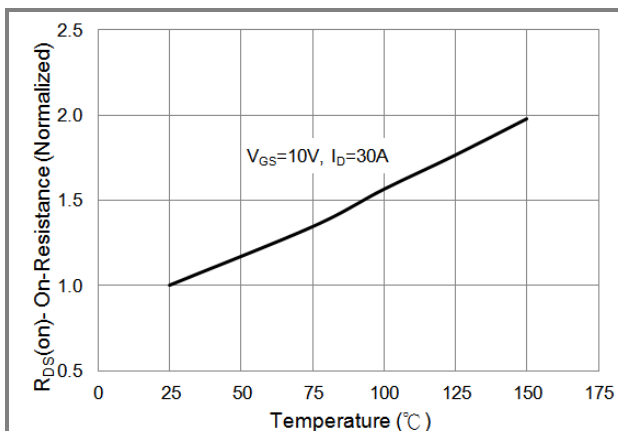
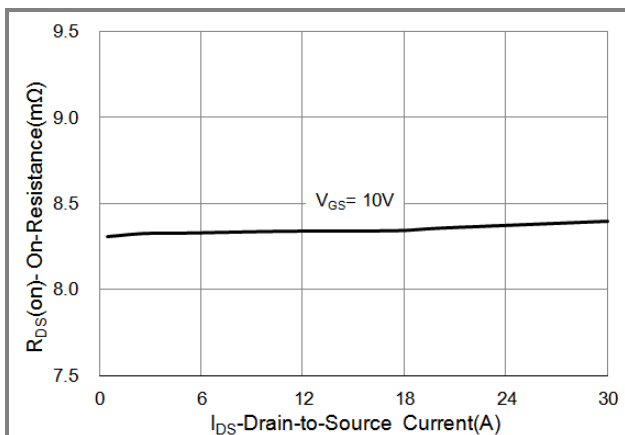
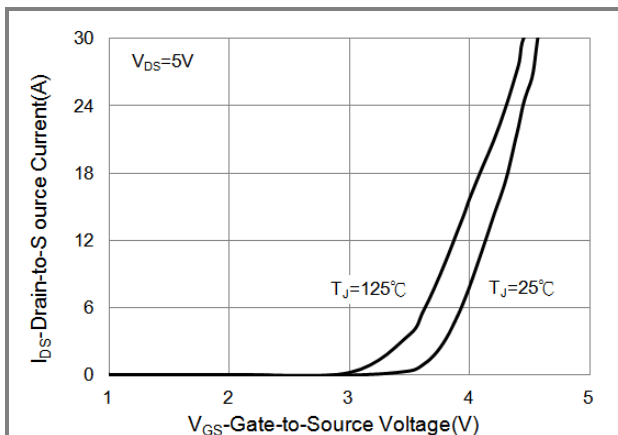
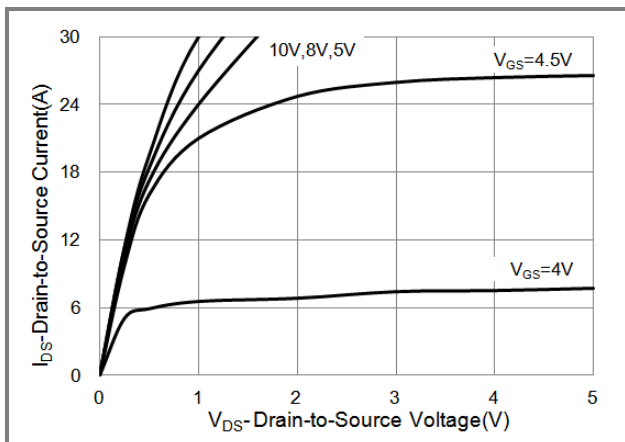
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2.0	2.8	3.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	-	8.4	10	mΩ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	-	-	1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Dynamic (Note 7)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =48V, I <sub>D</sub> =25A, V <sub>GS</sub> =10V (Note 1,2)	-	52	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	11	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	15	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	2904	-	pF
Output Capacitance	C <sub>oss</sub>		-	241	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	112	-	
Turn-On Delay Time	td(on)	V <sub>DD</sub> =30V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω (Note 1,2)	-	18	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	48	-	
Turn-Off Delay Time	td(off)		-	54	-	
Turn-Off Fall Time	t <sub>f</sub>		-	18	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	60	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.7	1.2	V

### NOTES :

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature  $T_J(MAX)=150^{\circ}\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J=25^{\circ}\text{C}$ .
4. The maximum current rating is package limited.
5.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a  $1\text{ inch}^2$  with 2oz.square pad of copper.
6. The test condition is  $L=0.1mH, I_{AS}=60A, V_{DD}=25V, V_{GS}=10V$
7. Guaranteed by design, not subject to production testing.

# PJD60N06

## TYPICAL CHARACTERISTIC CURVES



# PJD60N06

## TYPICAL CHARACTERISTIC CURVES

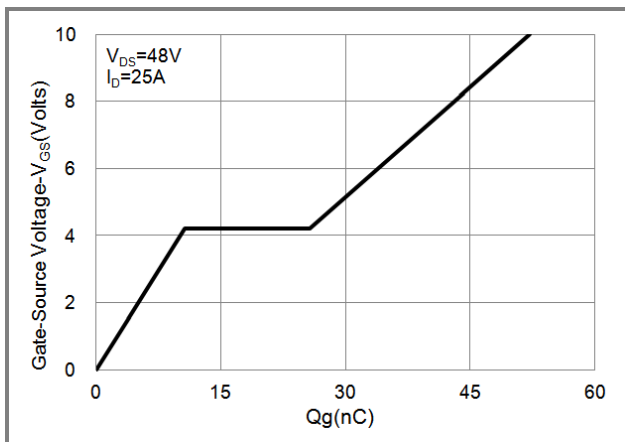


Fig.7 Gate-Charge Characteristics

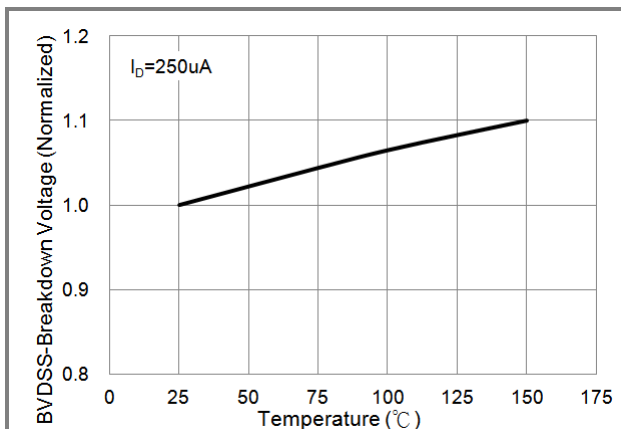


Fig.8 Breakdown Voltage Variation vs. Temperature

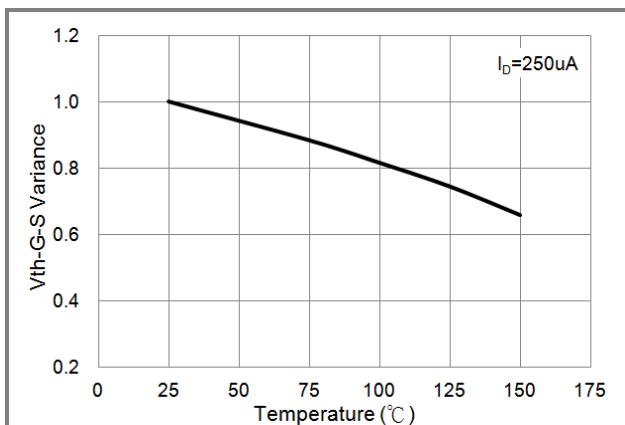


Fig.9 Threshold Voltage Variation with Temperature

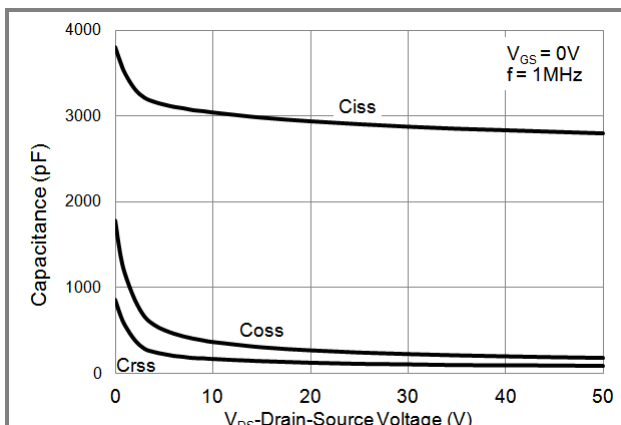


Fig.10 Capacitance vs. Drain-Source Voltage

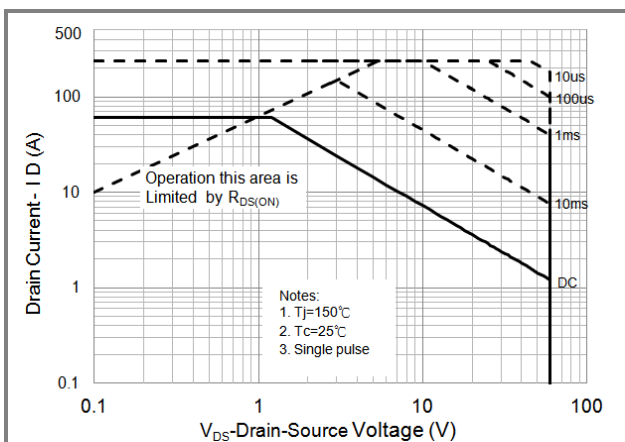


Fig.11 Maximum Safe Operating Area



## PJD60N06

### TYPICAL CHARACTERISTIC CURVES

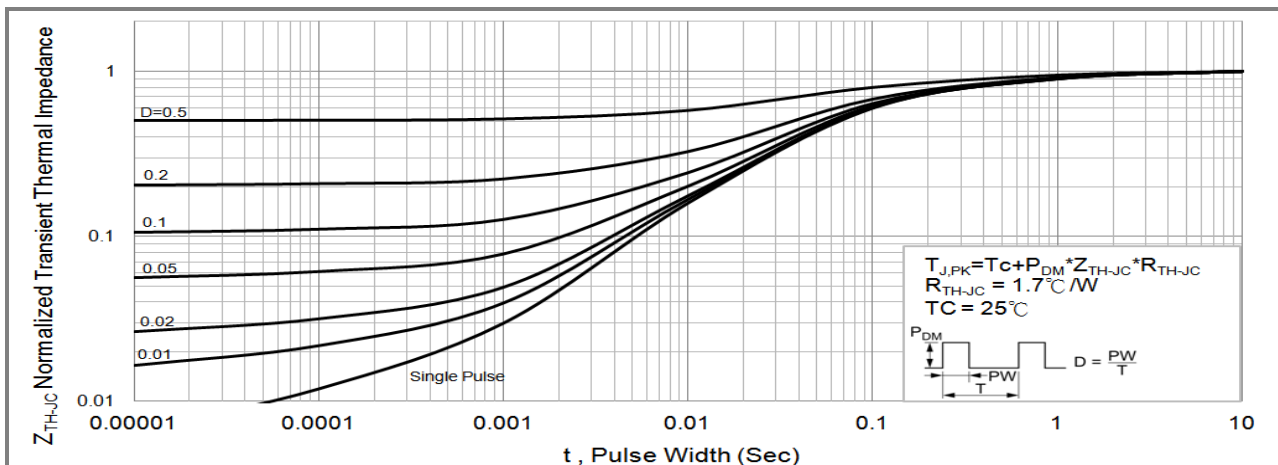
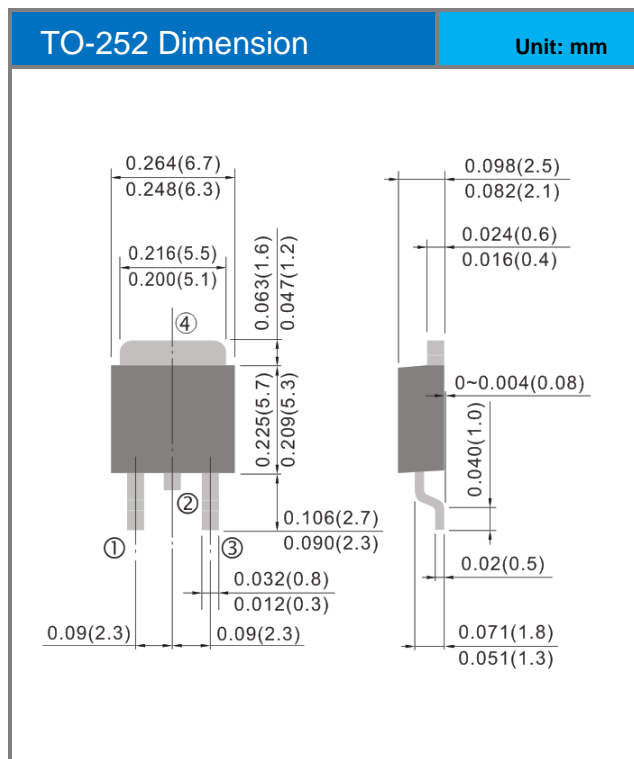


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

# PJD60N06

## Packaging Information



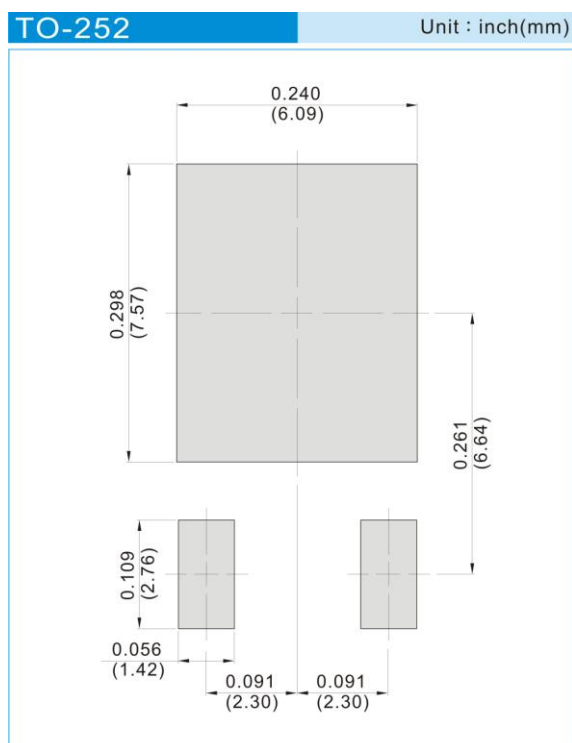


## PJD60N06

### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJD60N06_L2_00001	TO-252	3,000pcs / 13" reel	D60N06	Halogen free

### MOUNTING PAD LAYOUT





## PJD60N06

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