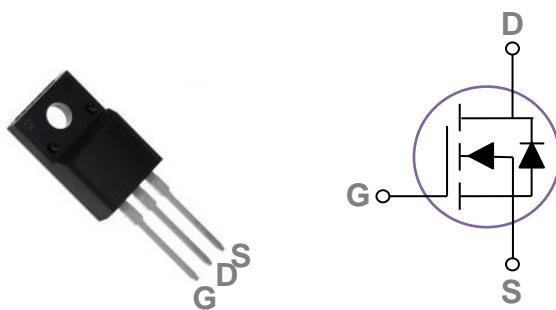


### General Description

These N-Channel enhancement mode power field effect transistors are using Super Junction technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

### TO220F Pin Configuration



BVDSS	RDS(ON)	ID
650V	0.2Ω	20A

### Features

- 20A, 650V,  $RDS(ON) = 0.2\Omega$  @  $VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- High efficient switched mode power supplies
- LED Lighting
- Adapter/charger

### Absolute Maximum Ratings ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	20	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	12.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	80	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	120	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	4.9	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	34	W
	Power Dissipation – Derate above $25^\circ C$	0.27	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	3.67	$^\circ C/W$



650V N-Channel MOSFETs

PJF20N65D

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	650	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =650V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	---	---	1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±30V , V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DSON</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =10A	---	0.16	0.2	Ω
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2	---	4	V

**Dynamic and switching Characteristics**

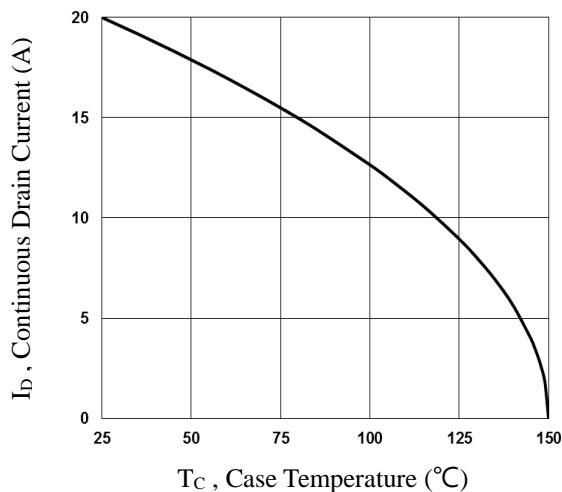
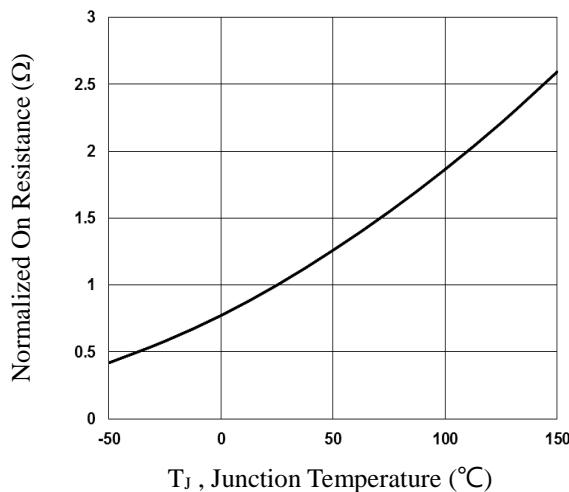
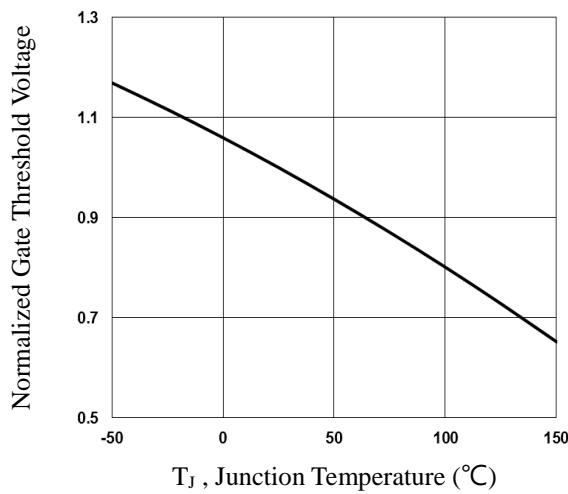
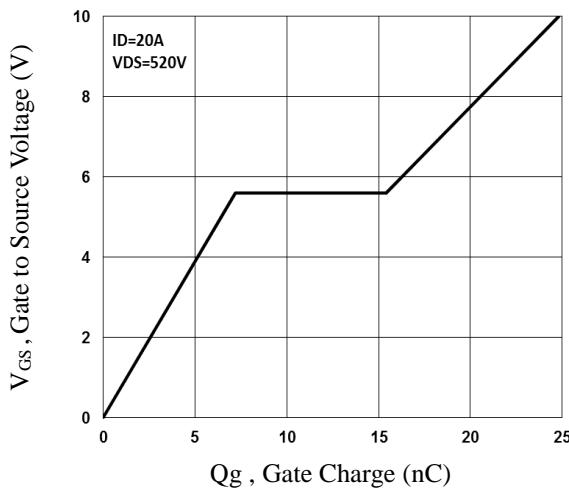
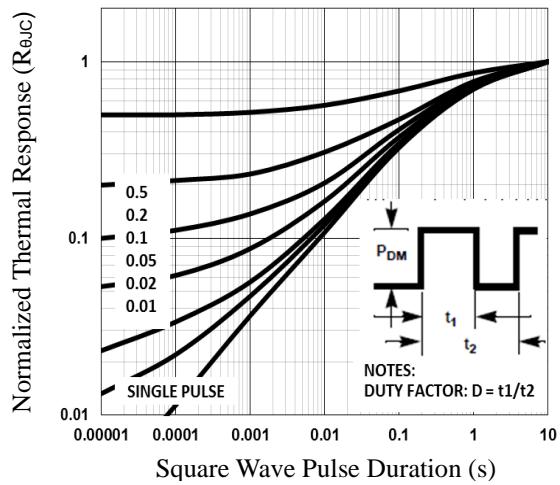
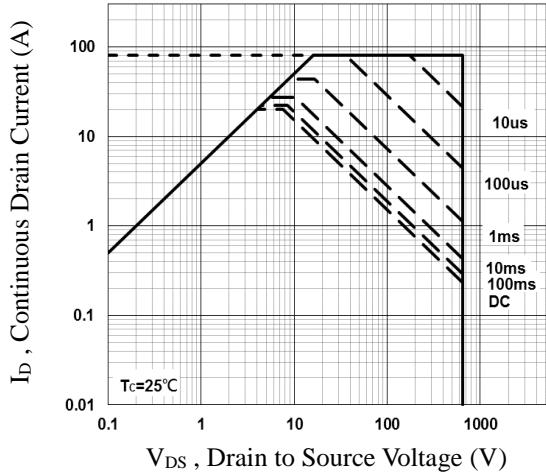
Q <sub>g</sub>	Total Gate Charge <sup>2,3</sup>	V <sub>DS</sub> =520V , V <sub>GS</sub> =10V , I <sub>D</sub> =15A	---	24.8	---	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2,3</sup>		---	7.2	---	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2,3</sup>		---	8.2	---	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>	V <sub>DD</sub> =520V , V <sub>GS</sub> =10V , R <sub>G</sub> =25Ω I <sub>D</sub> =20A	---	40.1	---	ns
T <sub>r</sub>	Rise Time <sup>2,3</sup>		---	49.8	---	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2,3</sup>		---	57.3	---	
T <sub>f</sub>	Fall Time <sup>2,3</sup>		---	63.7	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V , V <sub>GS</sub> =0V , F=1MHz	---	1433	---	pF
C <sub>oss</sub>	Output Capacitance		---	925	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	3.9	---	

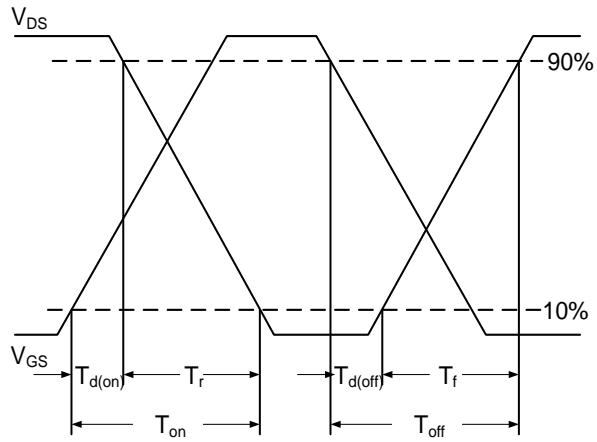
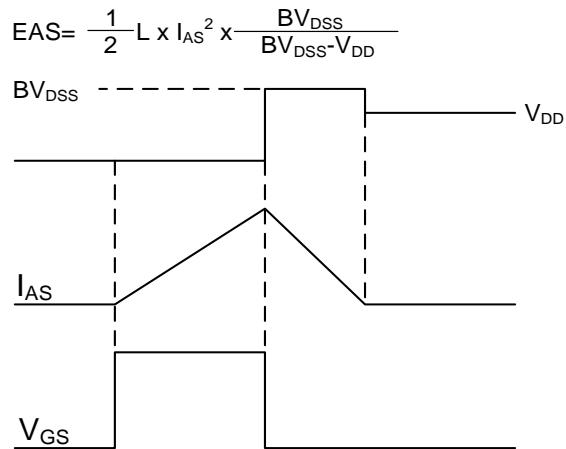
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	20	A
I <sub>SM</sub>	Pulsed Source Current		---	---	40	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =15A , T <sub>J</sub> =25°C	---	---	1.4	V
t <sub>rr</sub>	Reverse Recovery Time <sup>2</sup>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A , dI/dt=100A/μs , T <sub>J</sub> =25°C	---	380	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge <sup>2</sup>		---	5.3	---	uC
I <sub>rrm</sub>	Peak reverse recovery current		---	25.7	---	A

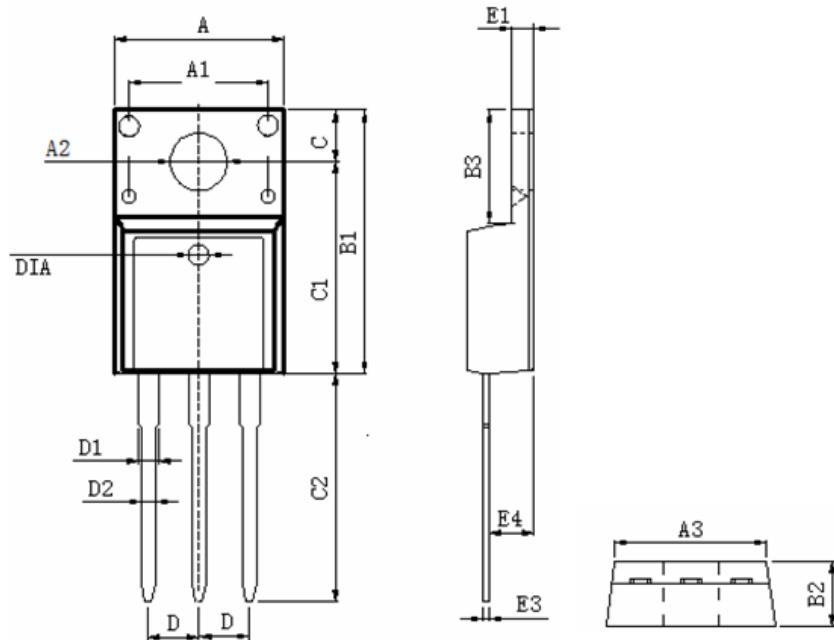
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=50V,V<sub>GS</sub>=10V,L=10mH,I<sub>AS</sub>=4.9A.,R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_C$** 

**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## TO220F PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	<b>10.460</b>	<b>9.860</b>	<b>0.412</b>	<b>0.388</b>
A1	<b>7.100</b>	<b>6.900</b>	<b>0.280</b>	<b>0.272</b>
A2	<b>3.500</b>	<b>3.100</b>	<b>0.138</b>	<b>0.122</b>
A3	<b>9.900</b>	<b>9.500</b>	<b>0.390</b>	<b>0.374</b>
B1	<b>16.170</b>	<b>15.570</b>	<b>0.637</b>	<b>0.613</b>
B2	<b>4.900</b>	<b>4.500</b>	<b>0.193</b>	<b>0.177</b>
B3	<b>6.880</b>	<b>6.480</b>	<b>0.271</b>	<b>0.255</b>
C	<b>3.500</b>	<b>3.100</b>	<b>0.138</b>	<b>0.122</b>
C1	<b>12.870</b>	<b>12.270</b>	<b>0.507</b>	<b>0.483</b>
C2	<b>13.380</b>	<b>12.580</b>	<b>0.527</b>	<b>0.495</b>
D	<b>2.590</b>	<b>2.490</b>	<b>0.102</b>	<b>0.098</b>
D1	<b>1.470</b>	<b>1.070</b>	<b>0.058</b>	<b>0.042</b>
D2	<b>0.900</b>	<b>0.700</b>	<b>0.035</b>	<b>0.028</b>
E1	<b>2.740</b>	<b>2.340</b>	<b>0.108</b>	<b>0.092</b>
E3	<b>0.600</b>	<b>0.400</b>	<b>0.024</b>	<b>0.016</b>
E4	<b>2.960</b>	<b>2.560</b>	<b>0.117</b>	<b>0.101</b>
DIA	<b>Φ1.5 TYP.</b>	<b>deep0.1 TYP.</b>	<b>Φ0.059 TYP.</b>	<b>deep0.004 TYP.</b>