



# PJF2N70 / PJU2N70

## 700V N-Channel Enhancement Mode MOSFET

ITO-220AB/TO-251

### FEATURES

- 700V,  $R_{DS(ON)}=5.5\Omega@V_{GS}=10V, I_D=2A$
- Low ON Resistance
- Fast Switching
- Low Gate Charge
- Fully Characterized Avalanche Voltage and Current
- Specially Designed for AC Adapter, Battery Charge and SMPS
- In compliance with EU RoHs 2002/95/EC Directives

### MECHANICAL DATA

- Case: TO-220AB / TO-251 Molded Plastic
- Terminals : Solderable per MIL-STD-750, Method 2026

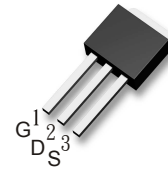
### ORDERING INFORMATION

TYPE	MARKING	PACKAGE	PACKING
PJF2N70	F2N70	ITO-220AB	50PCS/TUBE
PJU2N70	U2N70	TO-251	80PCS/TUBE

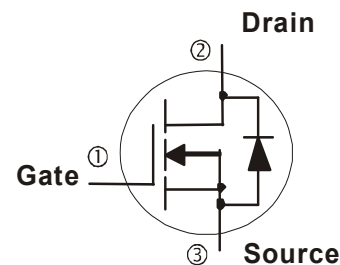
ITO-220AB



TO-251



### INTERNAL SCHEMATIC DIAGRAM



### Maximum RATINGS and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted )

PARAMETER	Symbol	PJF2N70	PJU2N70	Units
Drain-Source Voltage	$V_{DS}$	700		V
Gate-Source Voltage	$V_{GS}$	$\pm 30$		V
Continuous Drain Current	$I_D$	2	2	A
Pulsed Drain Current <sup>1)</sup>	$I_{DM}$	8	8	A
Maximum Power Dissipation Derating Factor	$P_D$	20 0.16	31 0.25	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150		$^\circ\text{C}$
Avalanche Energy with Single Pulse $I_{AS}=2A, V_{DD}=50V, L=45mH$	$E_{AS}$	140		mJ
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	6.25	4	$^\circ\text{C/W}$
Junction-to Ambient Thermal Resistance	$R_{\theta JA}$	62.5	100	$^\circ\text{C/W}$

**Note** : 1) Maximum DC current limited by the package

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## ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Units
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	700	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	-	4.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=1A$	-	5.5	6.5	$\Omega$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=700V, V_{GS}=0V$	-	-	10	$\mu A$
Gate Body Leakage	$I_{GSS}$	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=560V, I_D=2A$ $V_{GS}=10V$	-	10.8	-	nC
Gate-Source Charge	$Q_{gs}$		-	2.1	-	
Gate-Drain Charge	$Q_{gd}$		-	4.5	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=350V, I_D=2A$ $V_{GS}=10V, R_G=25\Omega$	-	11.2	18	ns
Turn-On Rise Time	$t_r$		-	10.8	16	
Turn-Off Delay Time	$t_{d(off)}$		-	22.4	31	
Turn-Off Fall Time	$t_f$		-	16.8	24	
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V$ $f=1.0MHz$	-	338	395	pF
Output Capacitance	$C_{oss}$		-	28.6	65	
Reverse Transfer Capacitance	$C_{rss}$		-	2.4	3.6	
<b>Source-Drain Diode</b>						
Max. Diode Forward Current	$I_S$	-	-	-	2.0	A
Max.Pulsed Source Current	$I_{SM}$	-	-	-	8.0	A
Diode Forward Voltage	$V_{SD}$	$I_S=2A, V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time	$t_{rr}$	$V_{GS}=0V, I_F=2A$ $di/dt=100A/\mu s$	-	260	-	ns
Reverse Recovery Charge	$Q_{rr}$		-	1.09	-	$\mu C$

**NOTE** : Plus Test: Pluse Width < 300us, Duty Cycle < 2%.



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Typical Characteristics Curves (  $T_a=25^\circ\text{C}$ , unless otherwise noted)

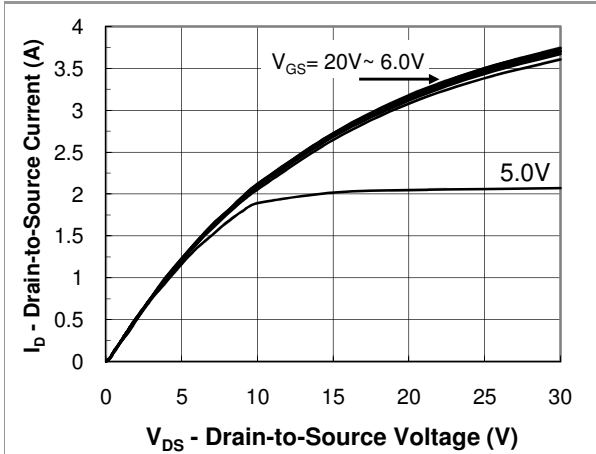


Fig.1 Output Characteristic

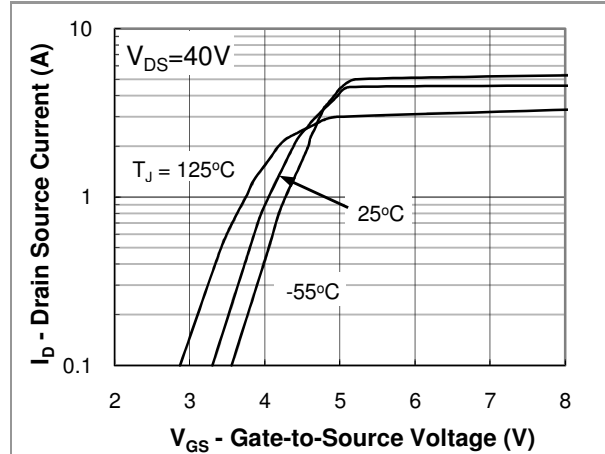


Fig.2 Transfer Characteristic

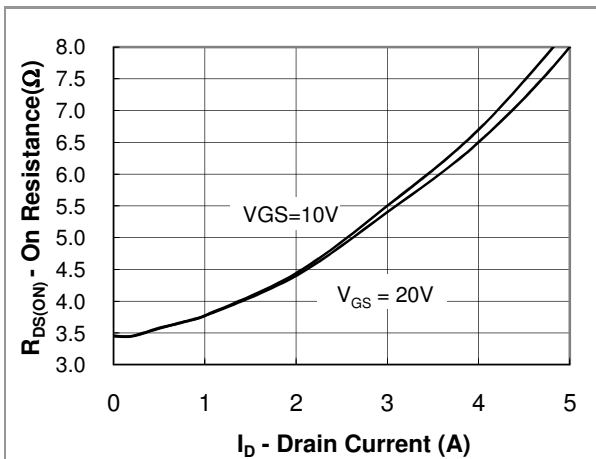


Fig.3 On Resistance vs Drain Current

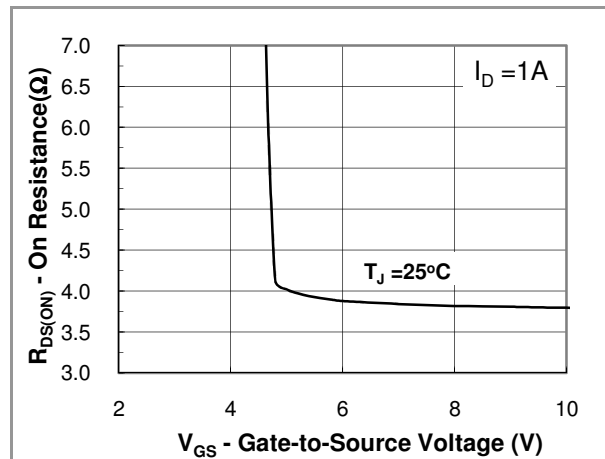


Fig.4 On Resistance vs Gate to Source Voltage

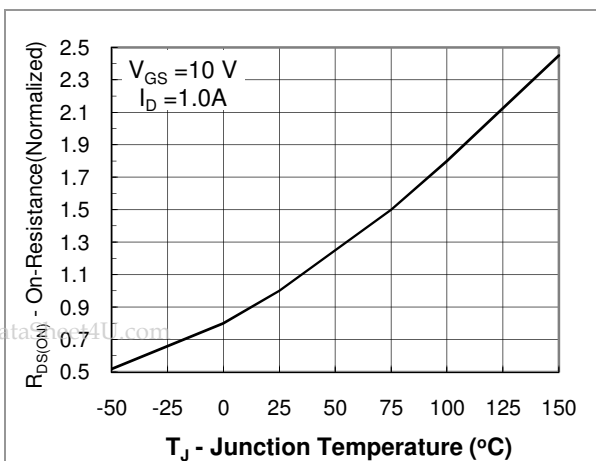


Fig.5 On Resistance vs Junction Temperature

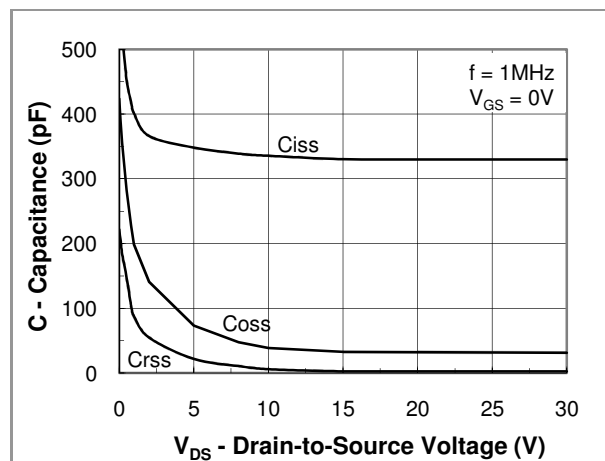


Fig.6 Capacitance



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Typical Characteristics Curves ( Ta=25°C, unless otherwise noted)

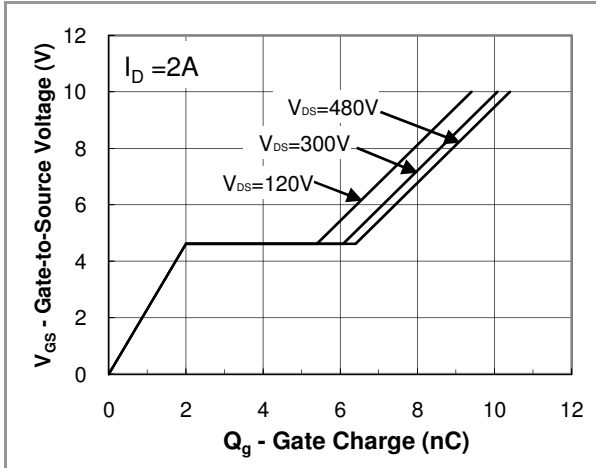


Fig. 7 Gate Charge Waveform

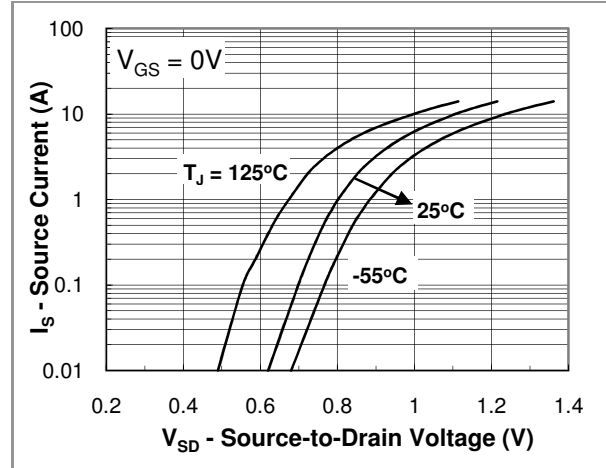


Fig.8 Source-Drain Diode Forward Voltage

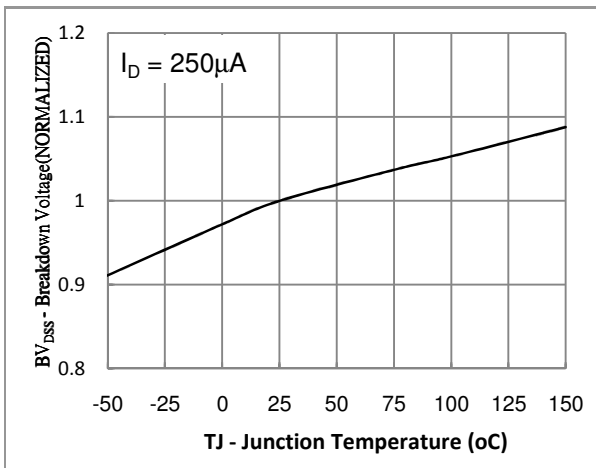


Fig.9 Breakdown Voltage vs Junction Temperature



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### LEGAL STATEMENT

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# HALOGEN FREE PRODUCT DECLARATION

(Use green molding compound:ELER-8)

1. Pan Jit can produce halogen free product use molding compound for packing from Mar.2008 that contain Br<700 ppm,Cl<700ppm, Br+Cl<1000ppm,Sb<sub>2</sub>O<sub>3</sub><100ppm.
2. If your company need halogen free product shall be note requirement green compound material on order for the halogen free product request.